

	Legend:	not applicable		not implemented		implemented	
Description	Accumulator No Operand	Accumulator Immediate	Accumulator Abs Address	Accumulator Rel Address	Byte at Abs Address	Word at Abs Address	Stack at SP + Offset
Load A from		LDI	LDA	LDR			LDS
Store A to			STA	STR			STS
Clear					CLB	CLW	
Negate	NEG				NEB	NEW	
Increment	INC				INB	INW	
Decrement	DEC				DEB	DEW	
Add		ADI	ADA	ADR	ADB	ADW	
Subtract		SBI	SBA	SBR	SBB	SBW	
Compare		CPI	CPA	CPR			
Add with Carry In		ACI	ACA	ACR	ACB	ACW	
Subtract with Carry In		SCI	SCA	SCR	SCB	SCW	
Jump to			JPA	JPR			
Jump to Subroutine			JPS				
Return from Subroutine	RTS						
No Operation (16 clocks)	NOP						
Terminal Input	INP						
Set FLASH bank*	BNK						
Output A to Terminal	OUT						
Clear Carry In Flag	CLC						
Set Carry In Flag	SEC		Description	Accumulator No Operand	Accumulator Immediate	Accumulator Abs Address	
Logical Shift Left (= ASL)	LSL						
Rotate Shift Left	ROL		Branch on Non-Zero			BNE	
Logical Shift Right	LSR		Branch on Zero			BEQ	
Rotate Shift Right	ROR		Branch on Carry Clear			BCC	
Arithmetic Shift Right	ASR		Branch on Carry Set			BCS	
Push on Stack	PHS		Branch on Plus			BPL	
Pull from Stack	PLS		Branch on Minus			BMI	

\* has no effect on version 1.3 (32KB FLASH Edition)