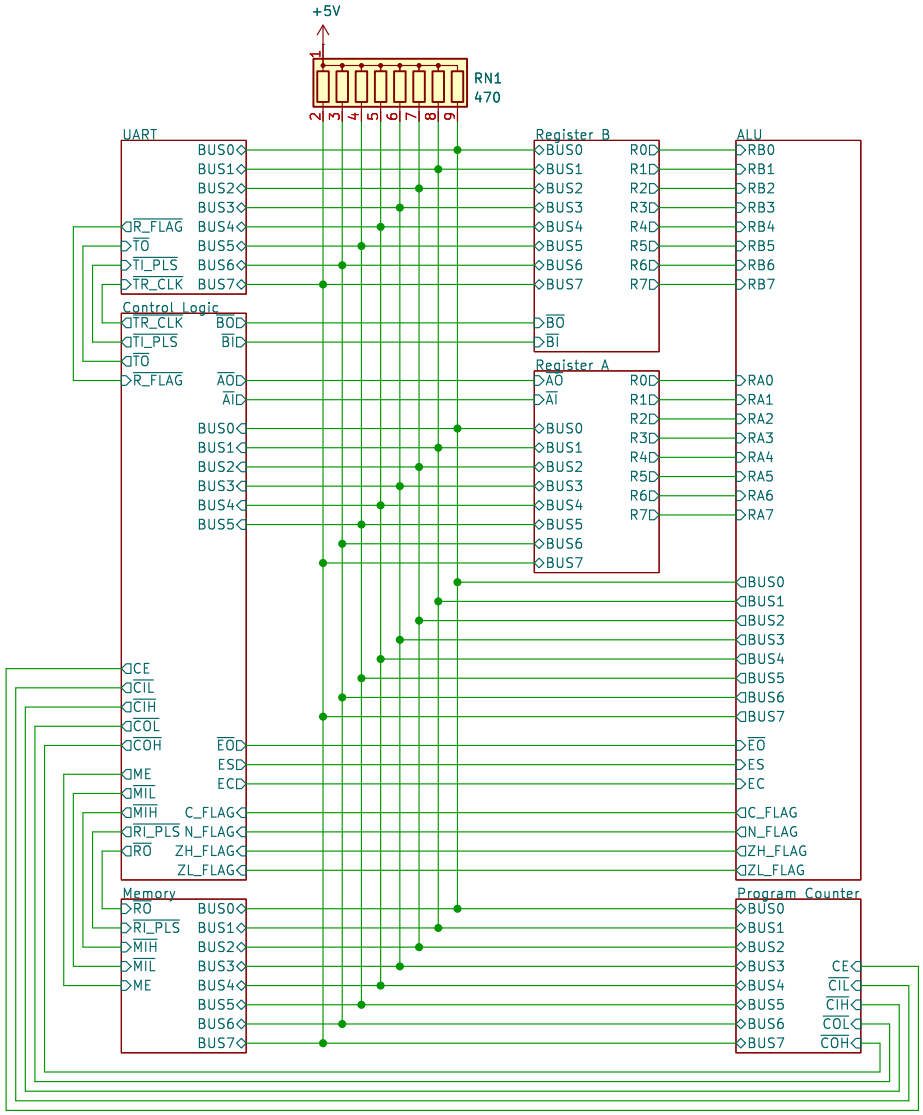

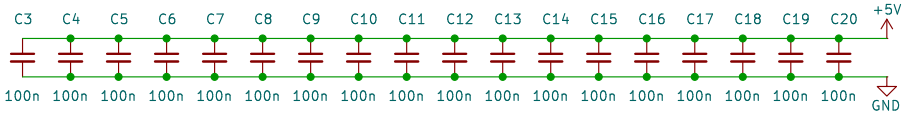

M2 H4 M2 H3 M2 H2 M2 H1

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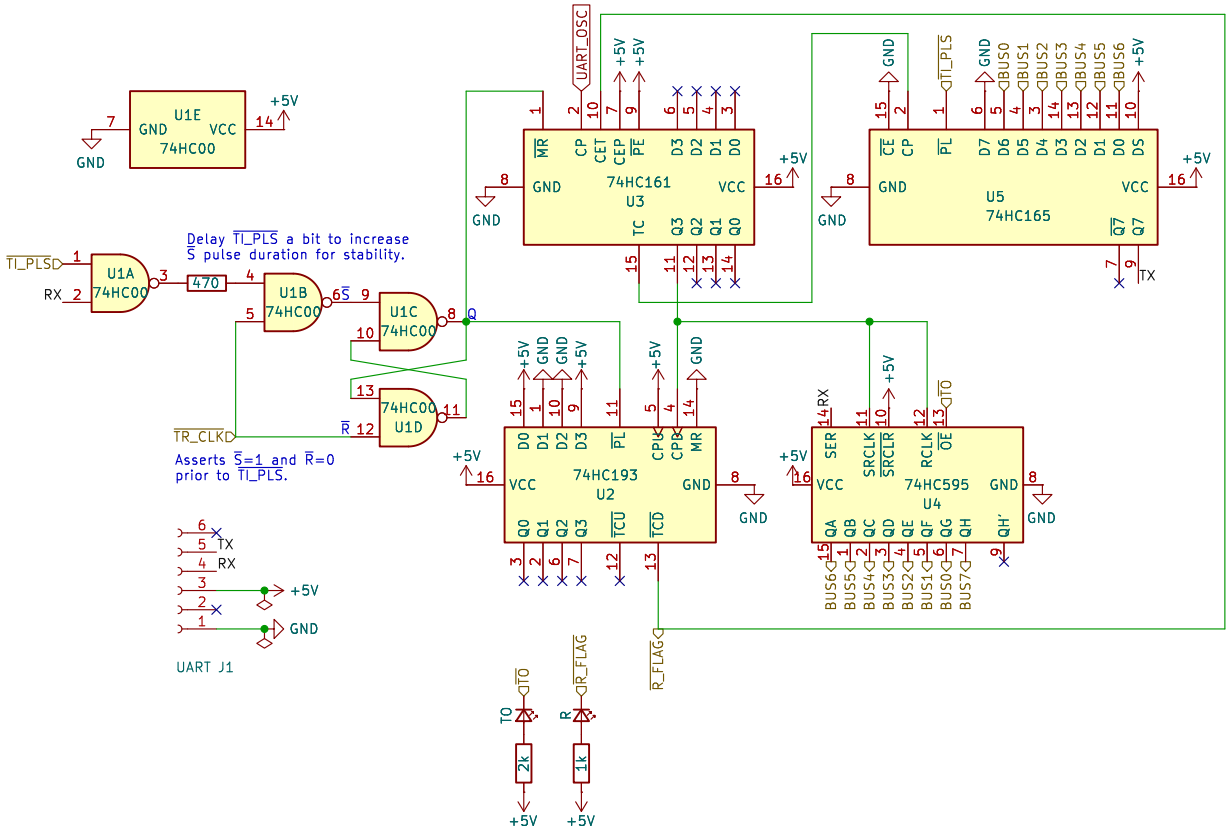
Sheet: /
File: 8-Bit CPU 32k.kicad_sch

Title: Minimal UART Computer 3

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 1/8



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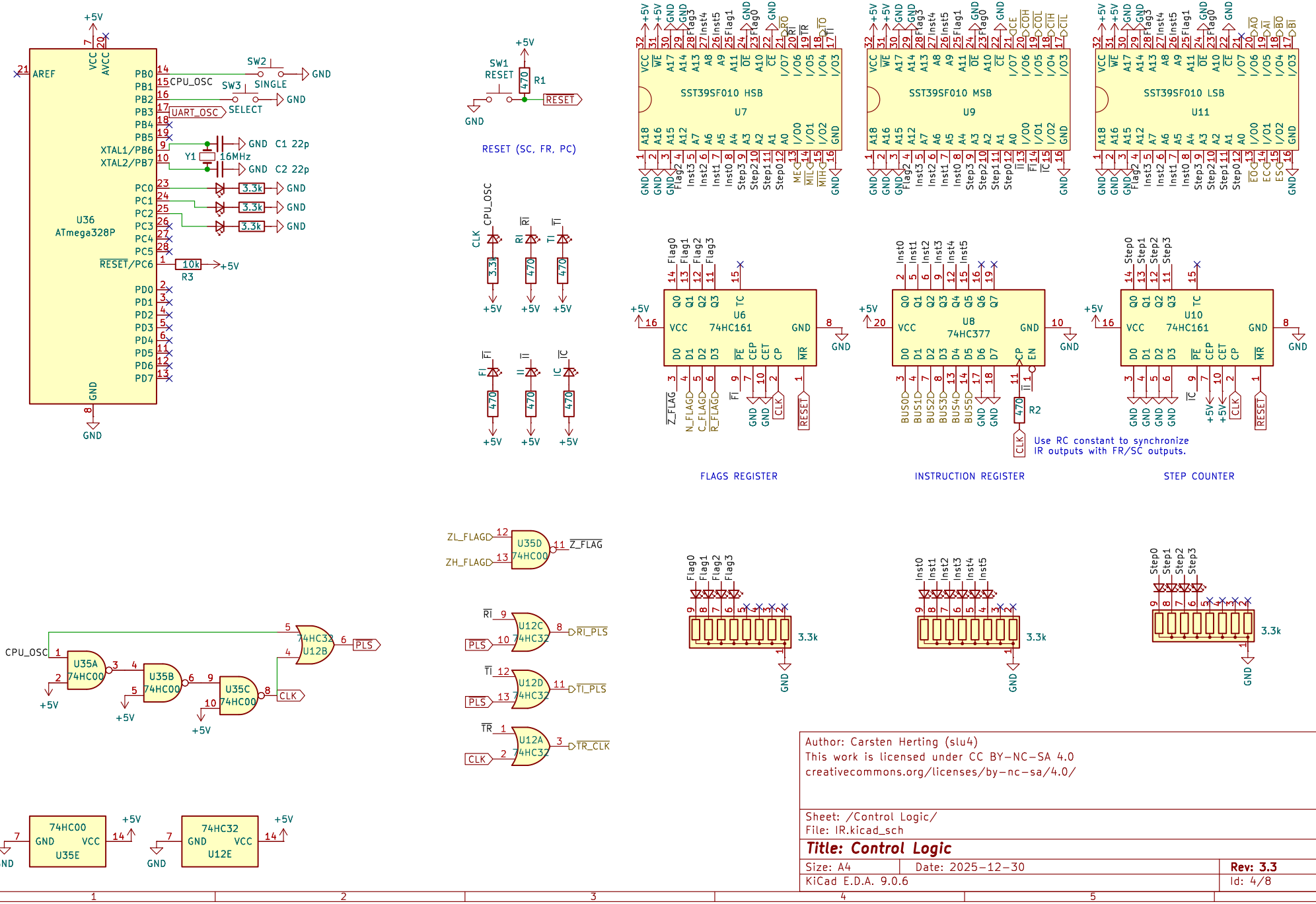
Sheet: /UART/
File: UART_RX.kicad_sch

Title: UART

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 2/8



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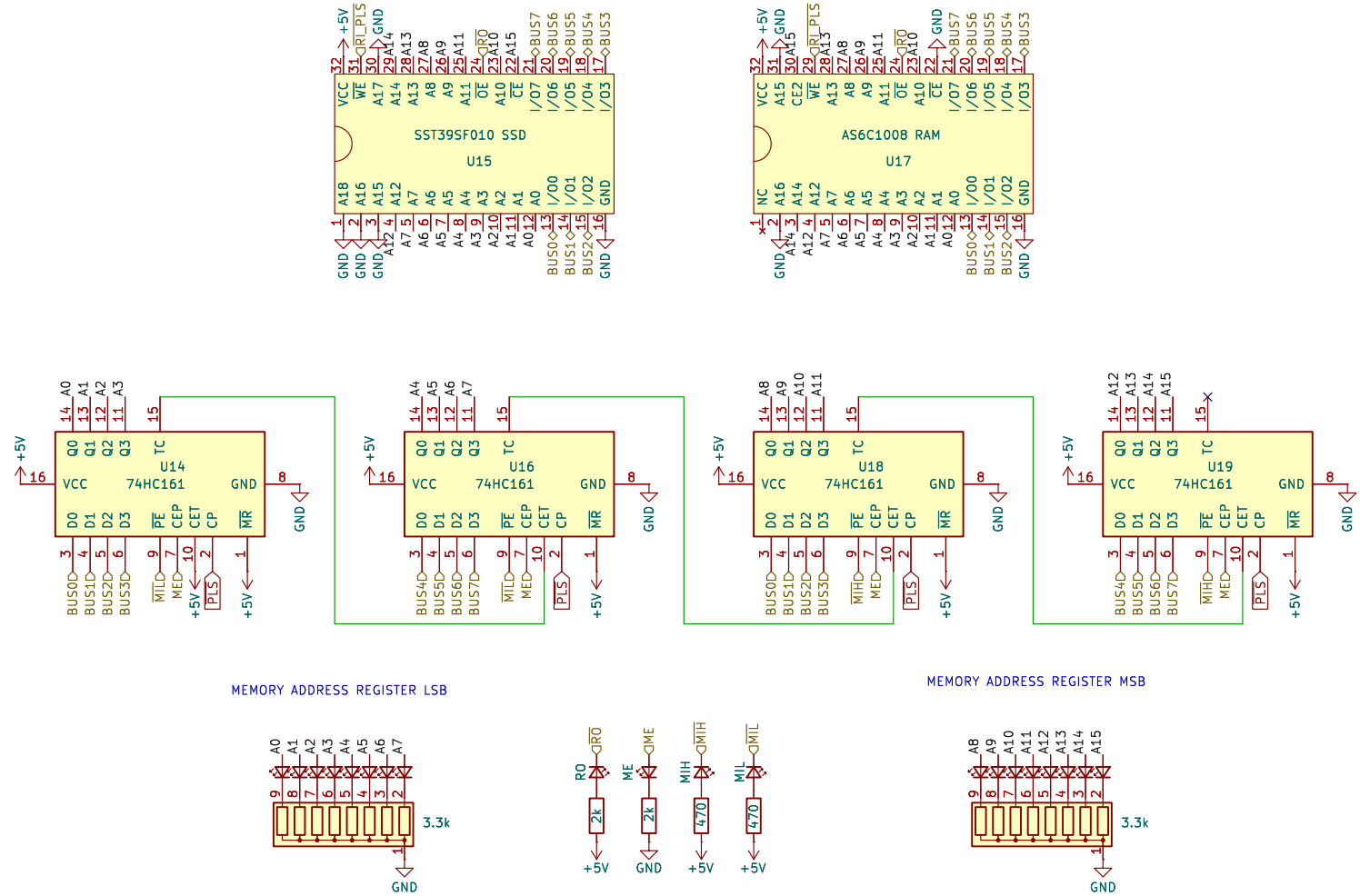
Sheet: /Control Logic/
File: IR.kicad_sch

Title: Control Logic

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 4/8



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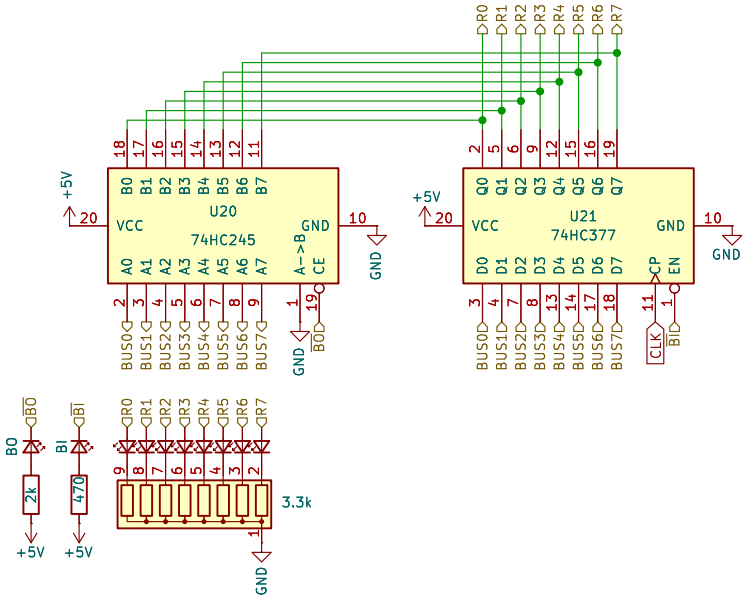
Sheet: /Memory/
File: RAM.kicad_sch

Title: RAM, ROM and MAR

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 5/8



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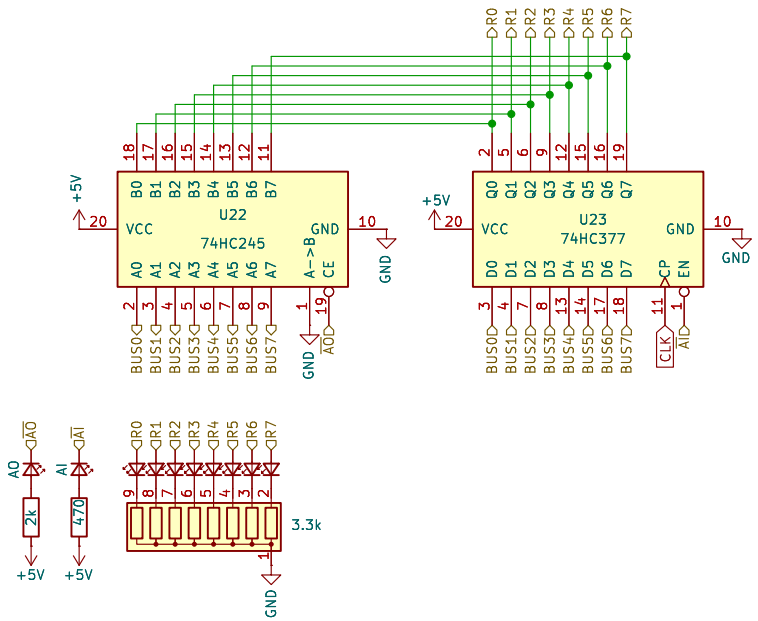
Sheet: /Register B/
File: RegB.kicad_sch

Title: B Register

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 7/8



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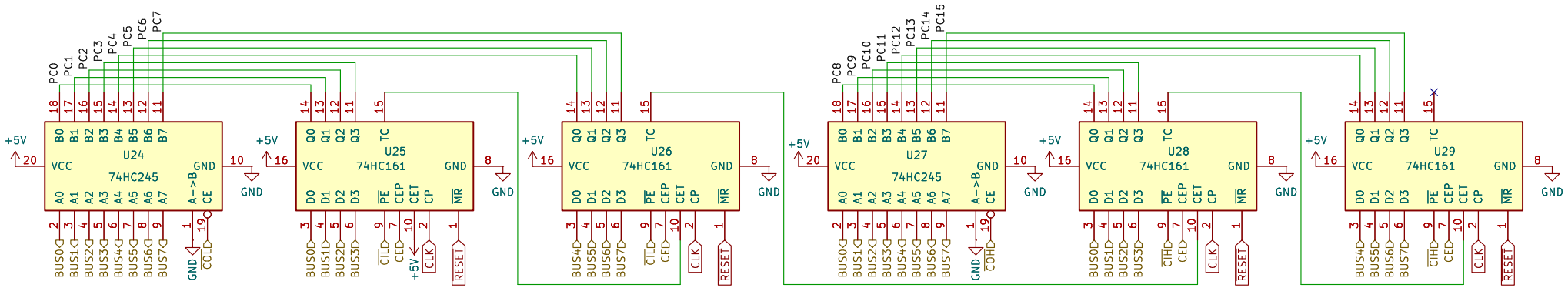
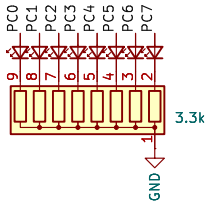
Sheet: /Register A/
File: RegA.kicad_sch

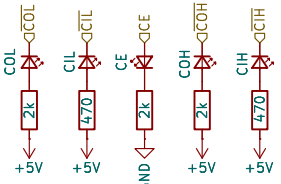
Title: A Register

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 8/8

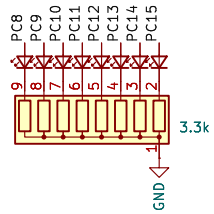


PROGRAM COUNTER LSB





PROGRAM COUNTER MSB



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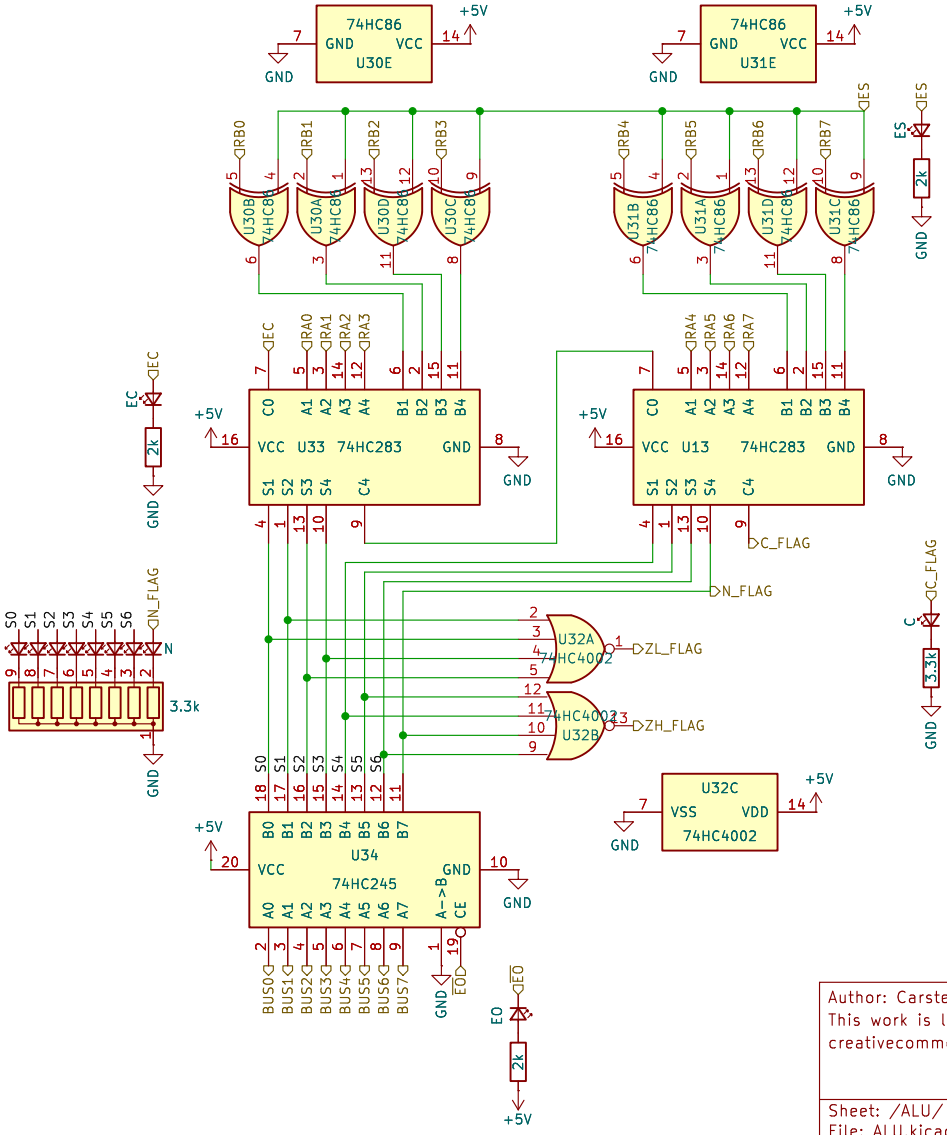
Sheet: /Program Counter/
File: PC.kicad_sch

Title: Program Counter

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 9/8



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Sheet: /ALU/
File: ALU.kicad_sch

Title: Arithmetic Unit

Size: A4	Date: 2025-12-30	**Rev: 3.3**
KiCad E.D.A. 9.0.6		Id: 10/8