# **Scott Madeux**

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# Education

# **Bachelor of Science in Computer Engineering**

Brigham Young University - Idaho

Rexburg, ID

**Expected Graduation: April 2020** 

- GPA: 3.72
- Relevant Coursework: VLSI Design, Advanced Digital Systems, Electronic Devices and Circuits, Embedded Systems, Computer Architecture, Object Oriented Programming, Data Structures

# **Projects**

### 8-bit Register File Design

60 Hours

VLSI Design Project

Rexburg, ID

- In a team of 3, designed a CMOS 8-bit register files using Glade (similar to Cadence) and LTspice
- Implemented design rules for spacing and size of substrates, contacts, interconnects, etc.
- Acquired techniques for efficient layout and debugging of CMOS circuits

**Motorized Camera Slider** 

80 Hours

Embedded Systems Project

Rexburg, ID

- Built a camera slider run by a stepper motor and controlled by a Raspberry Pi
- Wrote a python script to control the camera slider using the parameters given by the user

#### FPGA Matrix Multiplier

20 Hours

Digital Systems Project

Rexburg, ID

- Created a module in Verilog that was able to multiply matrices in parallel
- Showed how FPGAs can be used to make certain applications such as matrix multiplication much faster

# Skills

• C++

- CMOS Circuits
- VLSI Design
- Glade (Cadence)

Python

- FPGA Design
- Verilog

• Linux

# **Work Experience**

# **Computer Science Tutor**

January 2017 - December 2018

Brigham Young University - Idaho

Rexburg, ID

- Demonstrated ability to teach difficult concepts in a manner that is easy to understand
- Tutored students for over 500 hours in computer science and electrical engineering

#### **Tour Guide**

May - September 2017 & 2018

Holland America Princess

Skagway, AK

- Ensured the safety and entertainment of 40-45 passengers during 2-day tours of Alaska and the Yukon
- Hired onto the company's highway program and gave premium tours to high paying guests (Uncommon for a first-year driver)