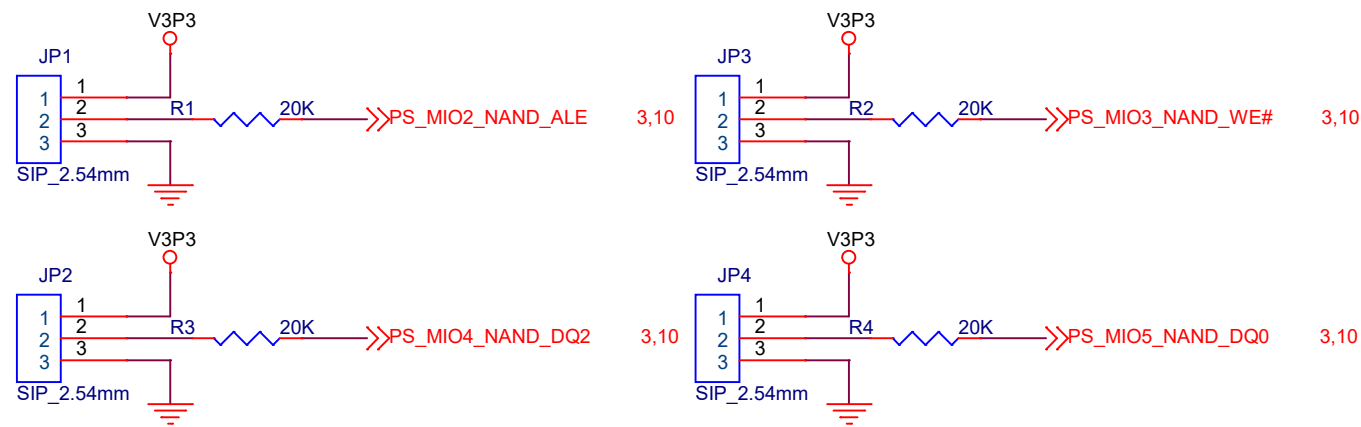


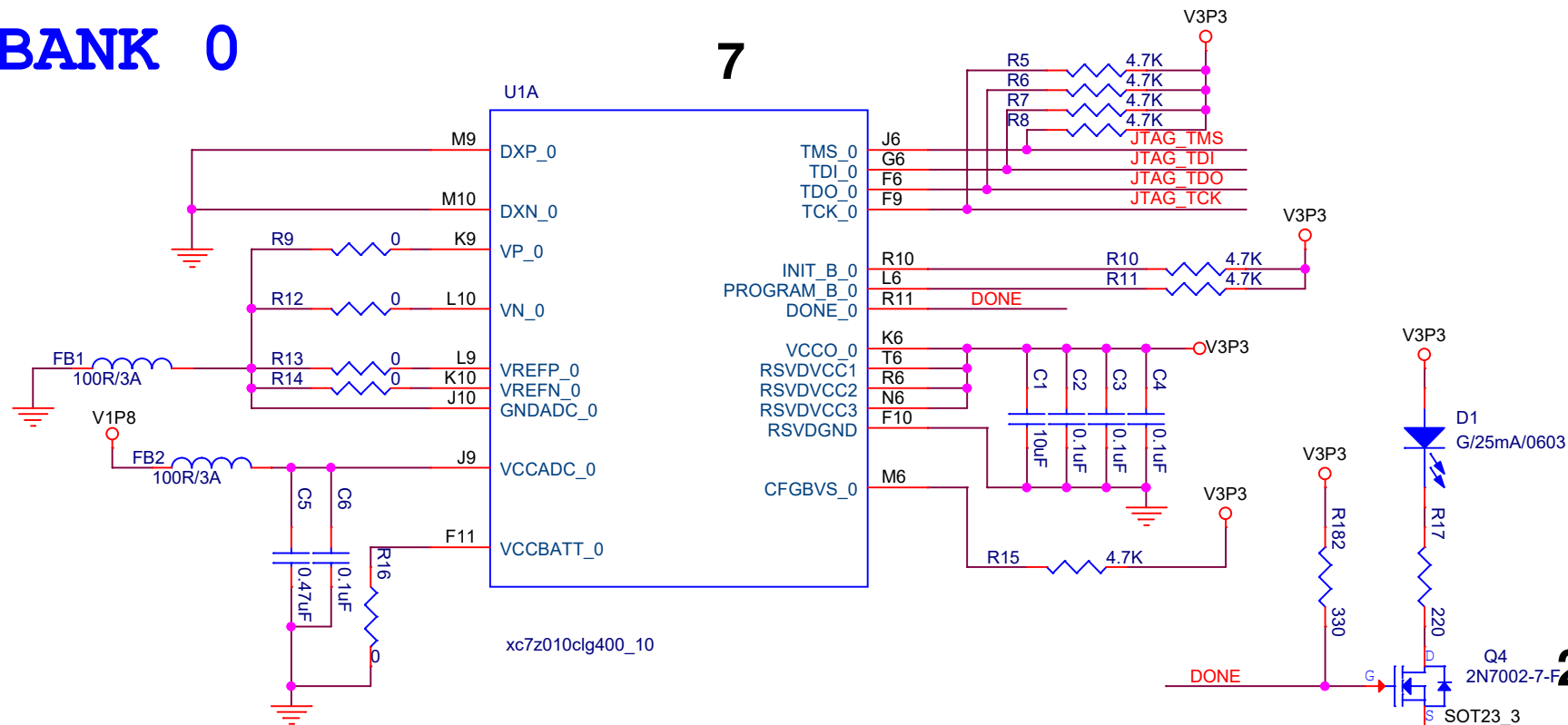
Boot Mode Select



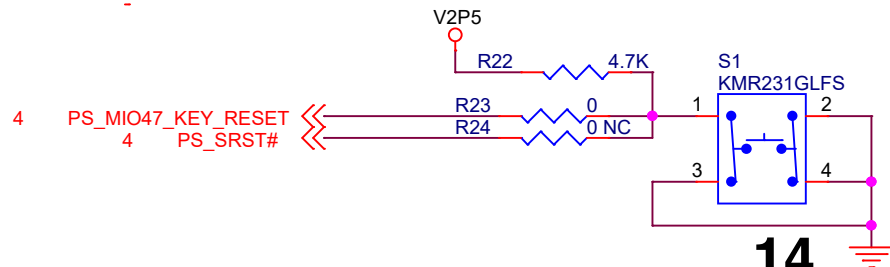
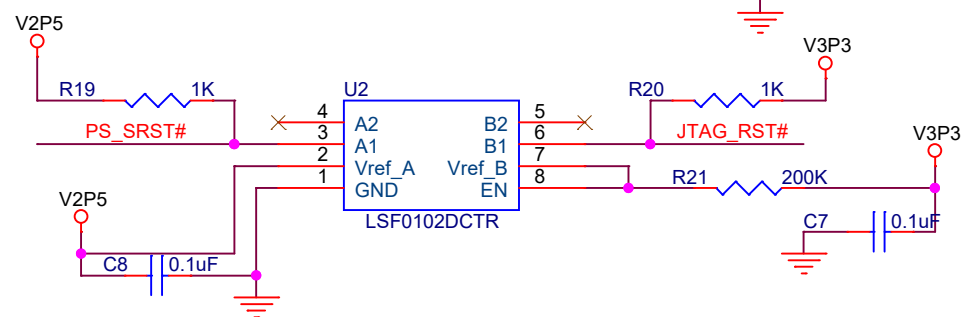
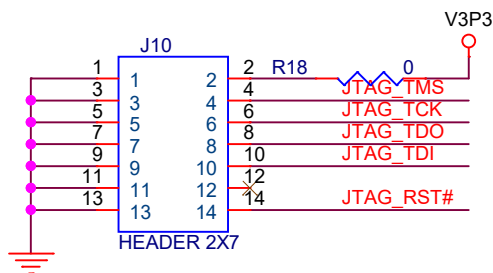
Boot Mode	JP1	JP2	JP3	JP4
Cascade JTAG	0	0	0	0
TF Card	0	1	0	1
QSPI Flash	0	0	0	1
NOR Flash	0	0	1	0
NAND Flash	0	1	0	0

BANK 0

7



20

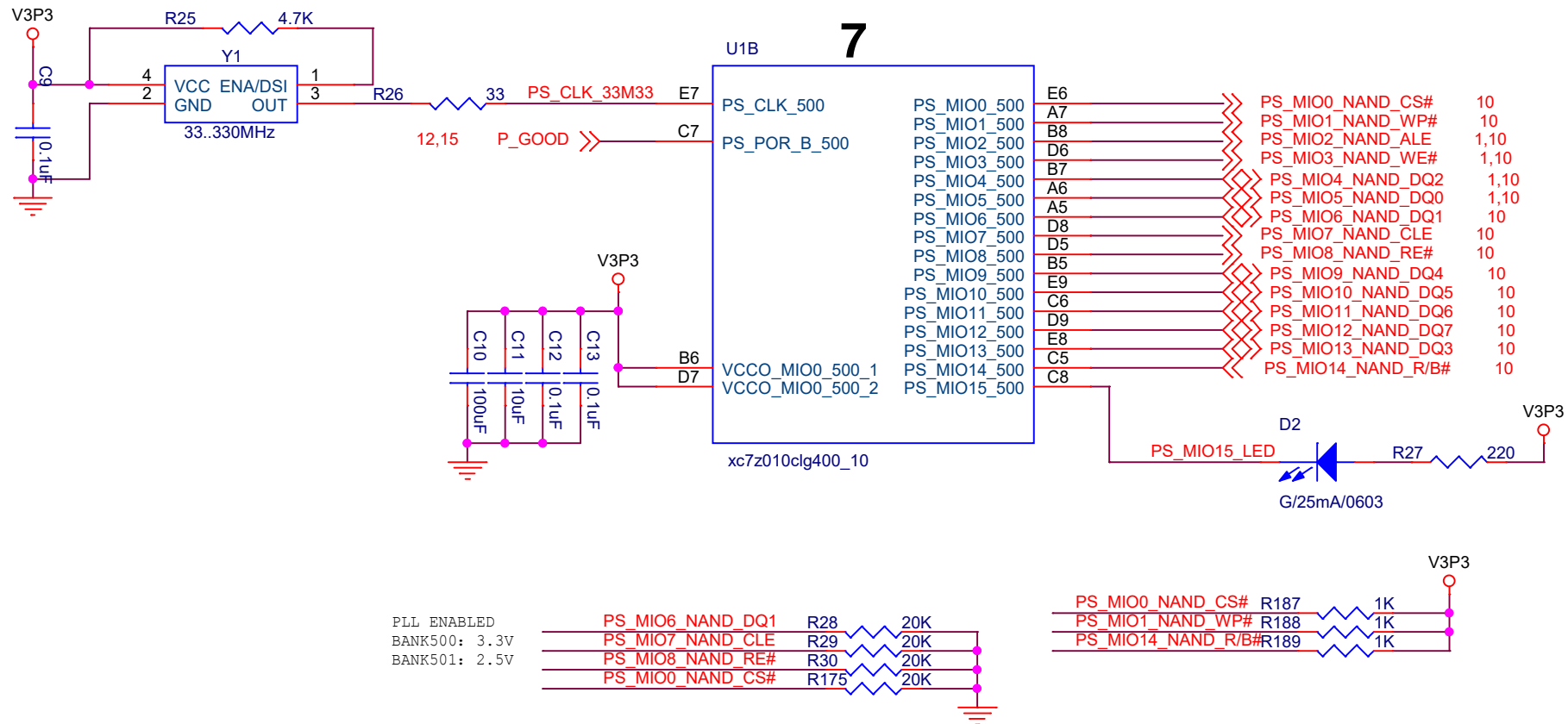


14

Title		
AntMiner_ControlBoard_XC7010		
Size	Document Number	Rev
A	<Doc>	V1.0
Date:	Friday, July 01, 2016	Sheet 2 of 15

BANK 500

7



PLL ENABLED
BANK500: 3.3V
BANK501: 2.5V

During an SD memory card boot sequence, the BootROM reads the logic level on the MIO pin 0 as an SD Card Detect. MIO pin 0 must be Low during the SDIO boot process.

Title		
AntMiner_ControlBoard_XC7010		
Size	Document Number	Rev
A	<Doc>	V1.0
Date:	Friday, July 01, 2016	Sheet 3 of 15

BANK 501

12 ETH_TX_CLK
12 ETH_TXD0
12 ETH_TXD1
12 ETH_TXD2
12 ETH_TXD3
12 ETH_TX_CTRL
12 ETH_RX_CLK
12 ETH_RX0
12 ETH_RX1
12 ETH_RX2
12 ETH_RX3
12 ETH_RX_CTRL

U1C
A19 PS_MIO16_501
E14 PS_MIO17_501
B18 PS_MIO18_501
D10 PS_MIO19_501
A17 PS_MIO20_501
F14 PS_MIO21_501
B17 PS_MIO22_501
D11 PS_MIO23_501
A16 PS_MIO24_501
F15 PS_MIO25_501
A15 PS_MIO26_501
D13 PS_MIO27_501

PS_MIO40_501
PS_MIO41_501
PS_MIO42_501
PS_MIO43_501
PS_MIO44_501
PS_MIO45_501

PS_MIO32_501
PS_MIO33_501
PS_MIO34_501
PS_MIO35_501
PS_MIO28_501
PS_MIO37_501
PS_MIO38_501
PS_MIO39_501

PS_MIO29_501
PS_MIO30_501
PS_MIO31_501
PS_MIO36_501

PS_MIO46_501
PS_MIO47_501
PS_MIO48_501
PS_MIO49_501
PS_MIO50_501
PS_MIO51_501
PS_MIO52_501
PS_MIO53_501

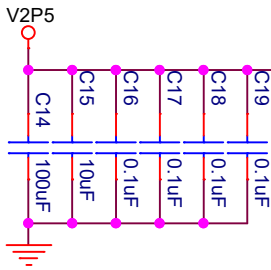
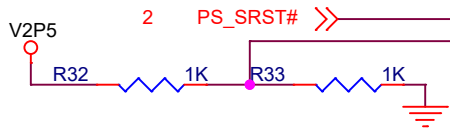
D14 R31 33 PS_MIO40_CD_CLK 11
C17 PS_MIO41_CD_CMD 11
E12 PS_MIO42_CD_D0 11
A9 PS_MIO43_CD_D1 11
F13 PS_MIO44_CD_D2 11
B15 PS_MIO45_CD_D3 11

A14 PS_MIO32_EN5 13
D15 PS_MIO33_EN6 13
A12 PS_MIO34_EN7 13
F12 PS_MIO35_EN8 13
C16 PS_MIO28_EN1 13

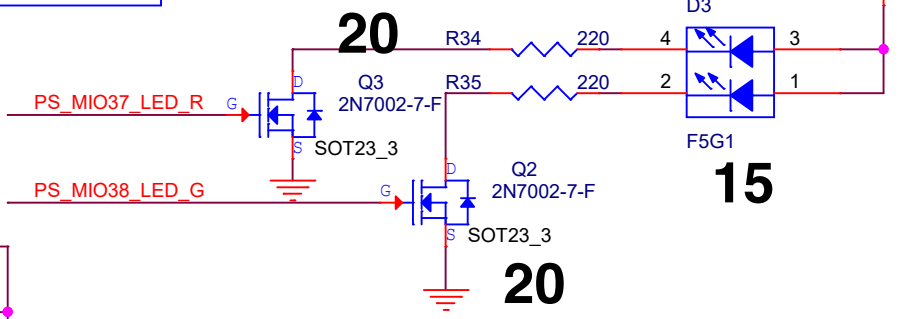
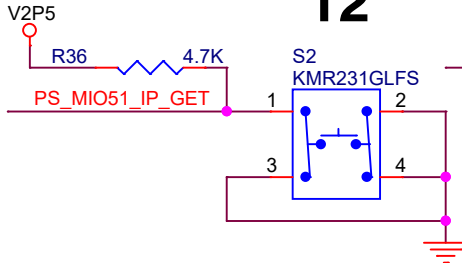
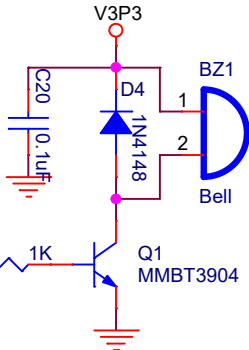
A10 PS_MIO37_LED_R
E13 PS_MIO38_LED_G
C18 PS_MIO39_BEEP

C13 PS_MIO29_EN2 13
C15 PS_MIO30_EN3 13
E16 PS_MIO31_EN4 13
A11 PS_MIO36_EN9 13

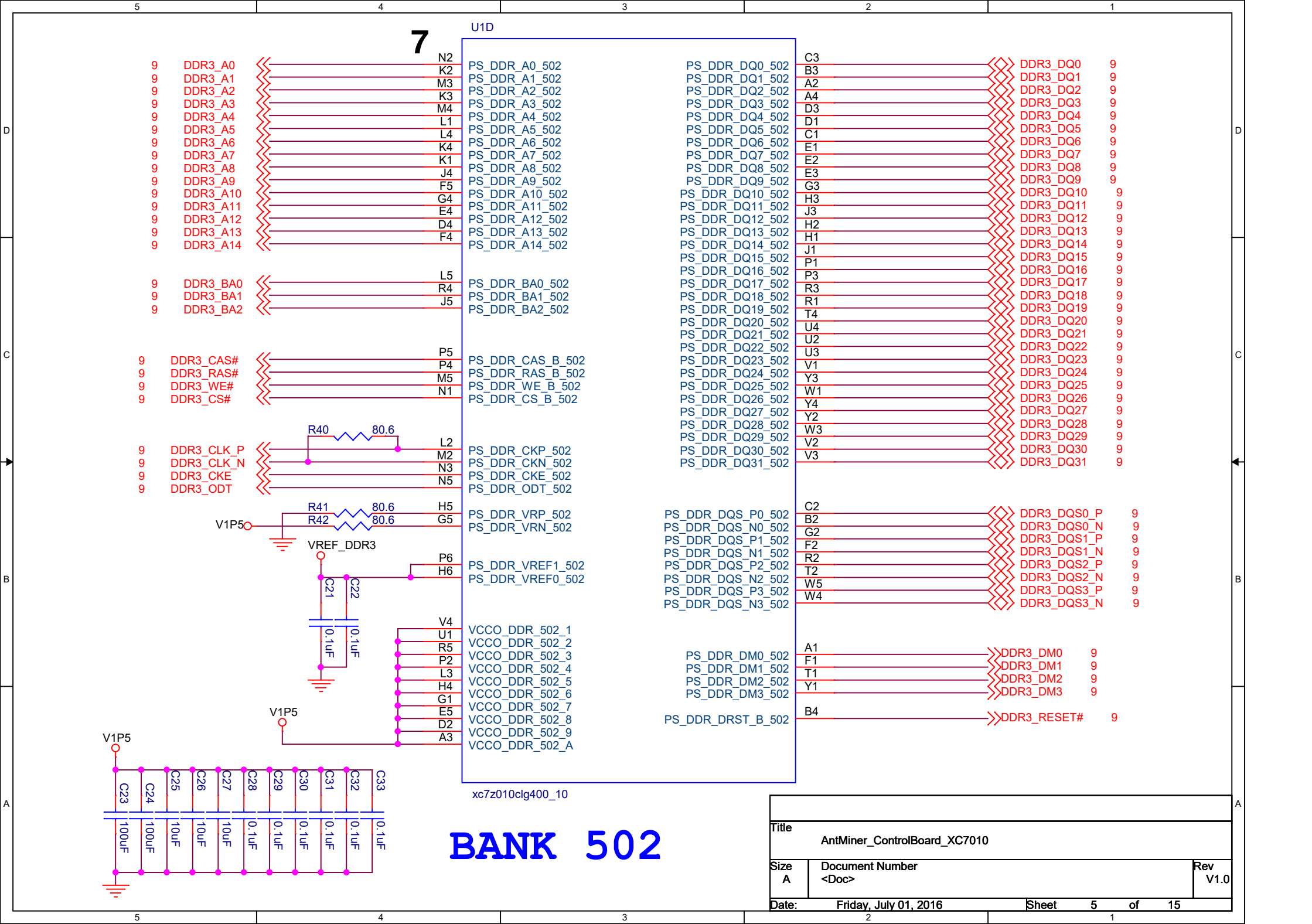
D16 PS_MIO46_CD_SW 11
B14 PS_MIO47_KEY_RESET 2
B12 MIO48_UART_TXD 11
C12 MIO49_UART_RXD 11
B13 PS_MIO50_CD_WP 11
B9 PS_MIO51_IP_GET
C10 ETH_MDC 12
C11 ETH_MDIO 12



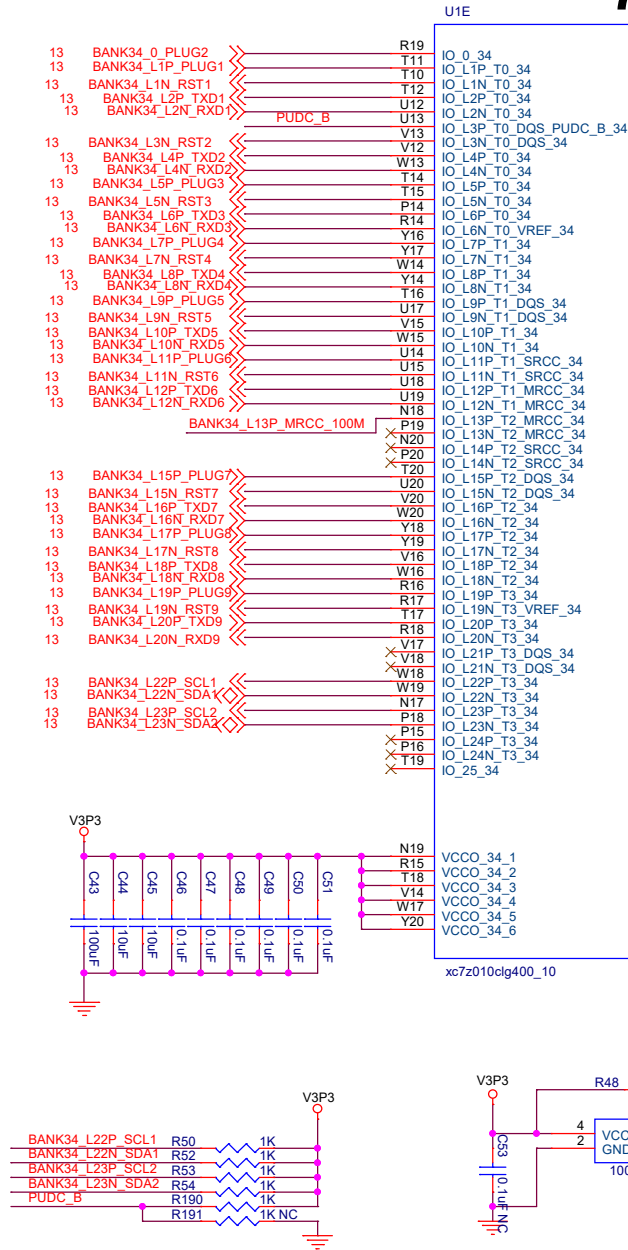
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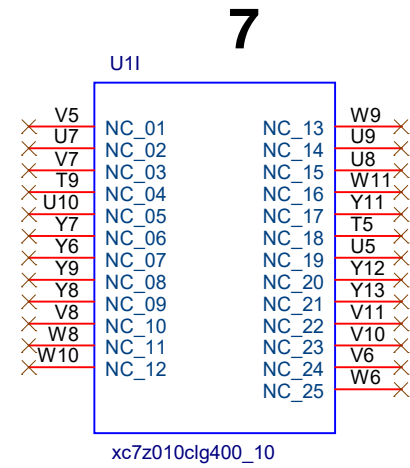
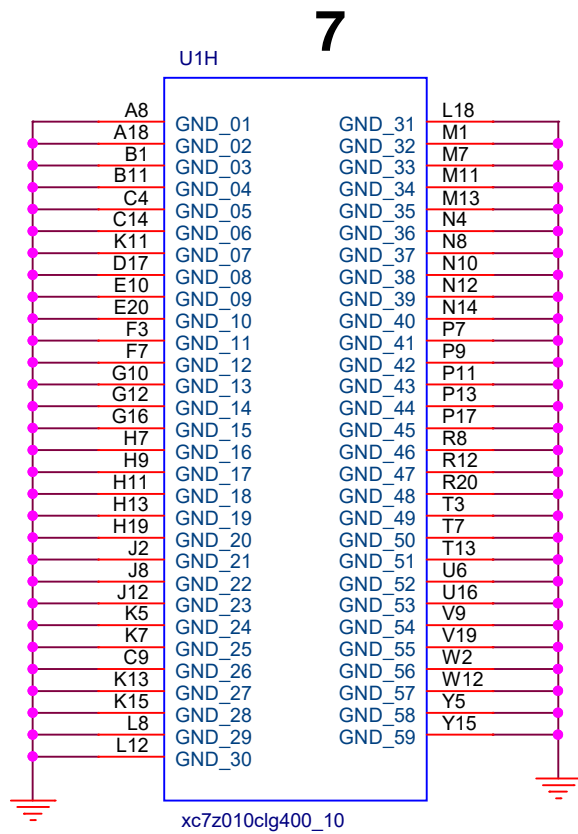


Title		
AntMiner_ControlBoard_XC7010		
Size	Document Number	Rev
A	<Doc>	V1.0
Date: Friday, July 01, 2016		
Sheet 4 of 15		

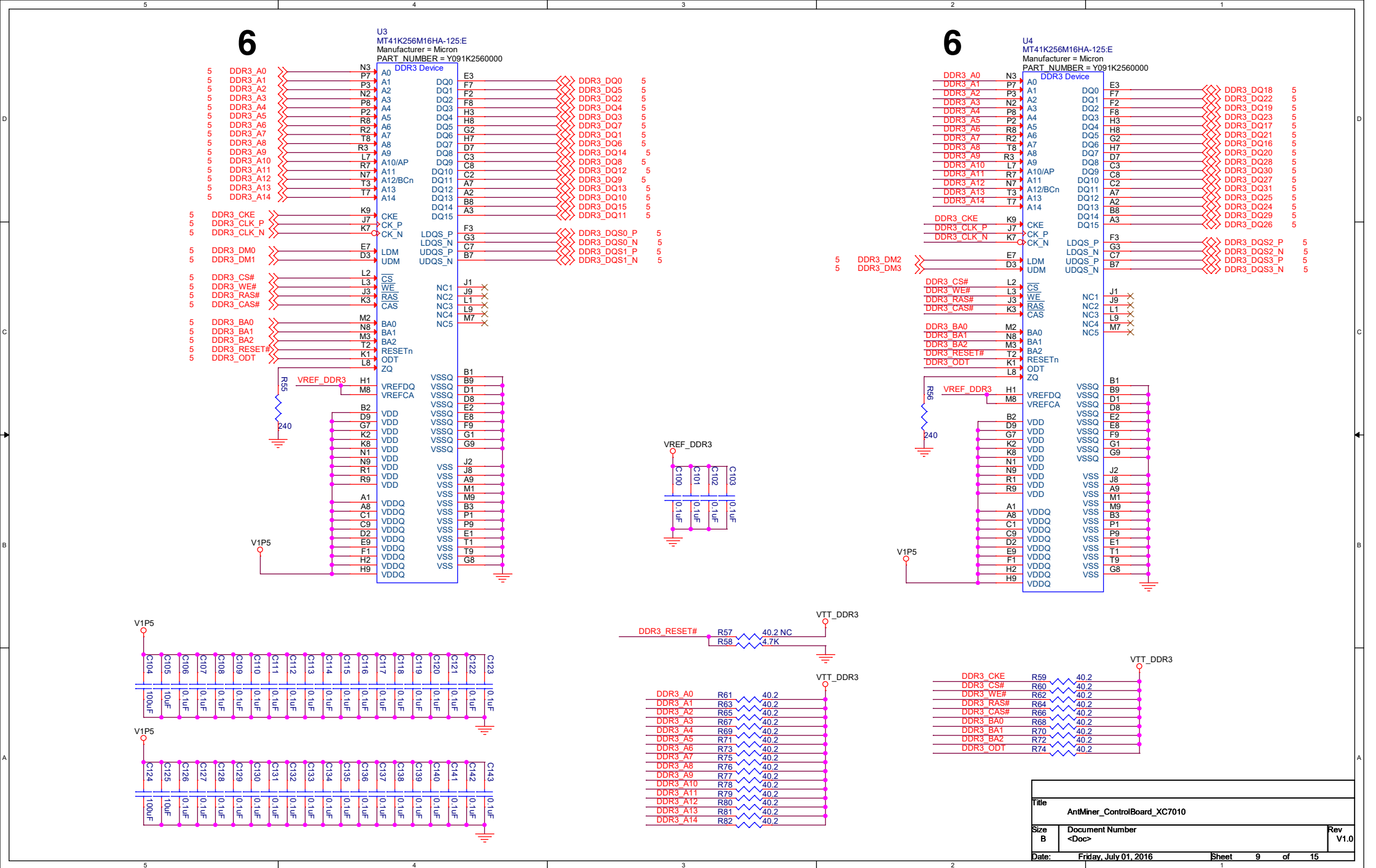


PL BANK34 、 35



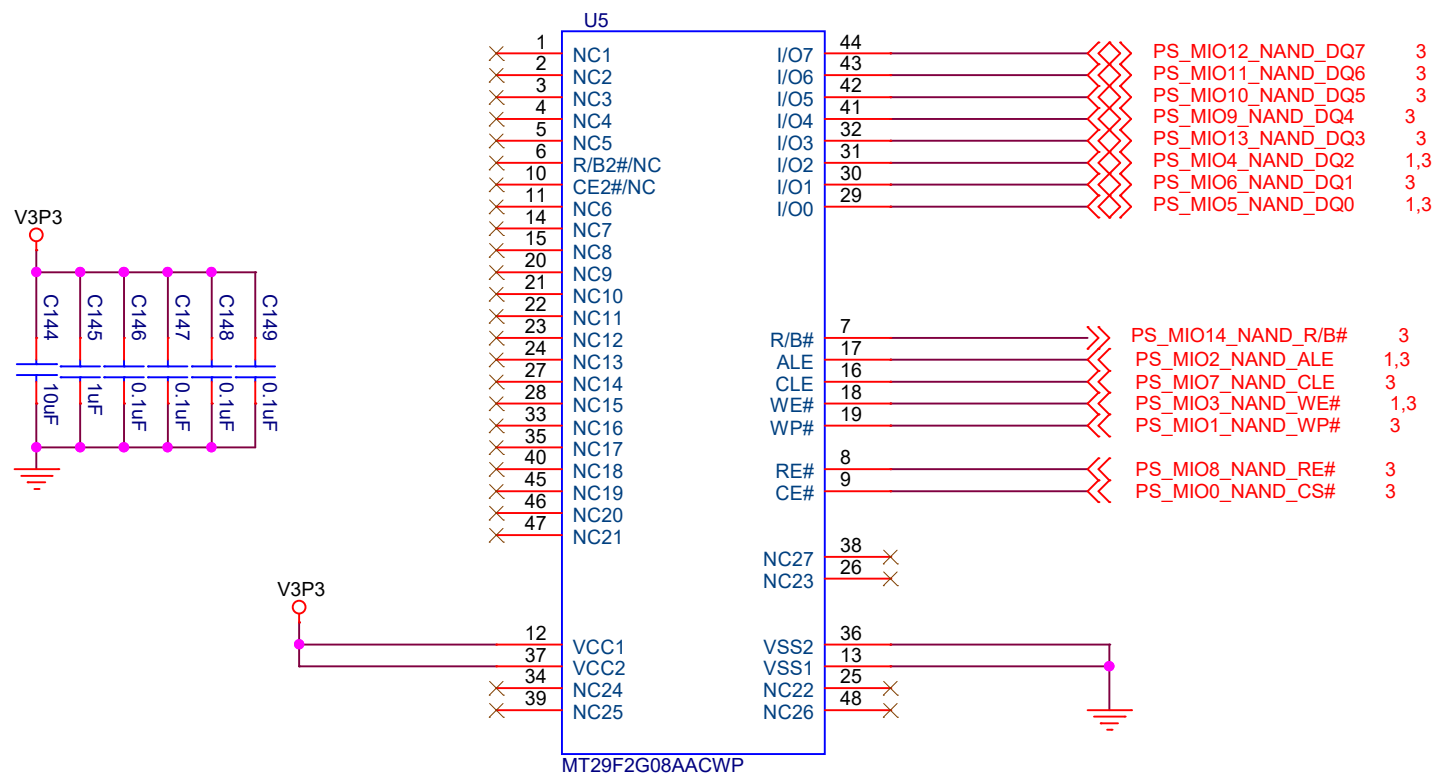


Title			
AntMiner_ControlBoard_XC7010			
Size	Document Number		Rev
A	<Doc>		V1.0
Date:	Friday, July 01, 2016	Sheet	8 of 15

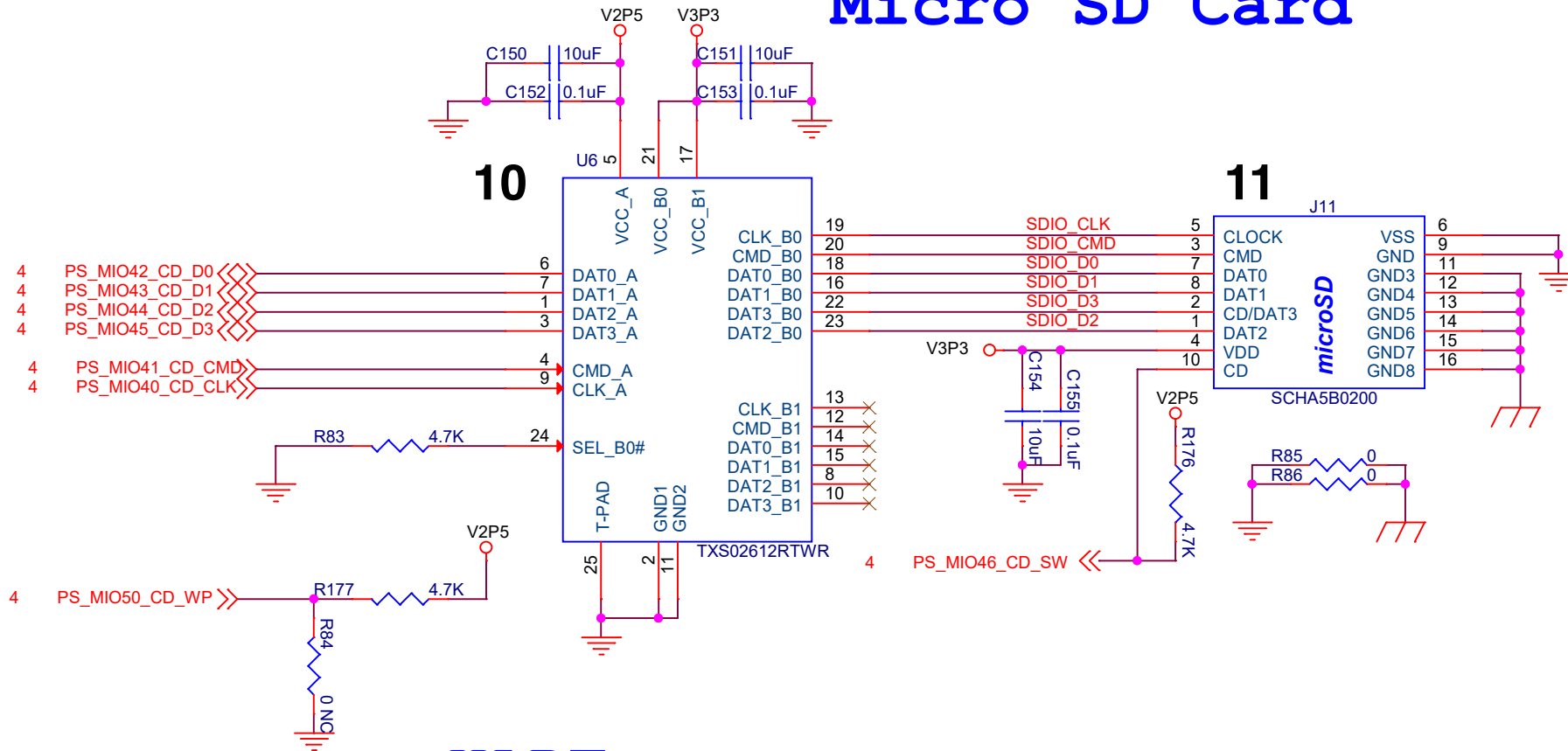


NAND FLASH

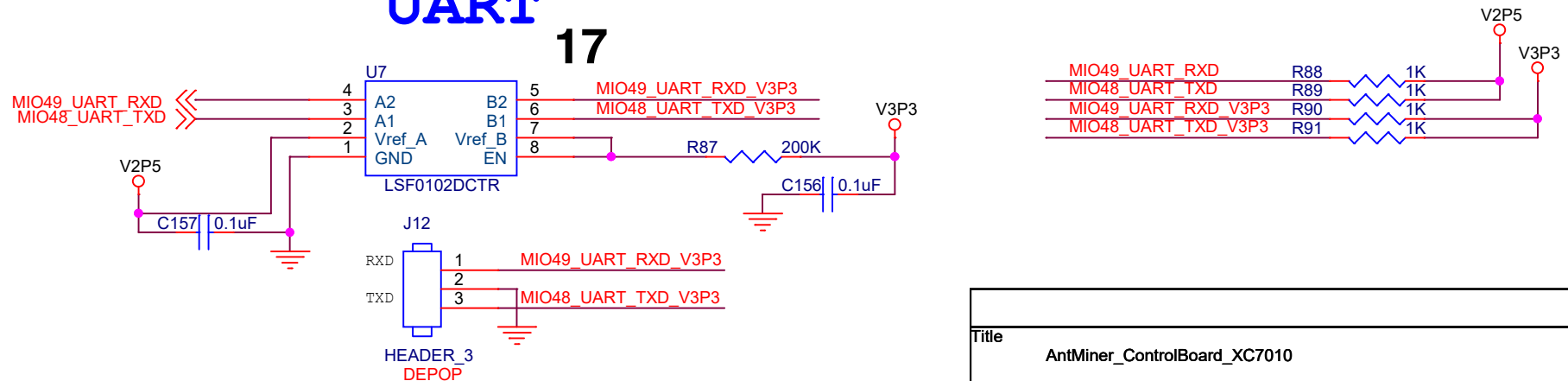
8



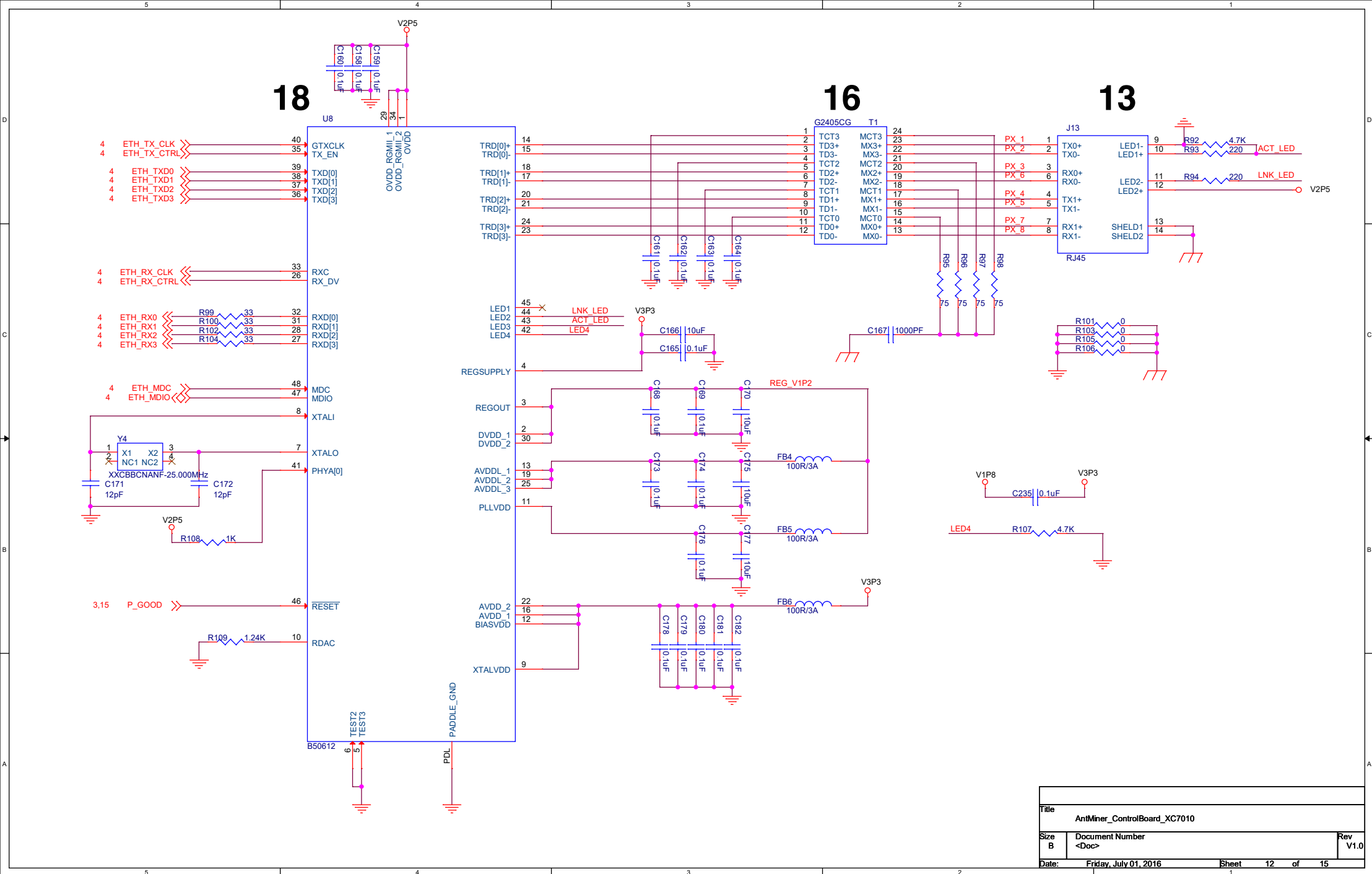
Micro SD Card



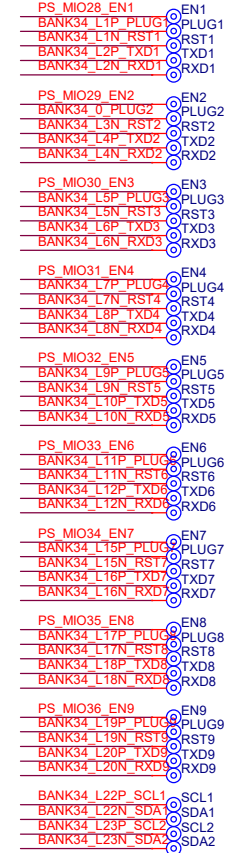
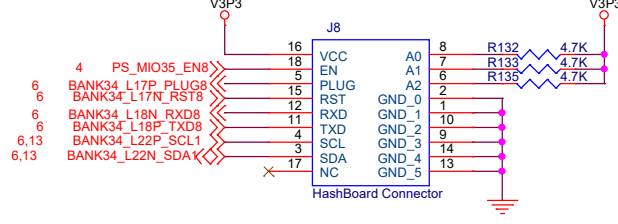
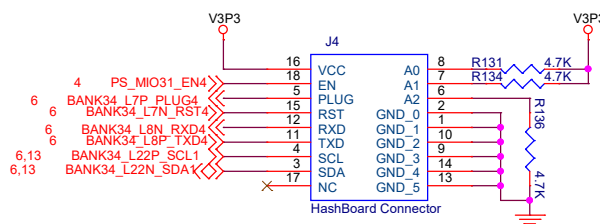
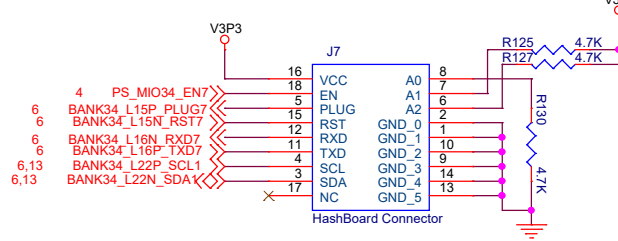
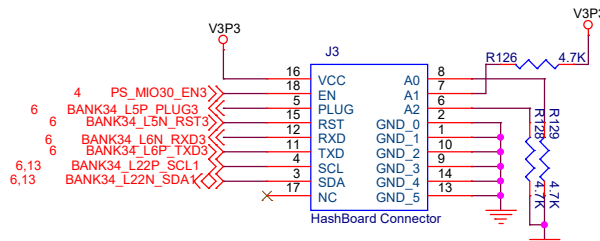
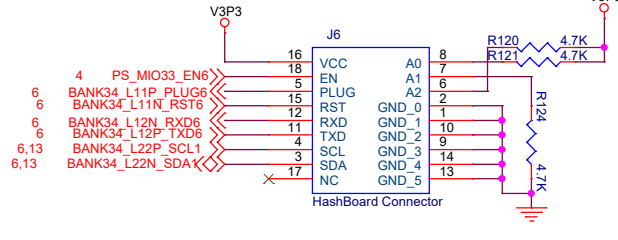
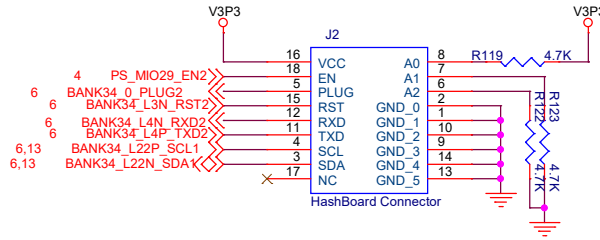
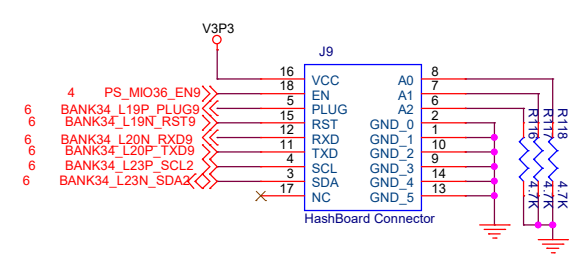
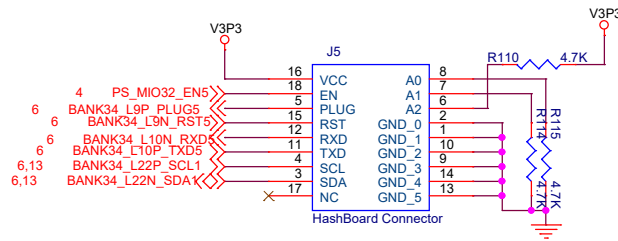
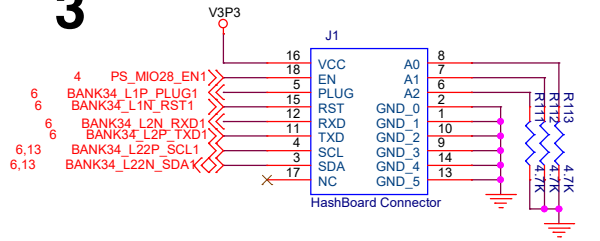
UART



Title		
AntMiner_ControlBoard_XC7010		
Size	Document Number	Rev
A	<Doc>	V1.0
Date:	Friday, July 01, 2016	Sheet 11 of 15

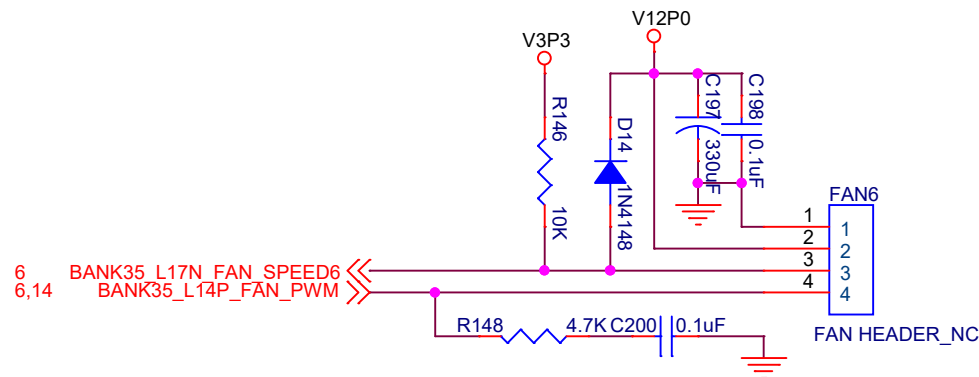
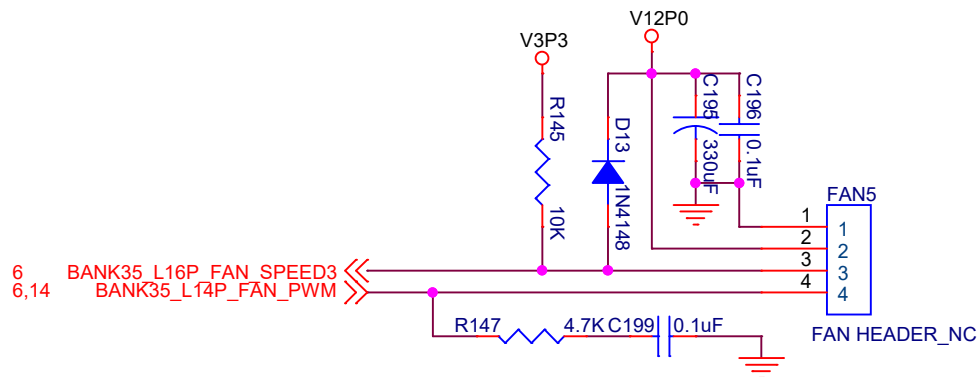
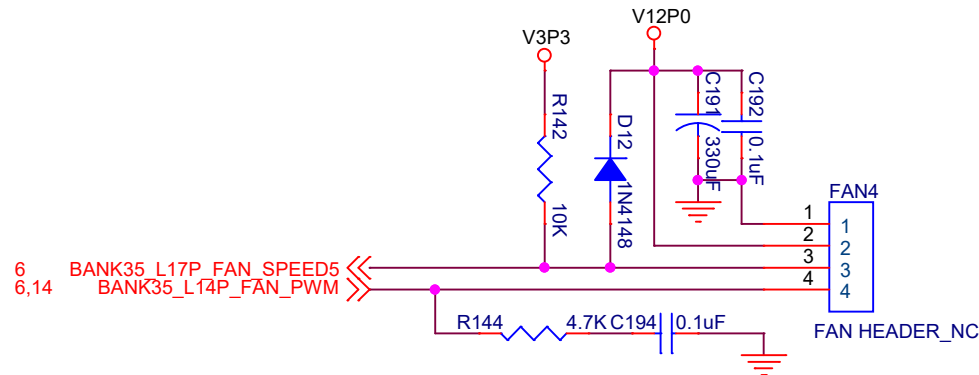
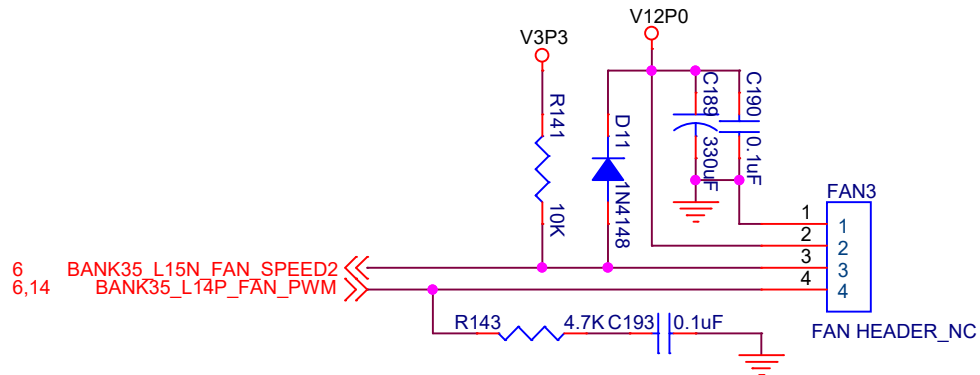
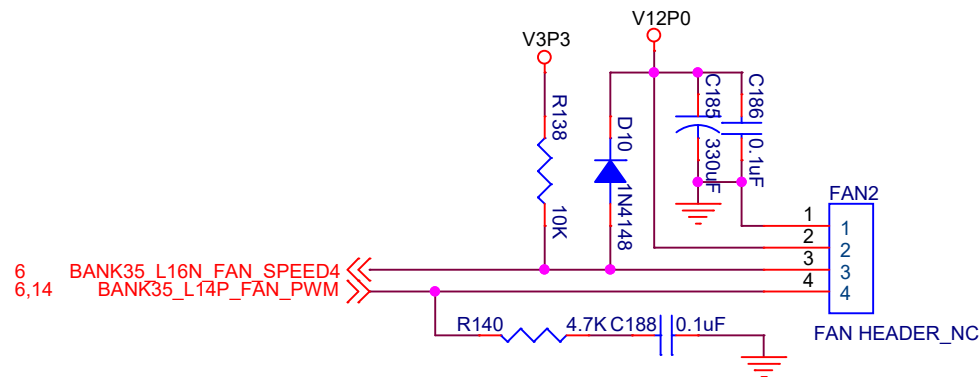
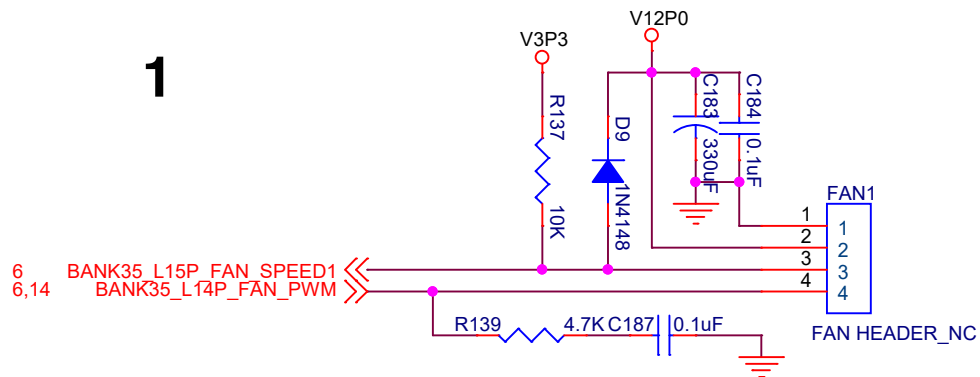


3



Title		
AntiMiner_ControlBoard_XC7010		
Size	Document Number	Rev
B	<Doc>	V1.0
Date:	Friday, July 01, 2016	Sheet 13 of 15

1



Title		
AntMiner_ControlBoard_XC7010		
Size	Document Number	Rev
A	<Doc>	V1.0
Date:	Friday, July 01, 2016	Sheet 14 of 15

4

