

# Srikanth Manikarnike

◆ 19000 NW Evergreen Pkwy Apt. 125 Hillsboro OR 97124 ◆ smanikar@cs.utah.edu ◆ 213-379-3116 ◆

EDUCATION	<b>University of Utah</b> Salt Lake City, Utah <ul style="list-style-type: none"><li>GPA: 3.8 on 4.0</li></ul> <b>R. V. College of Engineering</b> Bangalore, India <ul style="list-style-type: none"><li>GPA: 71.2 on 100.0</li></ul>	<i>Master of Science – Computer Science</i>  <i>Bachelor of Science – Computer Science</i>
SUMMARY	<ul style="list-style-type: none"><li>5 years of work experience as a networks and systems programmer</li><li>Expertise in C, linux kernel programming, OOP methodologies</li><li>Knowledge of IPSec, Layer 2/Layer 3 protocols, FreeBSD</li></ul>	
WORK EXPERIENCE	<b>Flux Research Group, University of Utah</b> <i>Research Assistant</i> <ul style="list-style-type: none"><li>Worked on enhancing <i>dummynet</i> – FreeBSD’s traffic shaping kernel module by adding mathematical models to its delay episodes and loss events.</li><li>Created predictable and repeatable environment on testbed nodes to produce required QoS on emulated links.</li><li>Added tuning features for links/nodes to mimic latencies and bandwidths of that of the real world (based on geographical location).</li></ul> <b>MindTree Ltd.</b> <i>Senior Software Engineer</i> <ul style="list-style-type: none"><li>Implemented microcode loader for IPSec Application Gateway for Intel’s IXP 2850 Network processor.</li><li>Wrote debug support for micro-engine driver issues reported on XScale Core Components.</li><li>Implemented debug support feature to fetch and write to DRAM and Scratch Pad memory using the micro-engine driver.</li><li>Developed and maintained the Captive Portal module for their access point and wireless switch with web, SNMP and command line interfaces.</li></ul> <i>Software Engineer</i> <ul style="list-style-type: none"><li>Provided debug functionality to Access Control List and Traffic Management module on Cavium Datapath.</li><li>Created a validation test setup for unit, system and regression tests.</li></ul>	<b>August 2012 – present</b> Salt Lake City, Utah  <b>October 2007 – July 2010</b> Bangalore, India
SKILLS	<b>Languages:</b> C, C++, MicroC, Racket <b>Operating systems:</b> Unix, Linux, VX Works, Windows <b>Tools:</b> gdb, iperf, isic, IXA SDK 4.2, , IXWlan, IXIA, git <b>Scripts:</b> python, perl, shell	
ADDITIONAL PROJECTS	<b>Ramdisk Implementation</b> <a href="#">[src]</a> Implemented a secondary storage disk device on primary memory, supporting synchronized random-access reads and writes.	C/Linux
	<b>Primality Testing</b> <a href="#">[src]</a> We implemented an efficient primality tester using AKS and Miller-Rabin Primality Testing algorithms.	C++/Linux
	<b>Dynamic Binary Translation</b> <a href="#">[src]</a> A solution to accelerate dynamic binary translation for fixed size instructions using CUDA.	C++, CUDA/Linux
	<b>Compiler for Python</b> <a href="#">[src]</a> Wrote a source-to-source compiler to translate Python to C using Racket.	Racket/Linux
	<b>Player &amp; AI for Triple Town</b> <a href="#">[src]</a> Wrote a playable version and an AI for Android game in Racket.	Racket/Mac OS