

SmartSeller V2 — OAuth Authorization Blocker Report

1. Executive Summary

SmartSeller V2 is currently unable to complete the OAuth2 Authorization Code flow with Mercado Libre from the browser environment. Although the backend start endpoint responds correctly, the navigation to Mercado Libre's authorization page is not completing from the browser context. This prevents token acquisition and blocks the MVP from operating with real sellers.

2. Current Technical State

- The /api/auth/meli/start endpoint responds with HTTP 307 and correct Location header.
- State and PKCE verifier cookies are properly set.
- Middleware does not intercept /api routes.
- CTA buttons use anchor navigation (no fetch-based OAuth trigger).
- However, browser navigation does not reach Mercado Libre authorization UI.

3. OAuth Circuit Status

- Start Endpoint: Functional (confirmed via curl).
- Browser Navigation: Blocked.
- Authorize (Mercado Libre): Not reached.
- Callback Execution: Not triggered.
- Token Exchange: Not executed.
- Refresh Flow: Not executed.
- Token Persistence: Not executed.

4. Business Impact

Without successful OAuth authorization, SmartSeller cannot ingest live seller data, cannot compute operational health signals, and cannot deliver clinical scoring. As a result, the MVP is functionally blocked.

5. Root Cause Hypothesis

- Potential redirect or rewrite rule at hosting layer (Vercel dashboard configuration).
- Possible server-side session guard redirecting to /enter.
- Edge caching or navigation behavior interfering with document redirect.

6. Immediate Corrective Plan

- Audit Vercel redirect and rewrite rules.
- Audit server-side layout guards for unintended redirects.
- Confirm document navigation behavior directly in production browser session.
- Re-validate callback and token exchange once navigation is restored.

Conclusion: The OAuth protocol implementation is structurally correct at the backend level. The blocker exists at the navigation or hosting layer. Resolution of this layer will unlock the entire authorization circuit.