Figure 1: Acceleration module 1.1
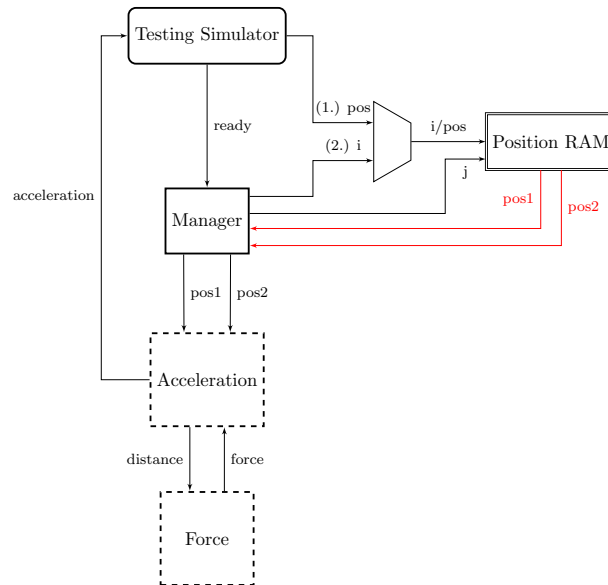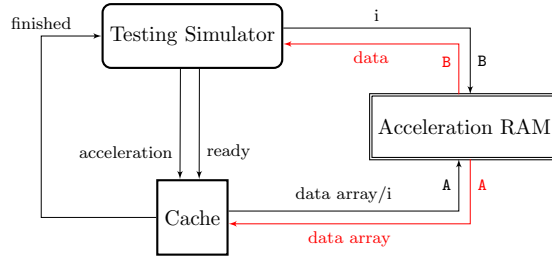

Figure 2: Acceleration module 1.2
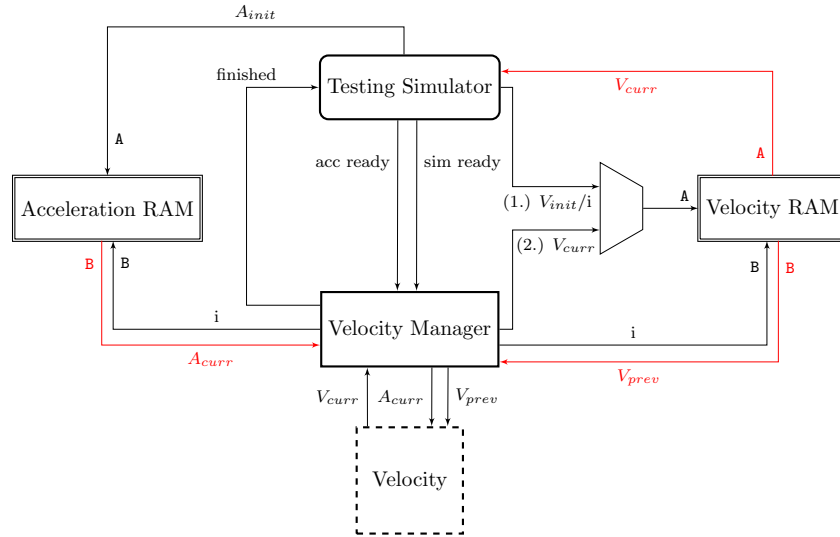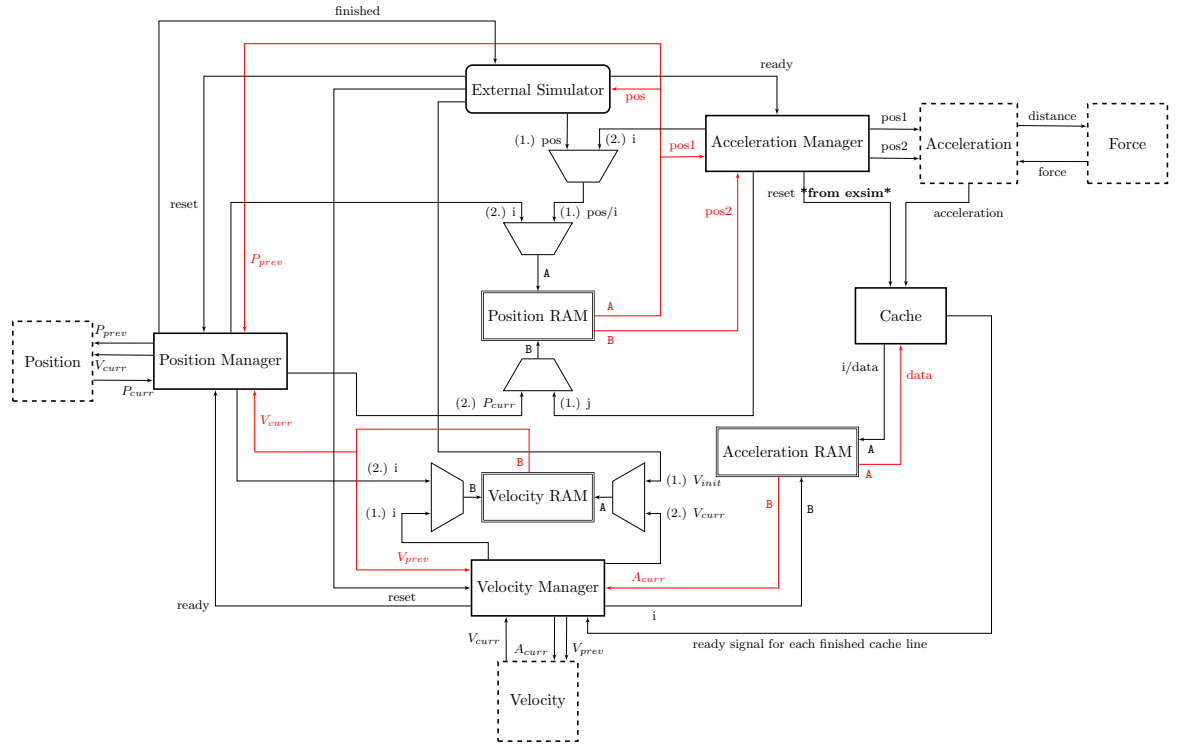
1

Figure 3: Cache module 1.1

Figure 4: Velocity-(/Position)-update module 1.1

Figure 5: Full LJ circuit

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