




# Mihir Shete

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## CONNECT @

 LinkedIn: mihirshete  
 Github: smihir  
 Facebook: mihirshete

## SKILLS

### PROGRAMMING

C • Lua • Shell • Matlab  
Octave • Python • Rails  
Assembly •  $\text{\LaTeX}$

### OTHERS

802.11 • Networking •  
Linux Device Drivers

## PROJECT LINKS

### QUALCOMM

*(Opensource contributions only)*  
Prima Wlan Driver  
Qualcomm MSM Kernel

### CYPRESS SEMICONDUCTOR

Blood Pressure Monitor

## COURSEWORK

### UNDERGRADUATE

- Embedded System Design
- Medical Instrumentation
- Digital Electronics and Computer Organization
- Microprocessor Programming and Interfacing
- Analog Electronics  
*(Professional Assistant)*
- Computer Programming I/II
- Probability and Statistics
- Control Systems

### QUALCOMM

- ARMv8 Architecture and Design

## EXPERIENCE

### QUALCOMM | ENGINEER

July 2012 – Till Date | Hyderabad, India

- A member of Wireless Connectivity group, we are involved in design and development of firmware and Linux drivers for Qualcomm's wireless chipsets.
- I primarily work on Qualcomm's opensource **Prima** driver and I am currently the maintainer of data path.
- Worked with the Linux community to design and develop the regulatory framework in Prima driver.
- Developed good understanding of new and upcoming wireless protocols and standards like WFA's - WPS, P2P, WMM and IEEE 802.11ac/p

### TEAMF1 NETWORKS | SOFTWARE ENGINEER

May 2010 – June 2012 | Hyderabad, India

- Design and Development of Linux Device drivers for 802.11 Wireless SoCs in Enterprise Routers and Access Points.
- Developed an excellent understanding of 802.11/a/b/g/n/i/r.
- Worked on securing the WiFi Alliance's 802.11n certification for interoperability.
- Involved in design and development of 802.11r solution independent of the available open source alternatives.
- Designed and Developed the RSTP functionality for Access Points.

### CYPRESS SEMICONDUCTOR | TRAINEE

July 2009 – December 2009 | Chennai, India

- Design and development of Blood Pressure Monitor.
- PCB design using EDA tools like Capture CIS and Allegro
- Gained good insight into working of SoCs and process that go in design and development of a good embedded system.

### MAHINDRA & MAHINDRA | SUMMER INTERN

June 2008 – July 2008 | Nagpur, India

- Did a detailed study of the Tractor Assembly Line.
- Drafted the Electrical Line Diagram of the Tractor Assembly Plant.

## EDUCATION

### BITS - PILANI, GOA CAMPUS

B.E(HONS.) ELECTRONICS AND INSTRUMENTATION

August 2006 – May 2010 | Goa, India

Cum. GPA 8.00/10

- Concentrated on projects and internships in the areas of Embedded systems' design and development.
- As a member of PSoC club which was newly founded, was involved in designing training material which is used by Cypress Semiconductor as the official Lab Book for the University Alliance Program.

### SHRI SHIVAJI SCIENCE COLLEGE

Grad. May 2006 | Nagpur, India

Score 92.53%

## LINKS

(Academic Project Reports)

- Design and Development of Blood Pressure Monitor
- Safety Critical Systems and Design

## ACADEMIC PROJECTS & RESEARCH

### DESIGN AND DEVELOPMENT OF BLOOD PRESSURE MONITOR

Jan 2010 – April 2010 | Goa, India

- Worked with **Aniket Sachan** and **Vikash Sharma** under the guidance of **Sarita Kumari** to continue the work done by me in Cypress Semiconductor.
- We concentrated more on developing simulation of circuits and filters involved in the designing of a BP Monitor so that we can improve upon them.
- Another aspect of this project was to do a detailed analysis of the available BP calculating algorithms and implement them in Matlab/Octave so that they can be studied in detail and be improved for better accuracy.

### DESIGN AND VERIFICATION OF SAFETY CRITICAL SYSTEMS

Jan 2010 – April 2010 | Goa, India

- The 6 months in Cypress' Medical engineering group fostered the importance of considering safety while designing systems that are used in critical areas and I took this study project under the guidance of **Dr. K.R Anupama**.
- This project was to study the established standards for designing safety critical systems and provide guidelines and introduction to formal methods for building Safety Critical Systems.

## AWARDS & RECOGNITION

2010	2 <sup>nd</sup>	Symphony Event held in Quark '10(techfest of BITS, Goa)
2006	National	Placed 23 <sup>rd</sup> in the Merit List prepared by Maharashtra State Board for Secondary and Higher Secondary Education
2006	National	Placed 921 <sup>st</sup> in All India Engineering Entrance Examinations