

PCB Design

Schematic

Advanced Integrated Systems Lab.

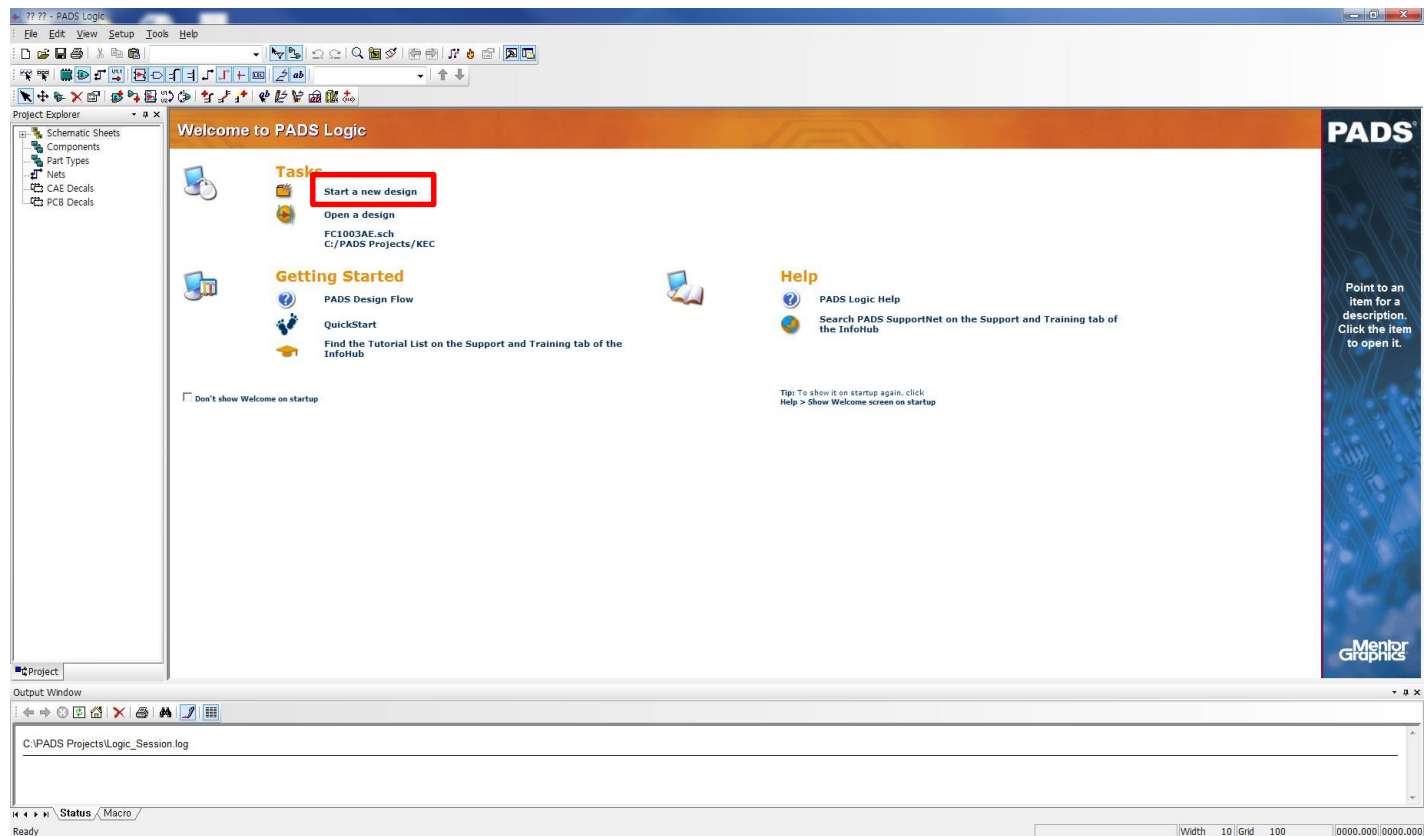


Korea University



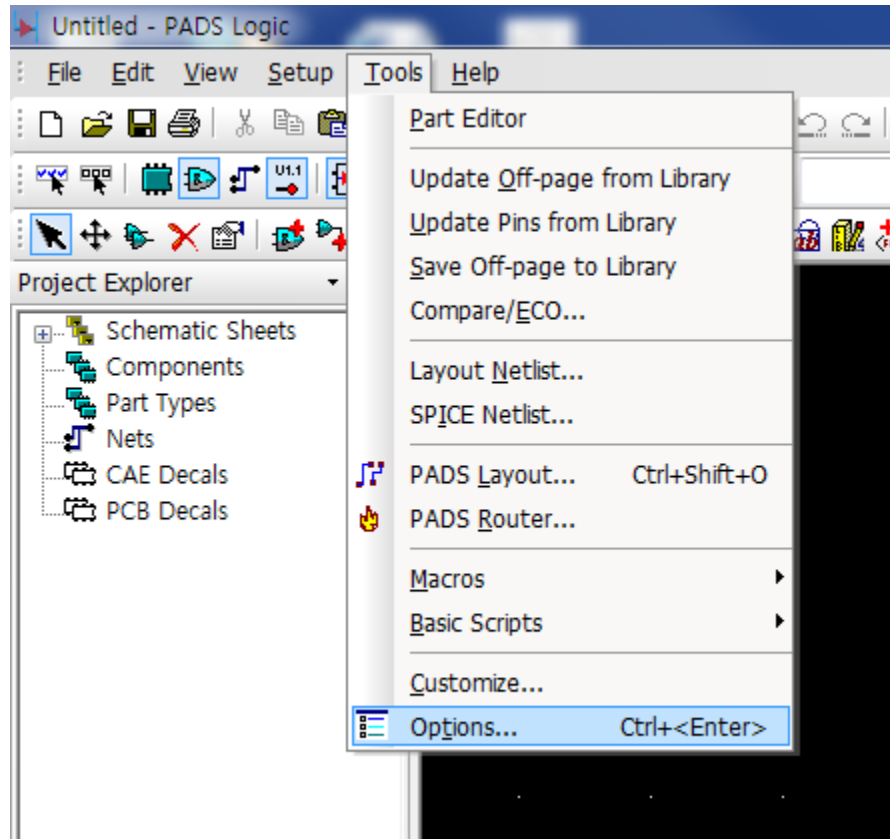
PADS Logic

- Start PADS Logic
 - Click 'start a new design'

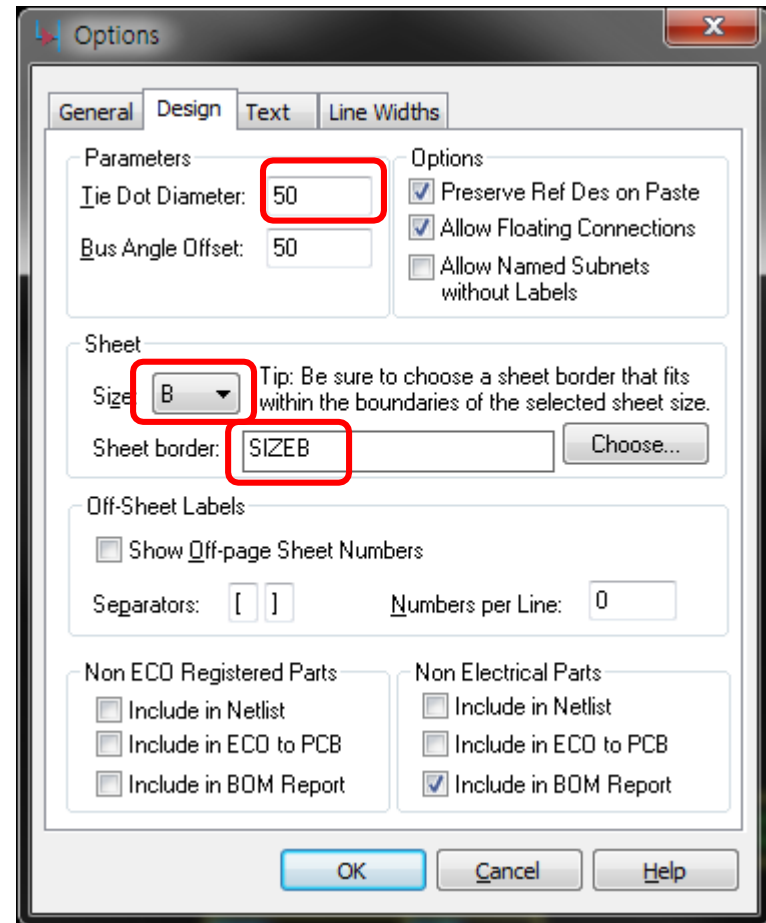
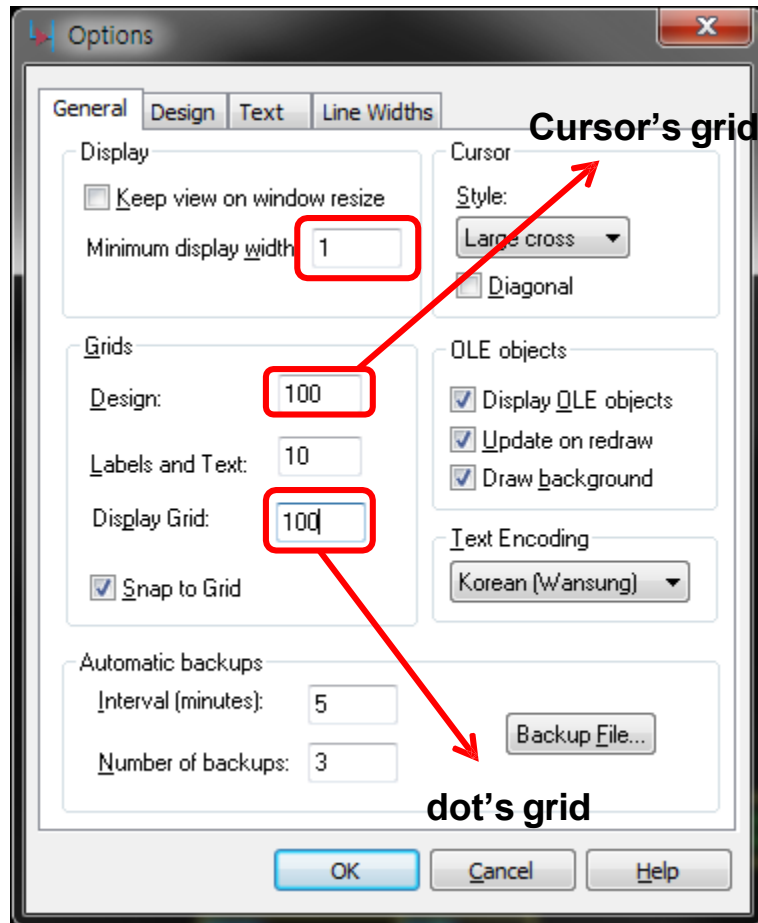


PADS Logic Setting (1)

- Tools-options



PADS Logic Setting (2)



Sheet Form (1)

- Edit the Sheet form
 - Let's delete the useless part of the sheet

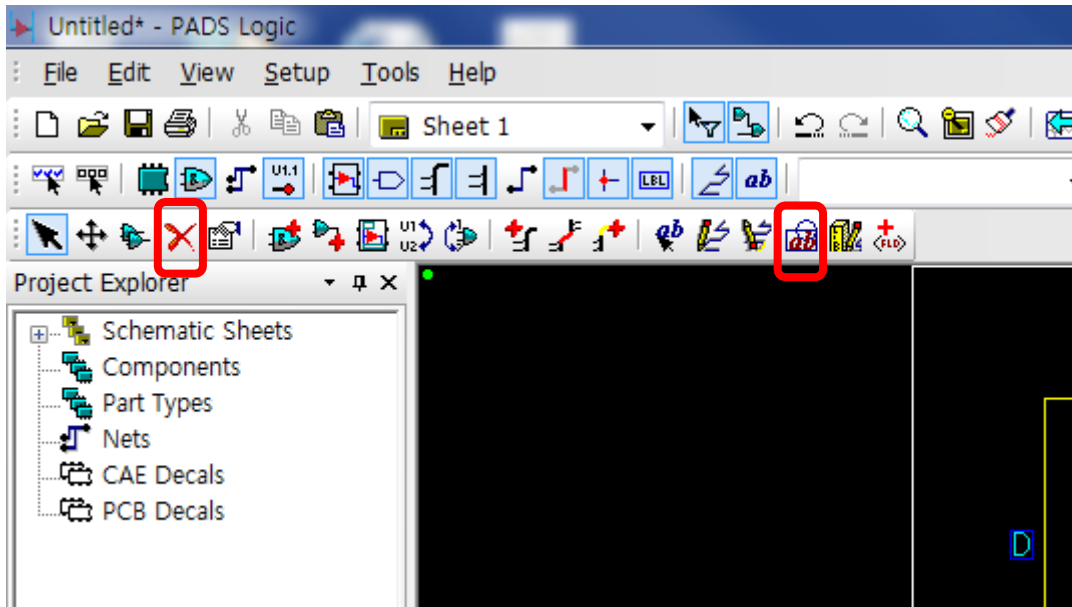
The image shows a technical drawing sheet form template. It features a grid with columns labeled 1 to 6 and rows labeled A to D. Two red boxes highlight specific areas:

- Top Right Box:** A table with the following structure:

REVISION/RECORD			
DATE	CODE (NO)	APPROVED	DATE
- Bottom Center Box:** A table with the following structure:

DESIGN	DATE	COMPANY	
<Drawn By>	<Drawn Date>	<Company Name>	
CHECKED	DATE	TITLE	
<Checked By>	<Checked Date>	<Title>	
QUALITY CONTROL	DATE	CODE	SIZE
<QC By>	<QC Date>	<Code>	<Size>
RELEASED	DATE	DRAWING NO	
<Released By>	<Release Date>	<Drawing Number>	
SCALE		REVISION	DATE
<Scale>		<Revision>	<Date>

Sheet Form (2)



- Click “Combine/Uncombine” and right-click the mouse on the sheet and click “Uncombine”.
- Click items(line, text) should be uncombined.
- Click the right button on the mouse on the sheet and click “Complete”
- Click “Delete” icon and deleted uncombined items.

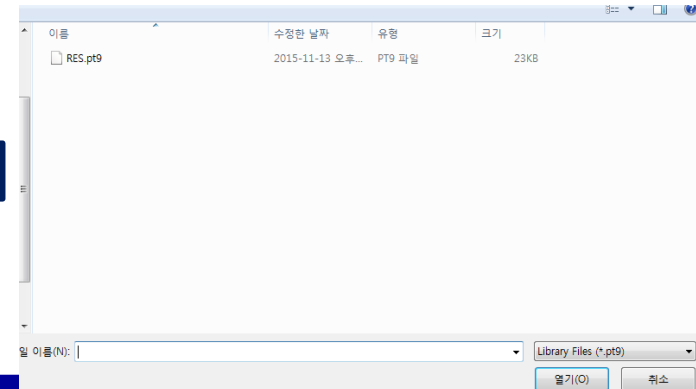
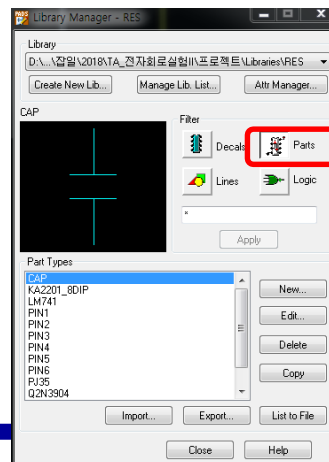
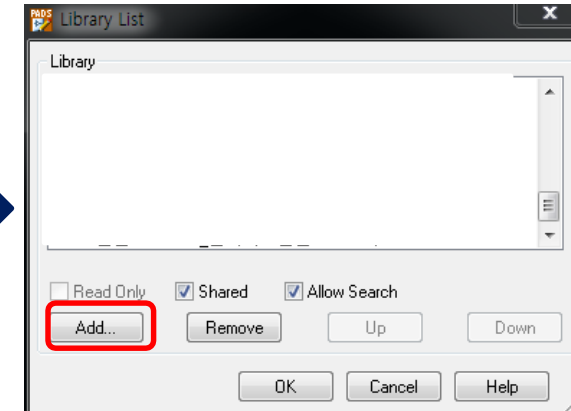
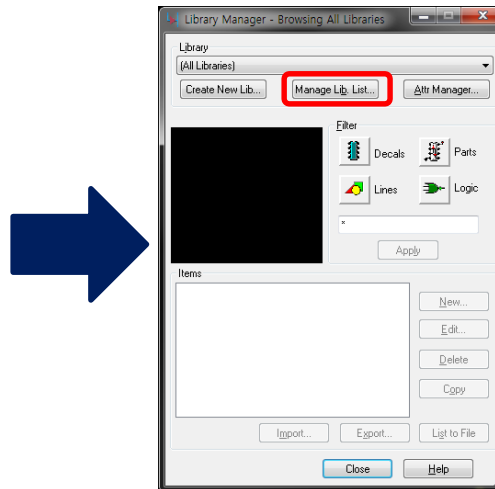
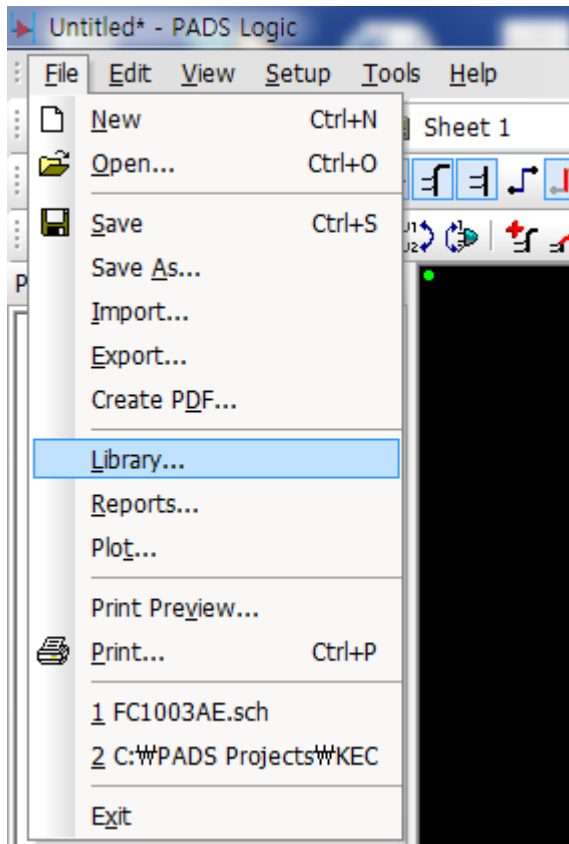
Sheet Form (3)

- Completed Screen

COMPANY: <Company Name>			
TITLE: <Title>			
CODE: <Code>	WELD: B	DRAWING NO.: <Drawing Number>	REVISION: <Revision>
SCALE: <Scale>		SHEET: 1 OF 1	

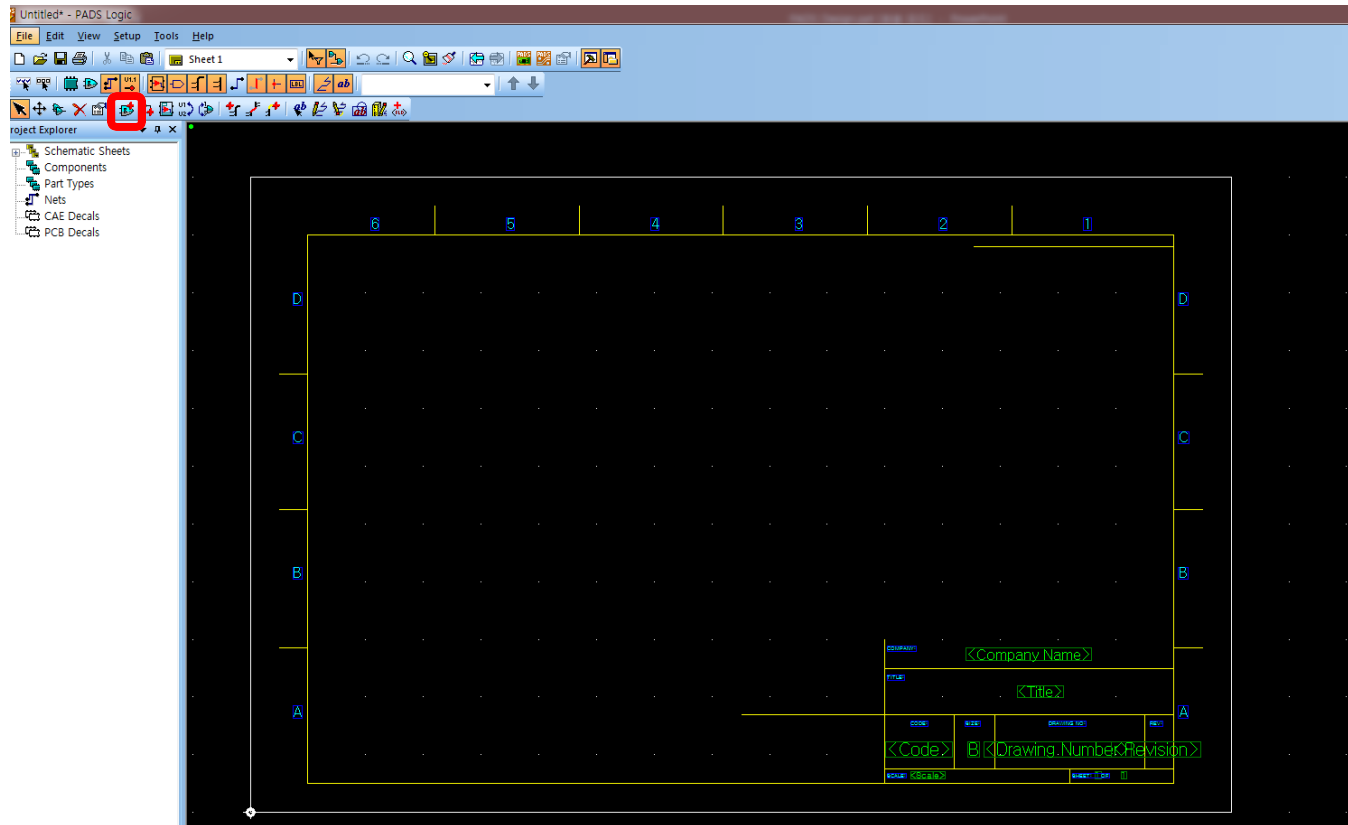
Add library

- Add library



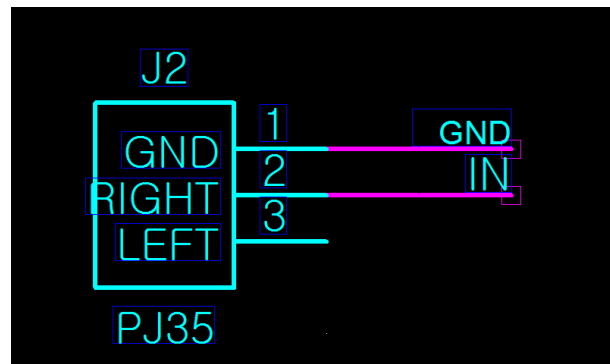
Draw Schematic (1)

- Add part

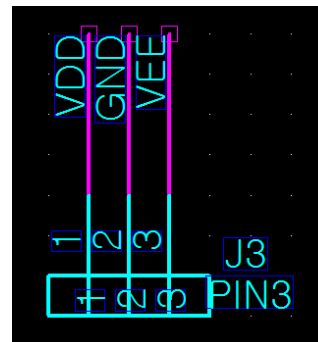


Draw Schematic (2)

- Draw wire – “F2”
- Use “PJ35” for the input jack and terminal 2(RIGHT) is the input terminal and 1 is GND.
(Leave terminal 3 floating)

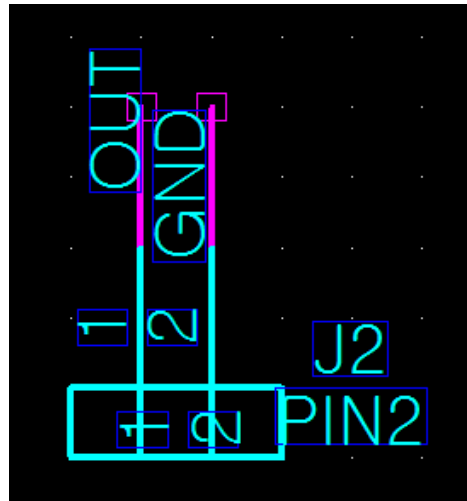


- Use “PIN3” for the DC supply input.



Draw Schematic (3)

- Use “PIN2” for the output jack and terminal 1 is OUT and terminal 2 is GND.



Draw Schematic (4)

- Audio driver

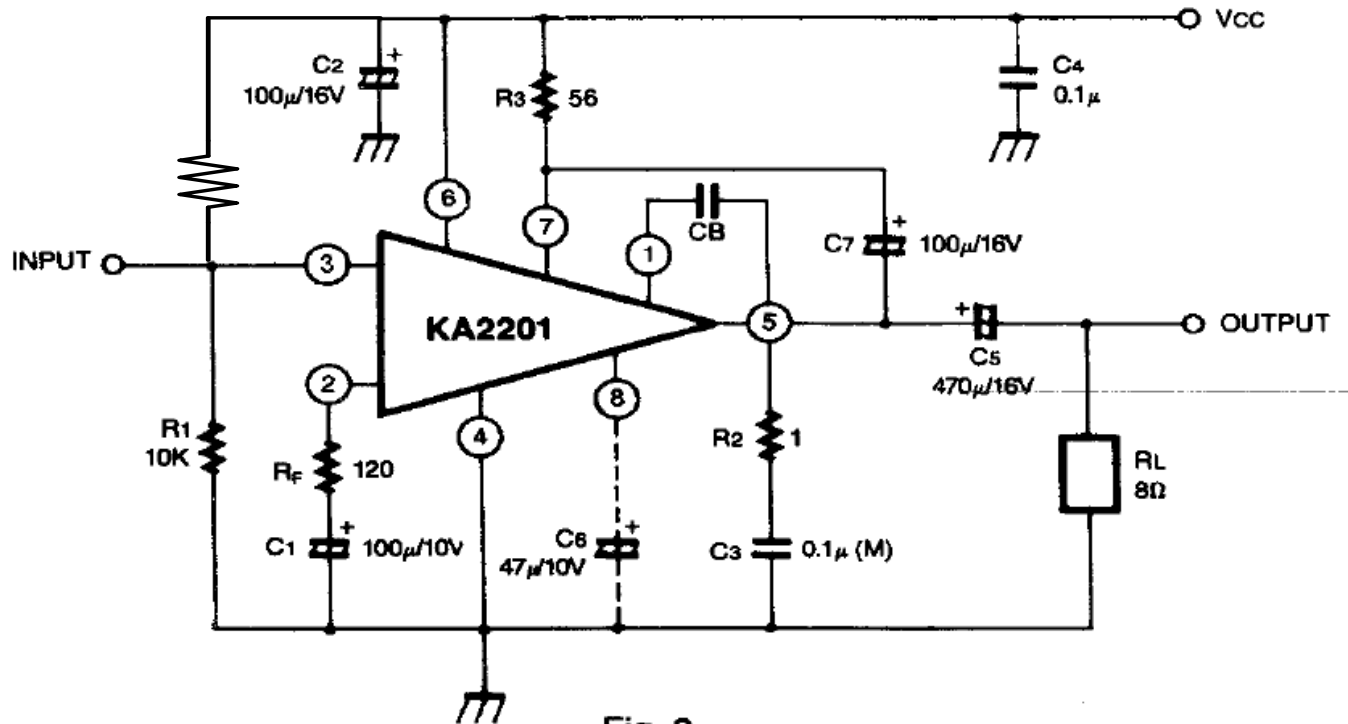


Fig. 2

Layout Netlist

- Generate netlist file after the design is done.

