

Smit Bhavsar

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Summary of Qualifications

- Excellent written documentation skills through writing detailed technical reports about hardware and experimental labs
- Displayed strong leadership and oral communication skills through effectively collaborating with team members on Ryerson Gujarati Association (RGA) and hosting various events
- Demonstrated strong problem-solving skills and ability to adapt and work under pressure through learning concepts of inheritance and polymorphism in my object-oriented engineering analysis and design (COE528) course
- Ability to manage relationships with others when engaging in teamwork, particularly in interdisciplinary environments such as collaborating with my peers on lab reports and maintaining healthy relationships throughout the process

Technical Skills

Programming Languages: Assembly, C#/C++, CSS, Git, HTML, Java, JavaScript, Linux, Python, Swift, SQL, Unix

Applications: Arduino, AutoCAD, Fusion360, MATLAB, Multisim, Quartus, SolidWorks, XCode

Hardware Equipment: Automatic Test Equipment (ATE), Function Generators, Soldering Iron, Multimeters, Oscilloscopes

Education

(B.Eng.), Computer Engineering (2019 – Present)

Ryerson University: CGPA – 3.53/4.33, Dean's List: 2019

Relevant Coursework: Computer Organization and Architecture (COE608), Digital Systems (COE328), Engineering Algorithms and Data Structures (COE428) and Microprocessor Systems (COE538)

Projects

Portfolio Website: HTML • CSS • Bootstrap • JavaScript / Visual Studio Code 08/2021

- Designed and developed a fully responsive portfolio website using HTML, CSS and JavaScript
- Incorporated Bootstrap Framework to access open-source templates for UI Interface elements

Multi-Stage Buffer Amplifier: NI Multisim 12/2021

- Systematized a circuit with a 3-stage 2N3904 transistor configurations with a single-supply that included a multistage, inverting, transistor amplifier to meet electrical requirement
- Circuit was engineered and designed upon Multisim software for voltage gain, quiescent current and input / output resistance.

General-Purpose Processor: VHDL / Quartus II 11/2020

- Constructed a general-purpose processor with the help of three different arithmetic logic units.
- Incorporated Latches, 4-16 Decoders, Moore/Finite State Machines (MSM/FSM), were all used in the creation of the ALU's and the results were all displayed in seven-segment displays (SSEG).

Experience

Dr. X Academy of Robotics and Coding 04/2021 Present

Robotics and Coding Instructor

- Instructing classes of 15-20 students on 2D/3D game making using Unity with both text-based coding (such as C# and Python) as well as mobile app development using App Inventor (primarily Android).
- Teaching a variety of robotics classes which included the use of many gyro, light and ultrasonic sensors, one of the topics included simulating Tesla's accident avoidance system.
- Increased the attendance of FTC as well as FLL robotics classes by 10% throughout the summer
- Educating a small class of 5-7 high school students on Java, including the basic principles of object-oriented programming, inheritance, encapsulation, abstraction and polymorphism.

Soccer World Ryerson 03/2021 - Present

Graphic Designer

- Designing 2+ graphics per week, including weekly flyers for upcoming games as well as events and saw engagement rise on Instagram by over 20%.
- Created daily Instagram stories and weekly posts which saw our page rise from 20 followers to 100+ followers within the first 3 months.

Ryerson Gujarati Association (RGA). 09/2020 - Present

Vice President of Technology

- Launched a fundraiser for the COVID-19 crisis in India and collaborated with Brock GSA and Laurier GSA to raise \$1400.
- Developed a fully functional website for the newly formed organization to plan and sell tickets to various online and in-person events, as well as promote upcoming events: www.ryersonga.com.
- Since the launch of the site in 2019, we've seen an increase of over 50 new people visit our website per month on average