## Assignment 2: Programming with OpenMP

Exercise 1 - OpenMP Hello World, get familiar with OpenMP Environment

- 1. Write an OpenMP C code with each thread printing Hello World from Thread X! where X is the thread ID.
- 2. How do you compile the code in question 1? Which compiler and flags have you used?

```
cc - openmp hello_world.c(gray)
```

OpenMP flag:-openmp

3. How do you run the OpenMP code on Dardel? What flags did you set?

```
srun -n 1 ./hello_world.out
export OMP_NUM_THREADS=4
```

# 4. How many different ways can the number of threads in OpenMP be changed? Which are they?

Here are three ways.

Changing the environment variable OMP\_NUM\_THREADS:

```
export OMP_NUM_THREADS=<number of threads to use>(bash shell)
```

setenv OMP\_NUM\_THREADS <number of threads to use>(csh or tcsh shell)

Using the runtime library functions:

omp\_set\_num\_threads(int num\_threads)

As a clause as part of the directive:

#pragma omp parallel num\_threads(int num\_threads)

#### Exercise 2 - STREAM benchmark and the importance of threads

Exercise 3 - Parallel Sum

Measure the performance of the serial code (average + standard deviation).

Out of 5 measurements with 10<sup>7</sup> elements we obtained:

• Average: 0.032559 s

• Standard deviation: 3.74e-5 s

Implement a parallel version of the serial\_sum called omp\_sum and use the omp parallel for construct to parallelize the program. Run the code with 32 threads and measure the execution time (average + standard deviation). Is and should the code be working correctly? If not, why not?

Using omp parallel for and 32 threads we get:

• Average: 0.052364 s

• Standard deviation: 0.00201

The code doesn't run correctly as multiple threads try to access and modify the sum variable concurrently, making it susceptible to race conditions.

Implement a new version called omp\_critical\_sum and use the omp critical to protect the code region that might be updated by multiple threads concurrently. Measure the execution time for the code in questions 2 and 3 by varying the number of threads: 1, 2, 4, 8, 16, 20, 24,

28, and 32. How does the performance compare to the program in questions 1 and 2? What is the reason for the performance gain/loss?

	1	2	4	8	16	20	24	28	32
omp_sum	0.033876	0.078455	0.056979	0.054617	0.053033	0.049611	0.054719	0.050377	0.051933
$omp\_critical\_$	su0m069273	0.161543	0.173142	0.193622	0.197640	0.195231	0.191220	0.193787	0.193857

Performance is lost when using critical to protect the code as the code is essentially serialised as threads only access the sum one by one (nonetheless giving a correct result).

Try to avoid the use of a critical section. Implement a new version called omp\_local\_sum. Let each thread find the local sum in its own data, then combine their local result to get the final result. For instance, we can use temporary arrays indexed by their thread number to hold the values found by each thread, like the code below.

- double local\_sum[MAX\_THREADS];
- Measure the performance of the new implementation, varying the number of threads to 1,32,64, and 128 threads. Does the performance increase as expected? If not, why not?

As expected, this method gives the correct sum. We get the following performance depending on the amount of threads:

	1	32	64	128
omp_local_sum	0.044071	0.027366	0.015175	0.014685

The performance increases with number of threads, and is better than the serial sum.

Write a new version of the code in question 4 called opt\_local\_sum using a technique to remove false sharing with padding. Measure the performance of the code by varying the number of threads to 1, 32, 64, and 128. Using a 256 byte struct (double + 248 char padding) the following results are obtained

	1	32	64	128
omp_local_sum	$0.05\overline{2609}$	0.041504	0.027234	0.029865

There is an improvement in performance over the serial sum, but omp local sum seems to perform better overall despite not accounting for false sharing.

#### Exercise 4 - DFTW, The Fastest DFT in the West

Our method for parallelisation is the following:

```
// DFT/IDFT routine
// idft: 1 direct DFT, -1 inverse IDFT (Inverse DFT)
int DFT(int idft, double *xr, double *xi, double *Xr_o, double *Xi_o, int N) {
#pragma omp parallel for reduction(+ : Xr_o[:N]) reduction( +:Xi_o[:N])
for (int k = 0; k < N; k++) {
   for (int n = 0; n < N; n++) {
      double angle = n * k * PI2 / N;
      double cos_val = cos(angle);
      double sin_val = sin(angle);</pre>
```

```
// Real part of X[k]
    Xr_o[k] += xr[n] * cos_val + idft * xi[n] * sin_val;
    // Imaginary part of X[k]
    Xi_o[k] += -idft * xr[n] * sin_val + xi[n] * cos_val;
}

// normalize if you are doing IDFT
if (idft == -1) {
#pragma omp parallel for
    for (int n = 0; n < N; n++) {
        Xr_o[n] /= N;
        Xi_o[n] /= N;
    }
} return 1;
}</pre>
```

Making use of the OpenMP reduction statement.

Measure the performance on Dardel 32 cores reporting the average values and standard deviation for DFTW using an input size equal to 10000 (N=10000).

```
DFTW calculation with N = 10000 Mean running time across 20 runs: 0.339029 seconds Standard deviation of running time for 20 runs: 0.016917 seconds
```

Prepare a speed-up plot varying the number of threads: 1,32,64, and 128.

### Seconds vs Threads

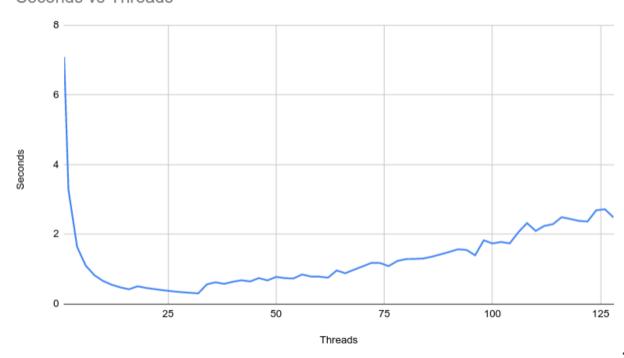


Figure 1: graph.png

Table comparing execution speed against thread count using 32 cores, varying the number of threads.

Threads	1	2	4	8	16	32	64	128
Seconds	7.080946	3.567994	1.799715	0.924253	0.47055	0.345014	1.003984	2.834436

Which performance optimizations (think about what you learned in the previous module) would be suitable for DFT other than parallelization with OpenMP? Explain, no need to implement the optimizations.

There are several suuitable optimizations

- Precomputation and lookup tables. We could precompute the values for the trigonometric functions and store them in a loopup table.
- Cache friendly memory access. We could implement cache blocking for more friendly cache access.
- Vectorization: We could implement vectorisation with SIMD instructions, which is even possible with OpenMP using the #pragma omp parallel for simd directive.