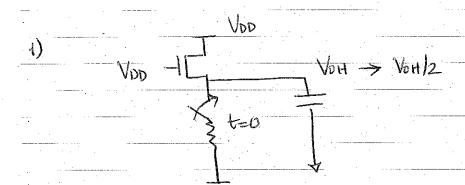
Homework #2 SOLUTIONS



This is a quadratic equation in VTI. When you solve this you get two roots for VTI. Select only the positive root as VTI for NMOS is always positive.

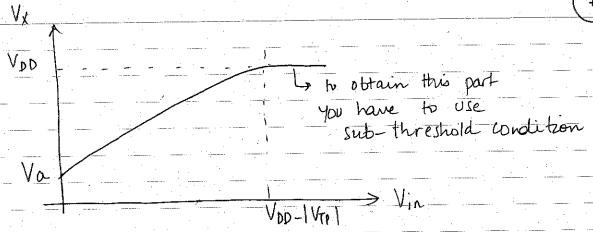
VT2 = 0,607 V

VT, aug = VT1 + VT2 = 0.671V $Ravg = \frac{1}{2} \left(Ra + Rb \right)$ @ Vou At both Vout = VoH & VoH NMOS device is in Caturation mode of operation. So resistance must be calculated using appropriate models. Ra = $\frac{V_{OU} - V_{OH}}{\frac{1}{2}} \beta_n \left(V_{G} - V_{OH} - V_{Tayg} \right)^2$ Ra = 0.735 $0.5 \times 115 \times 10^{-6} \left[0.735 - 0.671\right]^{2}$ Ra = 3.1208 XID6 SZ Notice, Ra is Very high This is to be expected since For Vout = VOH, the NMOS device is nearly in cut-off R6 = VDD - VOH/2 1 Bn (VDD - VOH - VTava)2 Rb = 25-088 $\frac{1}{2} \times 115 \times 10^{-6} \left(2.5 - 0.88 - 0.671 \right)^{2}$ Pb = 3.128 x104 -

MIQUELPHUS

Rayg = 0.5 / 3.1208 XID6 + 3.128 XID4) Rang = 1.5760 x106 sz Rt. VDD RSW Rsw + Rt ht = equivalent resistance of the transistor. Under the condition RSW << Rt Vout >0

(i) (ii) MIQUELRIUS



Va is the initial value of Vx that you can obtain for Vin = 0.

The general shape of the plot remains the same even if you were to consider body effect as long as 12p1Vx <<1.

Also, if the body and source are not tied together then the device will be under body effect.

(6) Solution without body effect: -

$$\frac{p_{p}}{2}\left(V_{x}-V_{p}\right)^{2}\left(1+1\lambda_{p}\right)V_{x}=\frac{V_{DD}-V_{x}}{R_{I}}$$

Vx = 1.5V

Bp = Kp (W/L)p

$$(WL) = \frac{2(2.5 - 1.5)}{30 \times 10^{3} \times (1.5 - 0.4)^{2} (1 + 0.1 \times 1.5)}$$



Solution w/ body effect

$$|V_{TP}| = |V_{1D}| + |\gamma| \left[\sqrt{2\phi_s} - V_{SB} - \sqrt{2\phi_s} \right]$$

$$\frac{\beta P}{2} \left(V_{X} - |V_{TP}| \right)^{2} \left(|+| \lambda P |V_{X}| \right) = \frac{V_{DD} - V_{X}}{R_{1}}$$

$$\frac{(W/L)_{p}}{30\times10^{-6}} = \frac{2(25-1.5)}{30\times10^{-6}} \times 20\times10^{-3} \left(1.5-0.5961\right)^{2} \left(1.15\right)$$

$$\left(\frac{N}{L}\right)_{p} = 3.5476$$
 with body effect.

You will receive credit for either one of those solutions.

PROBLEM #3 VDD= 2.5V_ - Vour varies T G=5PF tplH=0.69 Reg CL Note: - Vout varies from O to VDD-VT. To find the charging time, we calculate Resistance at the beginning of the transition and half way through the transition at the output vode: Reg = 0.5 (Ra + Rb) at Vour = 0 at Vour = VOD-Vr Ra = VDD 1 Bn (VDD - VTO)2 (1+) VDD) note at initial point there is no body effect. SINCE VSB = 0 Ra = $\frac{1}{2} \times \frac{115 \times 10^{-6} \times 20}{2} \left(2.5 - 0.43 \right)^{2} \left(1 + 0.06 \times 2.5 \right)$ Ra = 882. 3353 SL To calculate Rb, we will need to calculate VT

VSB = VDD-VT due to the body effect.

$$V_T = 0.43 + 0.4 \left(\frac{0.6 + 2.5 - V_T}{2} - \sqrt{0.6} \right)$$

You get a quadratic equation in 4, which you can solve for 4.

SO VOD-VT = 8/6 2.5-0.6167 = 1.883 V

$$Rb = V_{DD} - \left(\frac{V_{DD} - V_{T}}{2}\right)$$

$$\frac{1}{2} \operatorname{Bn} \left(V_{DD} - \frac{V_{DD} - V_T}{2} - V_T \right)^2 \left(1 + \lambda \left(\frac{V_{DD} + V_T}{2} \right) \right)$$

$$R_6 = 2.5 - 1.883/2$$

 $\frac{2.5 - 1.883}{1.5583} = \frac{1 \times 115 \times 10^{-6} \times 20}{2} \left(2.5 - \frac{1.983}{2} - 0.616 + \frac{1}{2} \right) \left(\frac{1 + 0.06 \times 1.883}{1.5583} \right)$

 $R_3 = \frac{1.5585}{5.385 \times 10^{-4}}$ 28942 S

$$Req = \frac{1}{2} \left(Ra + Rb \right) = \frac{1}{2} \left(\frac{882 \cdot 3}{2794.5} \right) = \frac{1838.4}{2794.5}$$

tp LH = 0.69 Reg CL



(b) If the load capacitor scales with device width delay is Unaffected

Since Reg x /W while CL X W

tplH => constant with width

(c) tpHL = 0.69 x5 kn x 5pF = 17.25 ns

(d) If the NFET is replaced with PFET, we compute its equivalent resistance as the output charges from Vout = 0 to Vout = Voo . Note, for PFET, the OIP goes all the way up to Voo.

Hence, we calculate the equivalent resistance at Vout = 0 & Vout = Voo/2

In class, we obtained the equivalent resistance of the PFET device as

$$\Gamma_{DSat} = \frac{1}{2} \times \beta_{p} \times \left(V_{DD} - |V_{TP}| \right)^{2}$$



Now, it is mentioned that $\beta p = \beta n$

$$\beta p = 115 \times 10^{-6} \times \left(\frac{20}{2}\right)$$

Insat =
$$\frac{1}{2} \times 115 \times 10^{-6} \times \frac{20}{2} \times \left(2.5 - 0.4\right)^{2}$$



IDSUt = 25x10-3A

 $R_{4} = \frac{3}{4} \times \frac{2.5}{2.5 \times 10^{-3}} = 750.0$

tplH = 0.69× 750 × 5×10-12 = 2.58 75 ns

Hence, in this case the PFET circuit will be faster since we have the same Bp=Fn.

Note:- You will receive credit even if you haven't quantified how much faster PFET charging is, if you have qualitatively explained the solution.

Qualitatively the circuit becomes faster because
the PFET sees an overall larger turn on" as
opposed to the NFET. The larger drop happens
because |VTP| < VTN. And second PMOS
has a good pull-up to "Is" SID the capacitor
can go all the way up to Voo.

Both of these effects combined help to lower the equivalent resistance of IMDS and hence the delay For PMOS will be lower than that for MOS.

| Common mistakes |
|---|
| Problem 3a) When you want to calculate equivalent resistance, You need 'V7' of the middle point of the |
| transition at the output node. |
| If the old node goes from 0 to VDD-VT" |
| then you ned to calculate |
| VT From D to VDD-VT as the swing. |
| Also the definition of equivalent vesistance is |
| 1 (Ra + Rb) |
| |
| initial half way through the transition |
| $R_{6} = V_{DS}$ |
| |
| $\frac{1}{2}$ β $\left(V_{GS}-V_{T}\right)^{2}$ $\left(1+\lambda V_{DS}\right)$ |
| Note Vas will be VIN - (VDD-VT) |
| Voc 1911 be V |
| Vos vill be Vod-(Vod-Vr) |
| |
| Several students did not use the night |
| values g Vos and Vers |
| Several students calculated VT incorrectly. |
| But if you understood the basic concept & show steps, you will receive credit for it. |