

## 3:8 Decoder

### Source Code And Output

ISE Project Navigator (P.20131013) - F:\Smit SIT\College\Google Classroom\Third year\Sem 5\VLSI\Lab\Lab4\exp4\exp4.xise - [exp4.v]

File Edit View Project Source Process Tools Window Layout Help

Design

View: Implementa Simul

Hierarchy

- exp4
  - xc3s400-5pq208
    - exp4 (exp4.v)

No Processes Running

Processes: exp4

  - Design Summary/Re...
  - Design Utilities
  - User Constraints
  - Synthesize - XST
    - View RTL Schem...
    - View Technology...
    - Check Syntax
    - Generate Post-Sy...
  - Implement Design
  - Generate Programmi...

```
9 // Project Name:
10 // Target Devices:
11 // Tool versions:
12 // Description:
13 //
14 // Dependencies:
15 //
16 // Revision:
17 // Revision 0.01 - File Created
18 // Additional Comments:
19 //
20 //////////////////////////////////////////////////
21 module exp4(
22     input [2:0]s,
23     output [7:0]y
24 );
25 assign y[0]=(~s[2]&~s[1]&~s[0]);
26 assign y[1]=(~s[2]&~s[1]&s[0]);
27 assign y[2]=(~s[2]&s[1]&~s[0]);
28 assign y[3]=(~s[2]&s[1]&s[0]);
29 assign y[4]=(s[2]&~s[1]&~s[0]);
30 assign y[5]=(s[2]&~s[1]&s[0]);
31 assign y[6]=(s[2]&s[1]&~s[0]);
32 assign y[7]=(s[2]&s[1]&s[0]);
33 endmodule
34
```

exp4.v exp4 (RTL1)

ISim (P.20131013) - [Default.wcfg]

File Edit View Simulation Window Layout Help

Instance... Simulation Ob...

Instance and Proces:

- exp4
- gbl

Object Name:

- y[7:0]

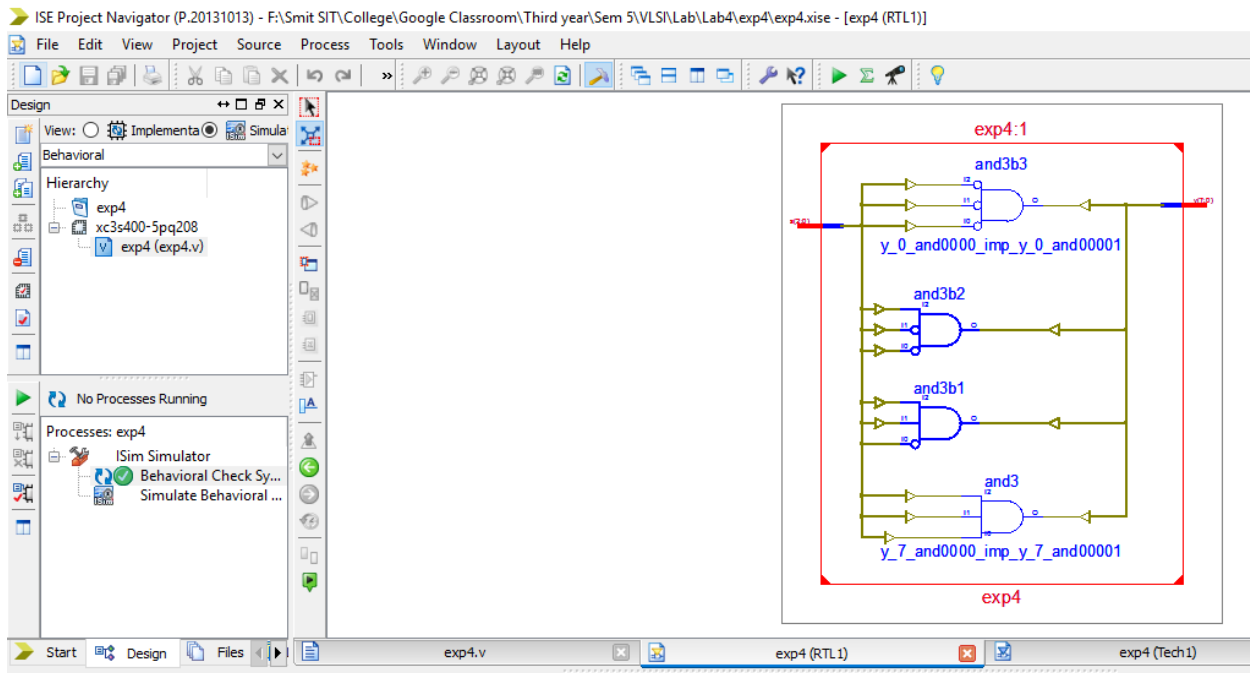
Name	Value
s[2:0]	000
y[7:0]	00000001

1,164,464,080 ps

X1: 1,164,464,080 ps

Default.wcfg

# RTL Schematic



# Technology Schematic

