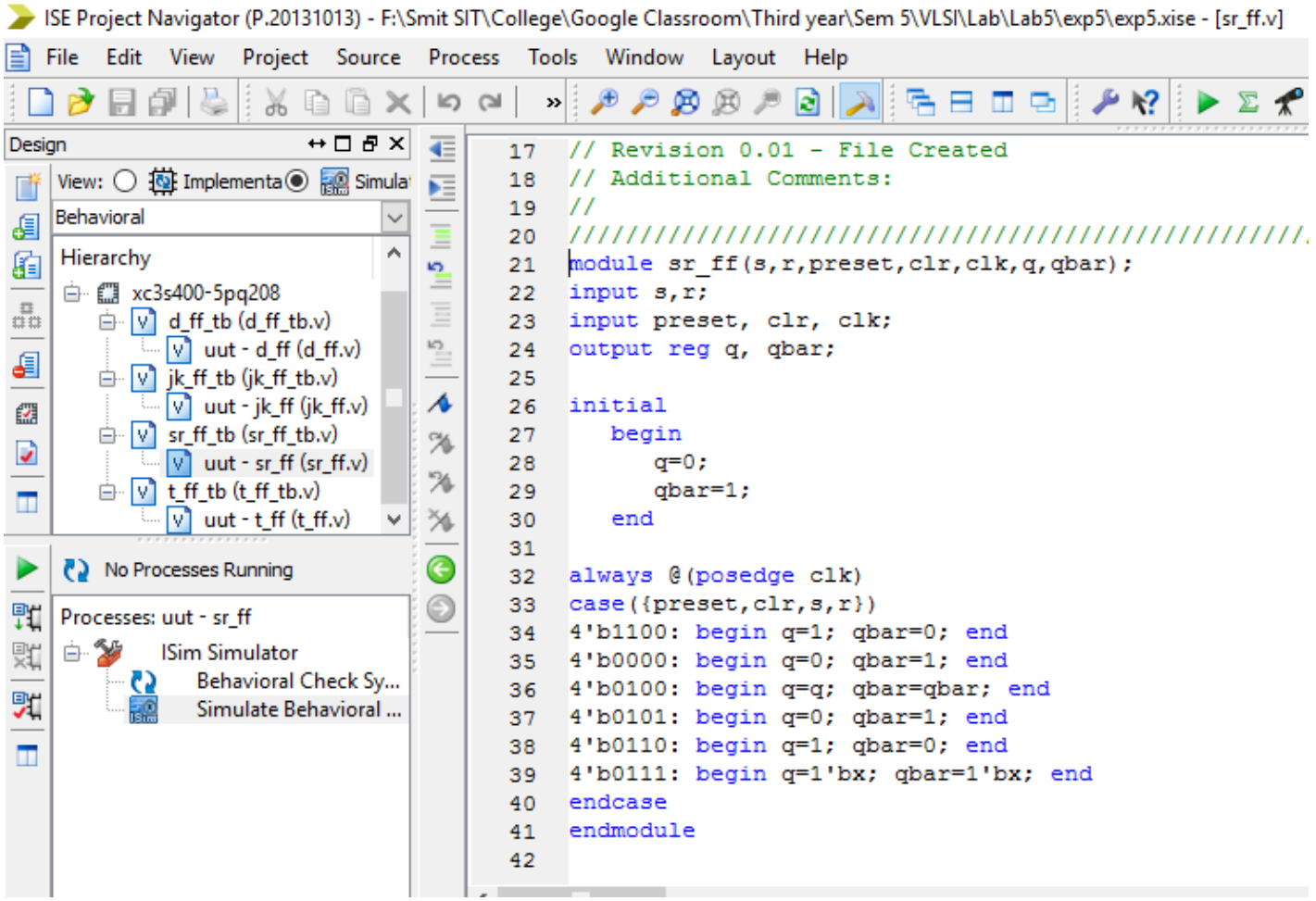
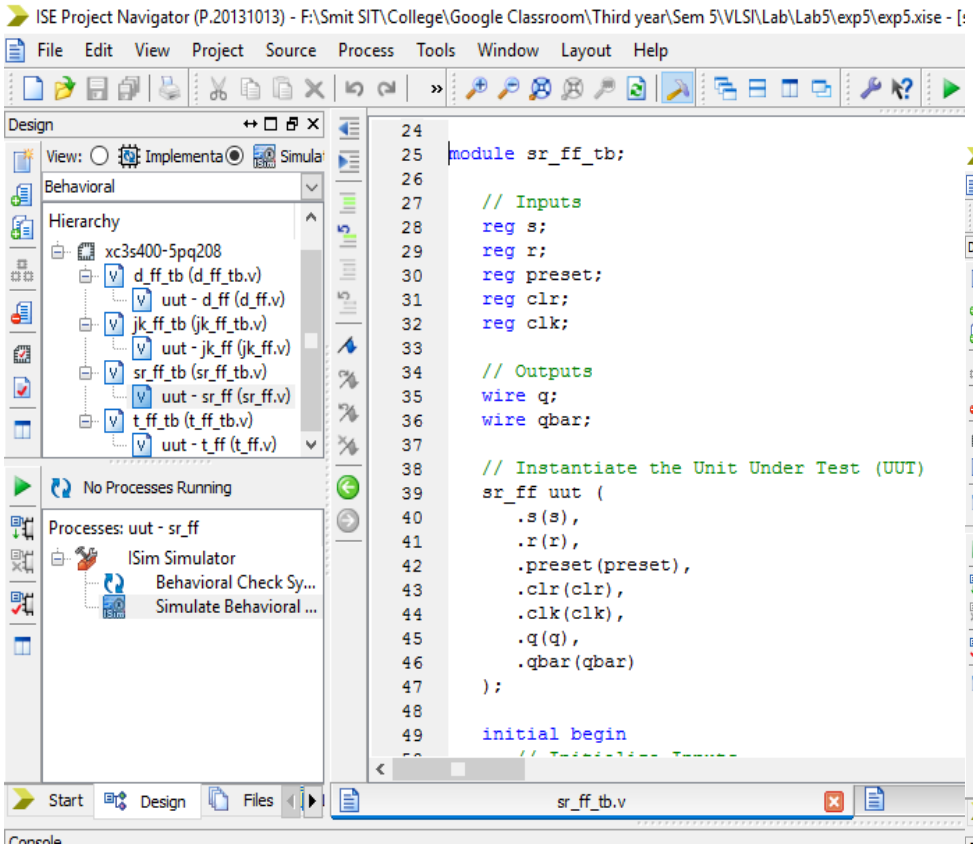
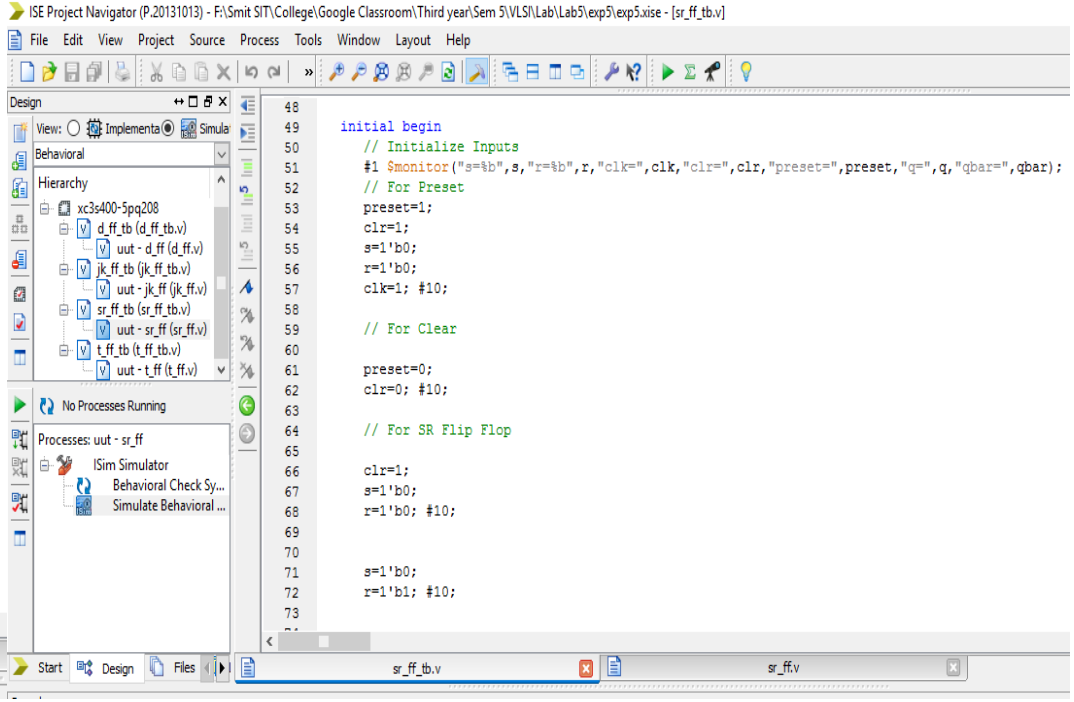
SR Flip Flop

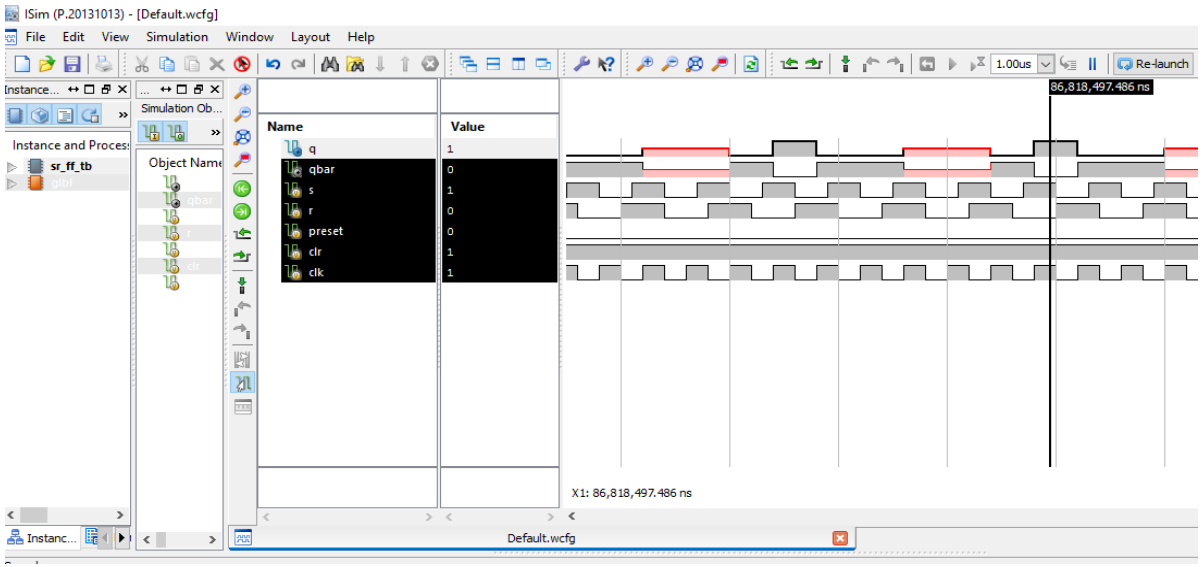
Source Code, Test Bench And Output



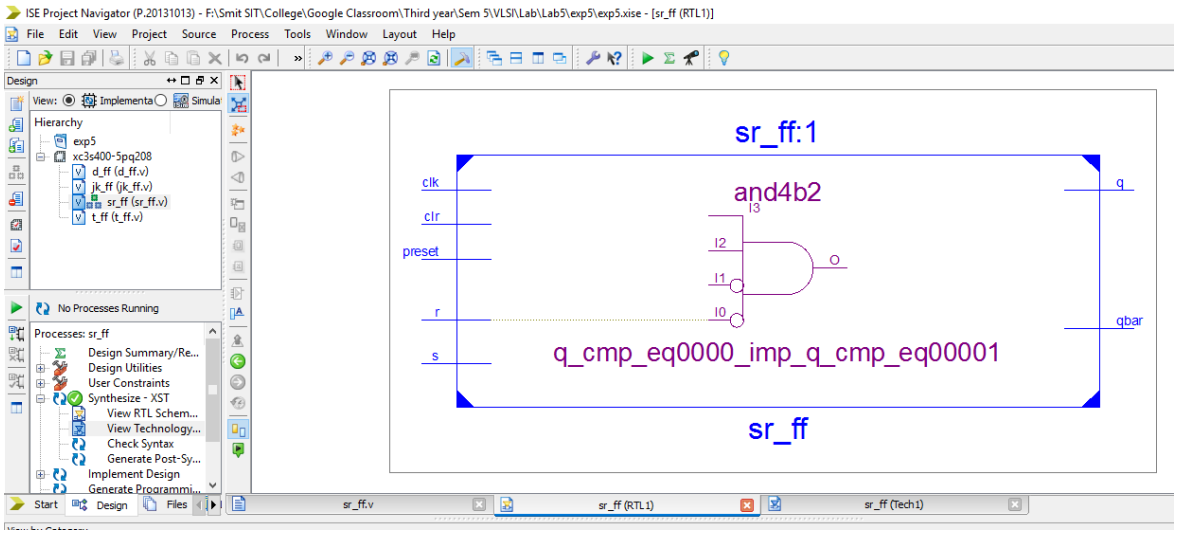





```
74
75        s=1'b1;
76        r=1'b0; #10;
77
78
79        s=1'b1;
80        r=1'b1; #10;
81  end
82  always #10 clk=~clk;
83  endmodule
84
85
```



RTL Schematic of SR Flip Flop



Technology Schematic of SR Flip Flop