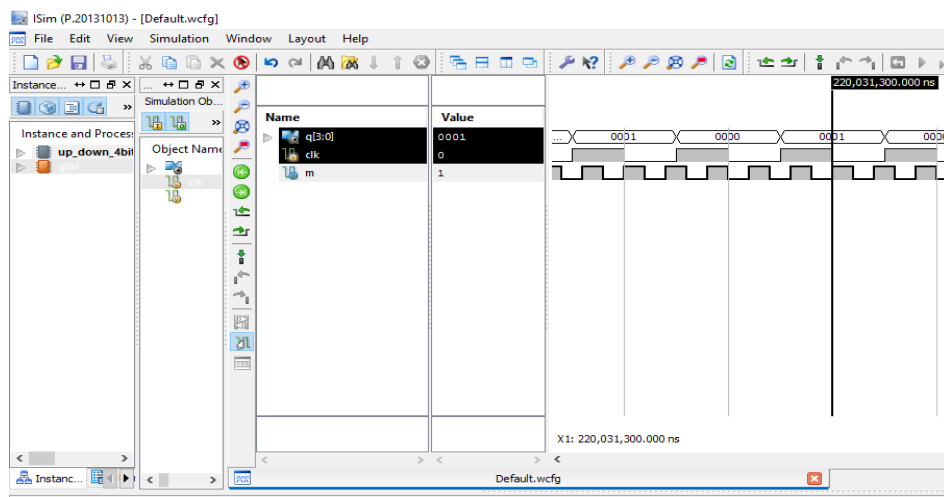
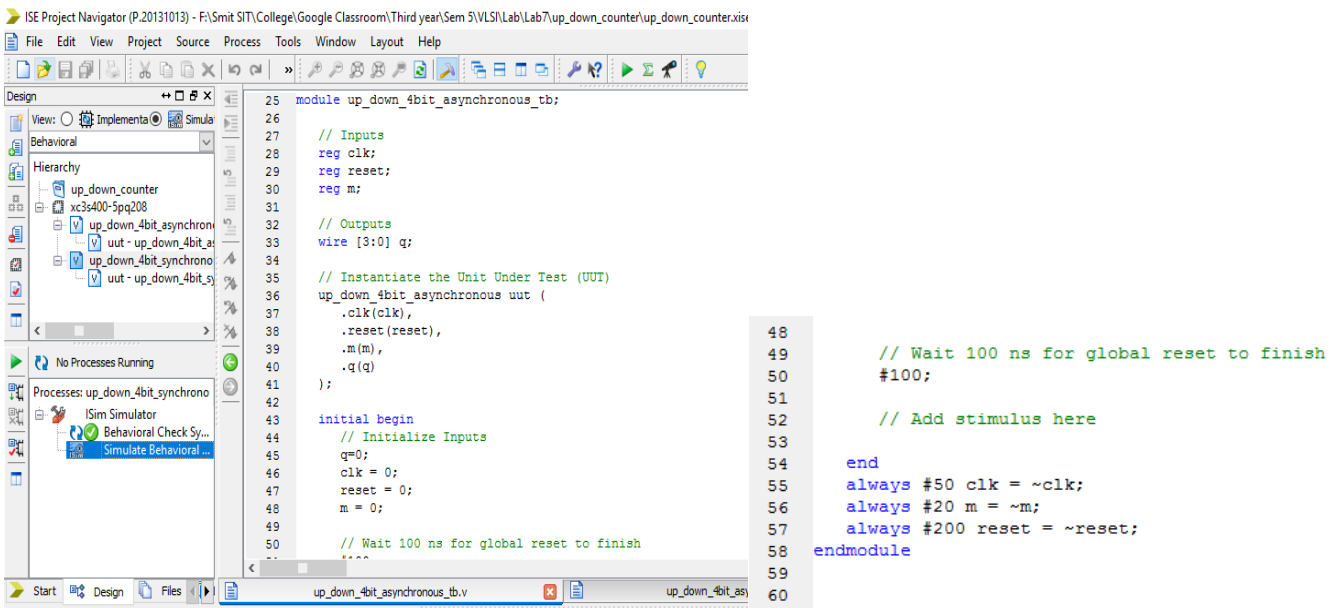
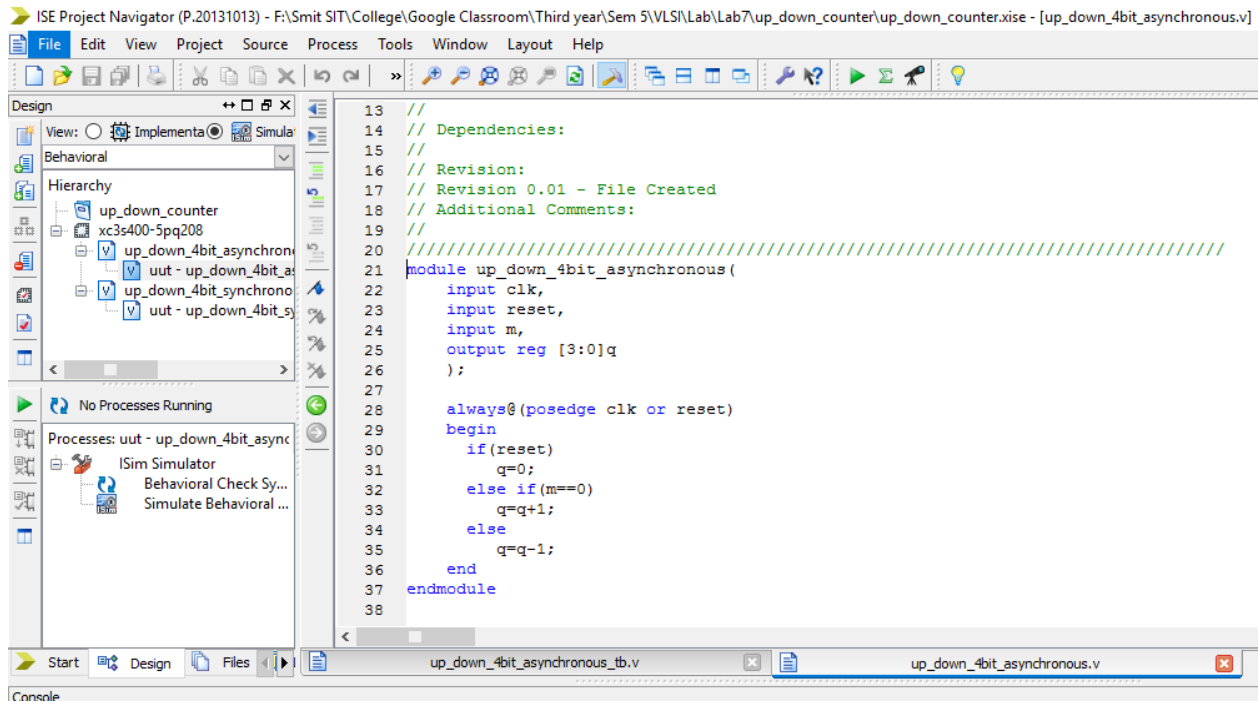
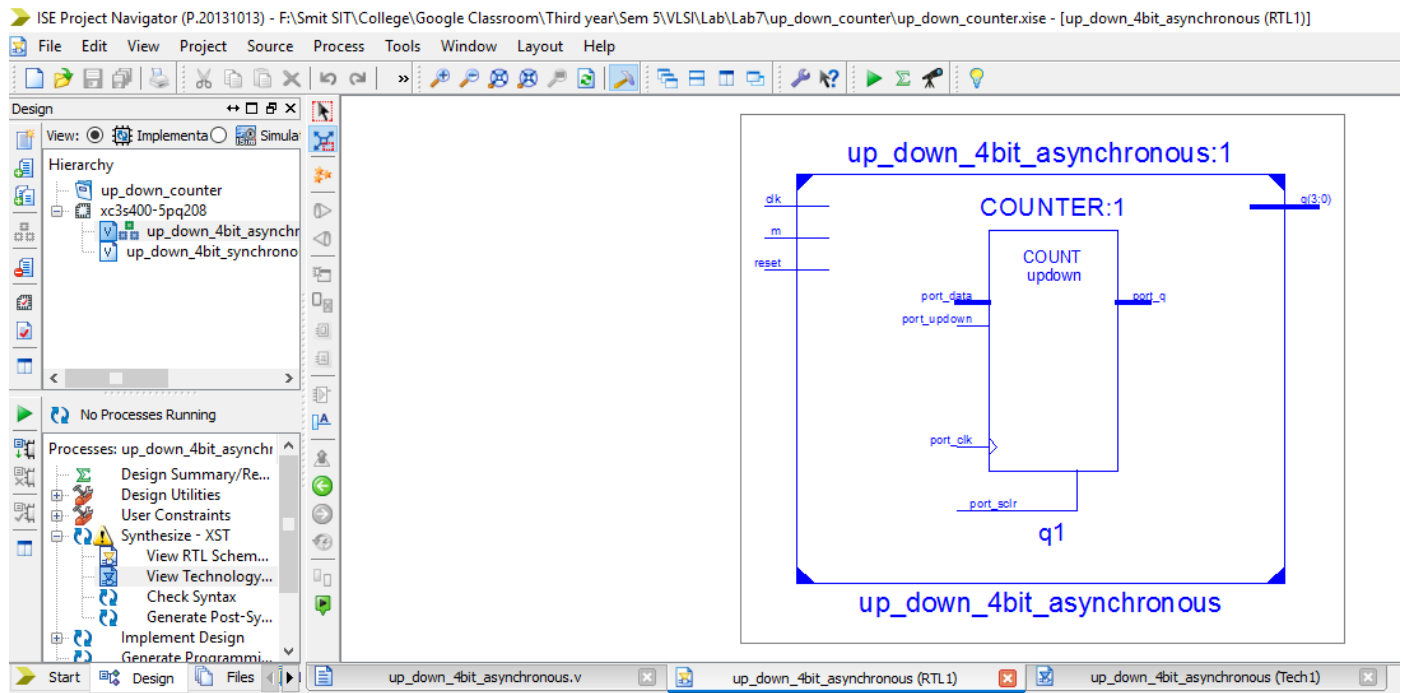


Asynchronous 4-bit Up-Down Counter

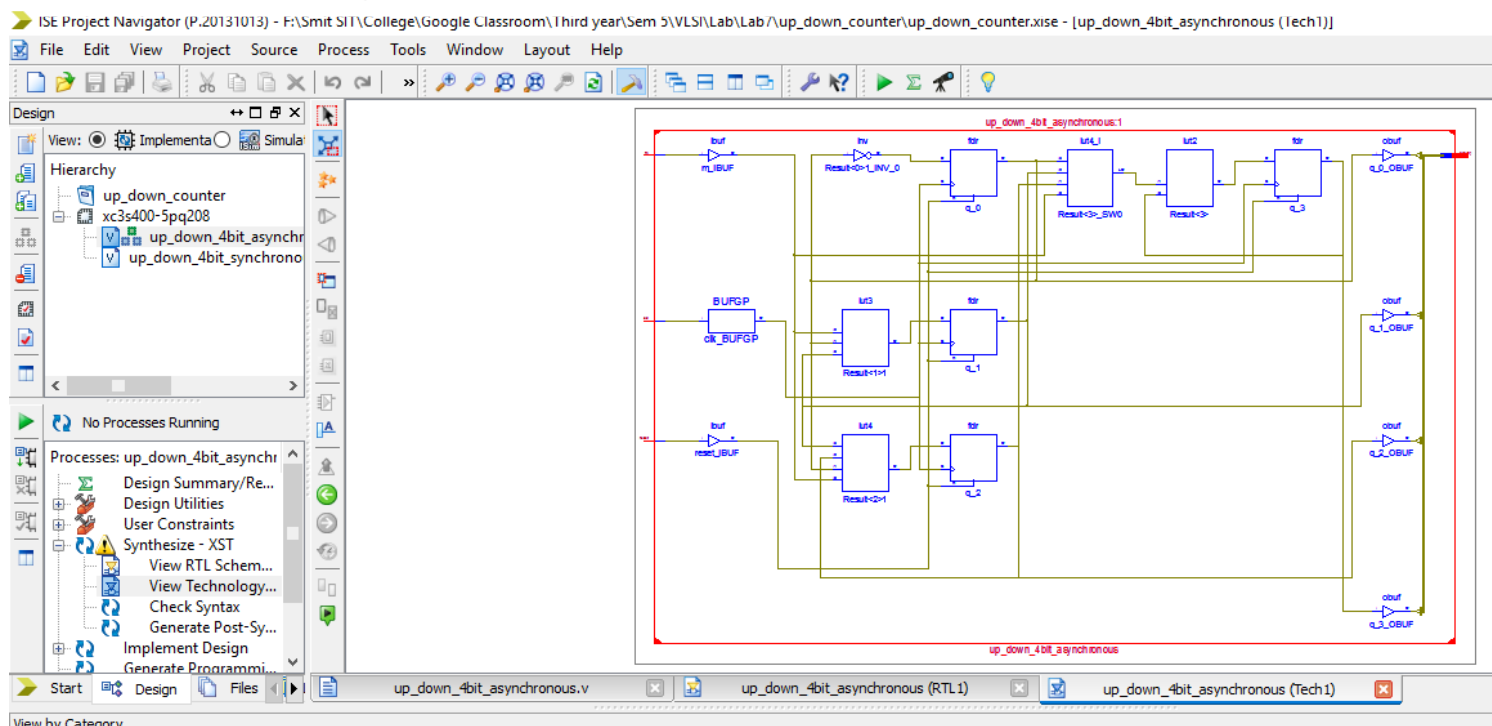
Source Code, Test Bench And Output



RTL Schematic of Asynchronous Counter

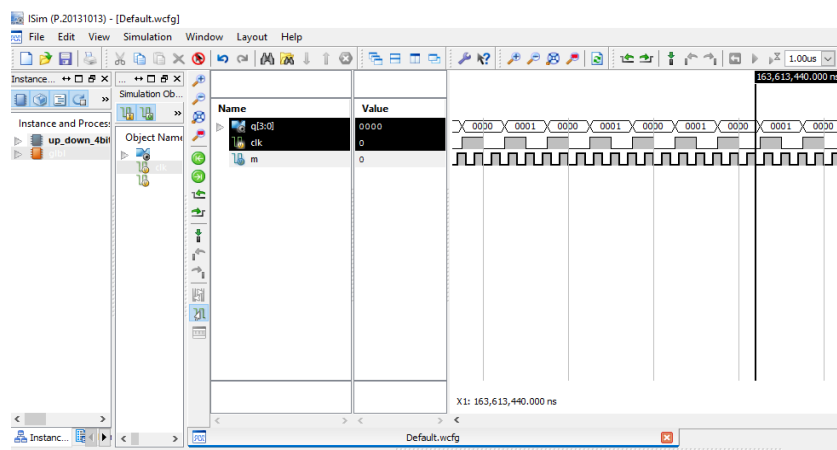
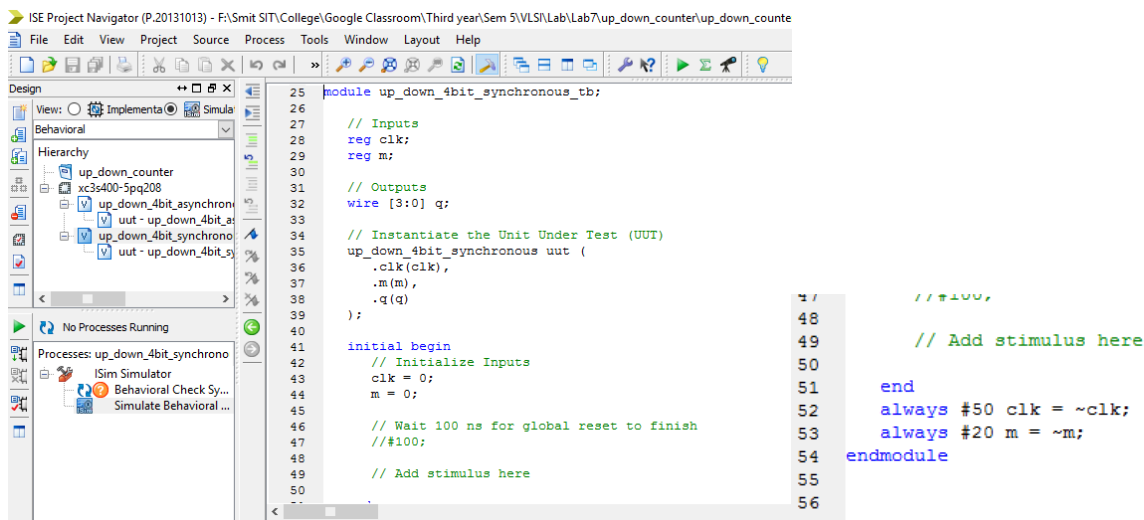
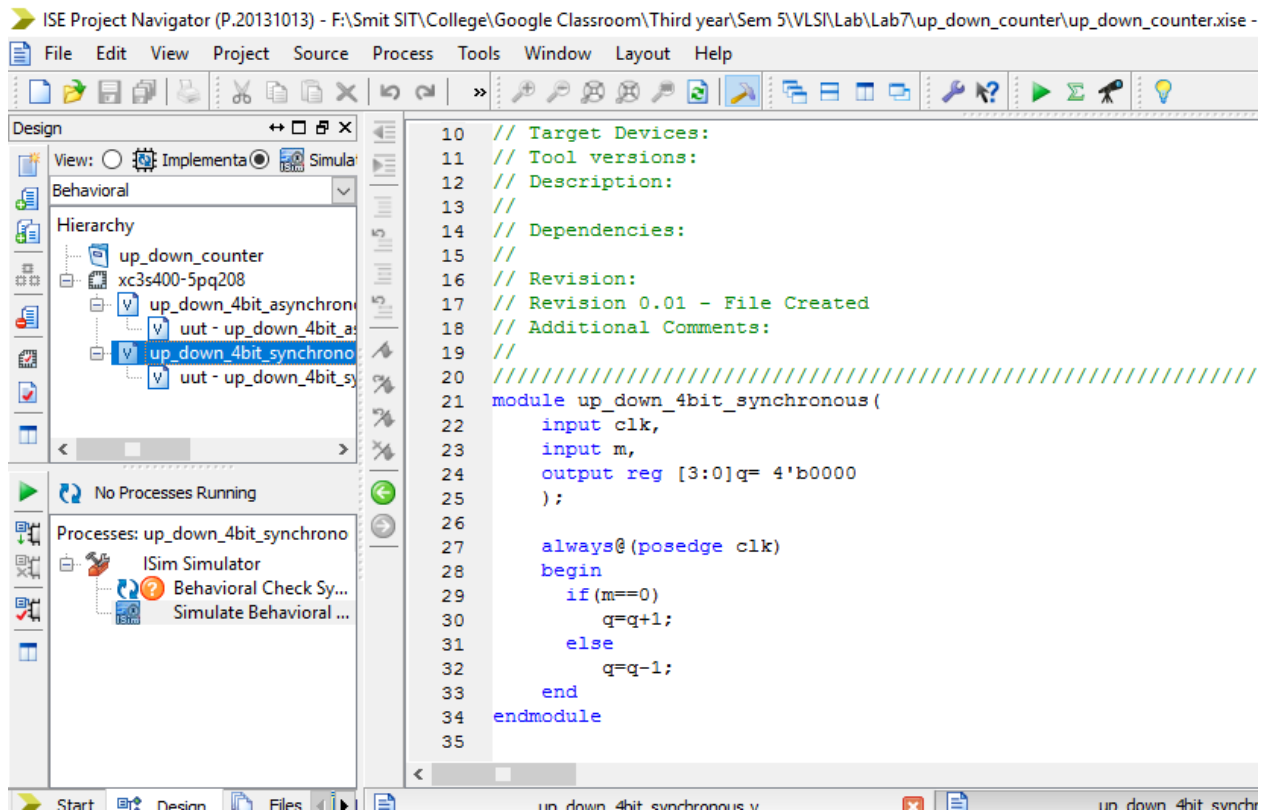


Technology Schematic of Asynchronous Counter

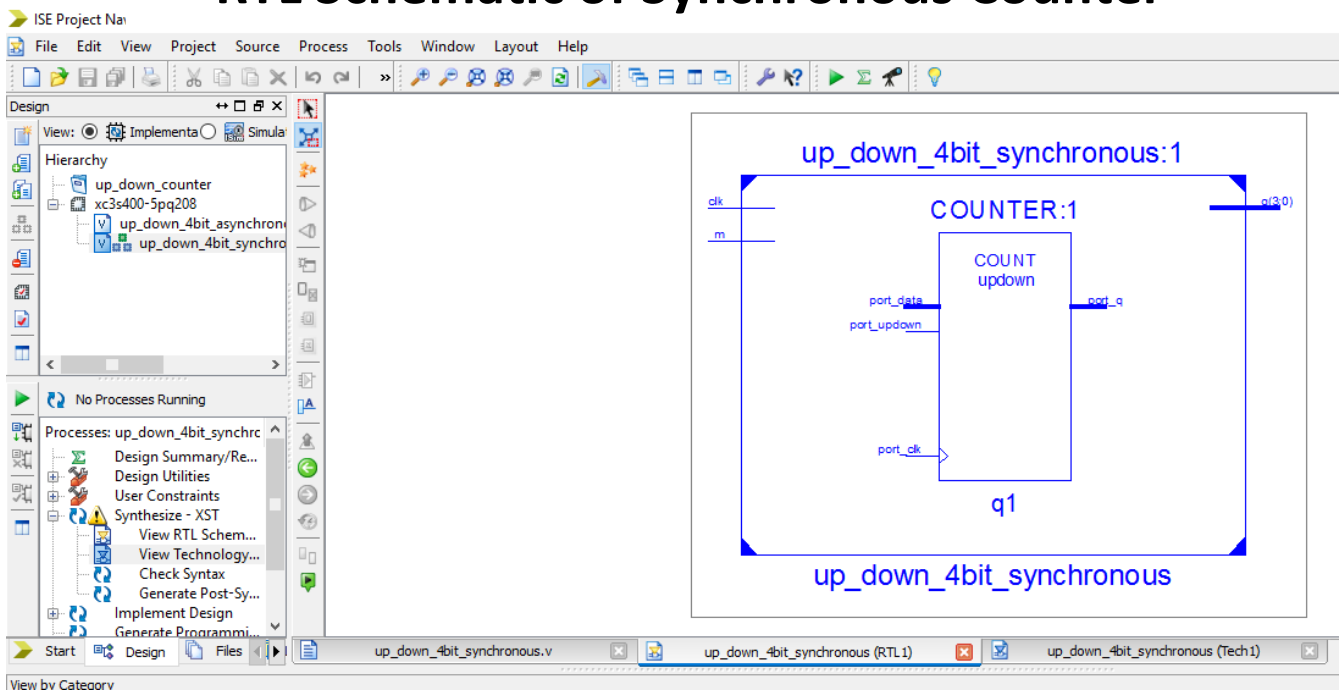


Synchronous 4-bit Up-Down Counter

Source Code, Test Bench And Output



RTL Schematic of Synchronous Counter



Technology Schematic of Synchronous Counter

