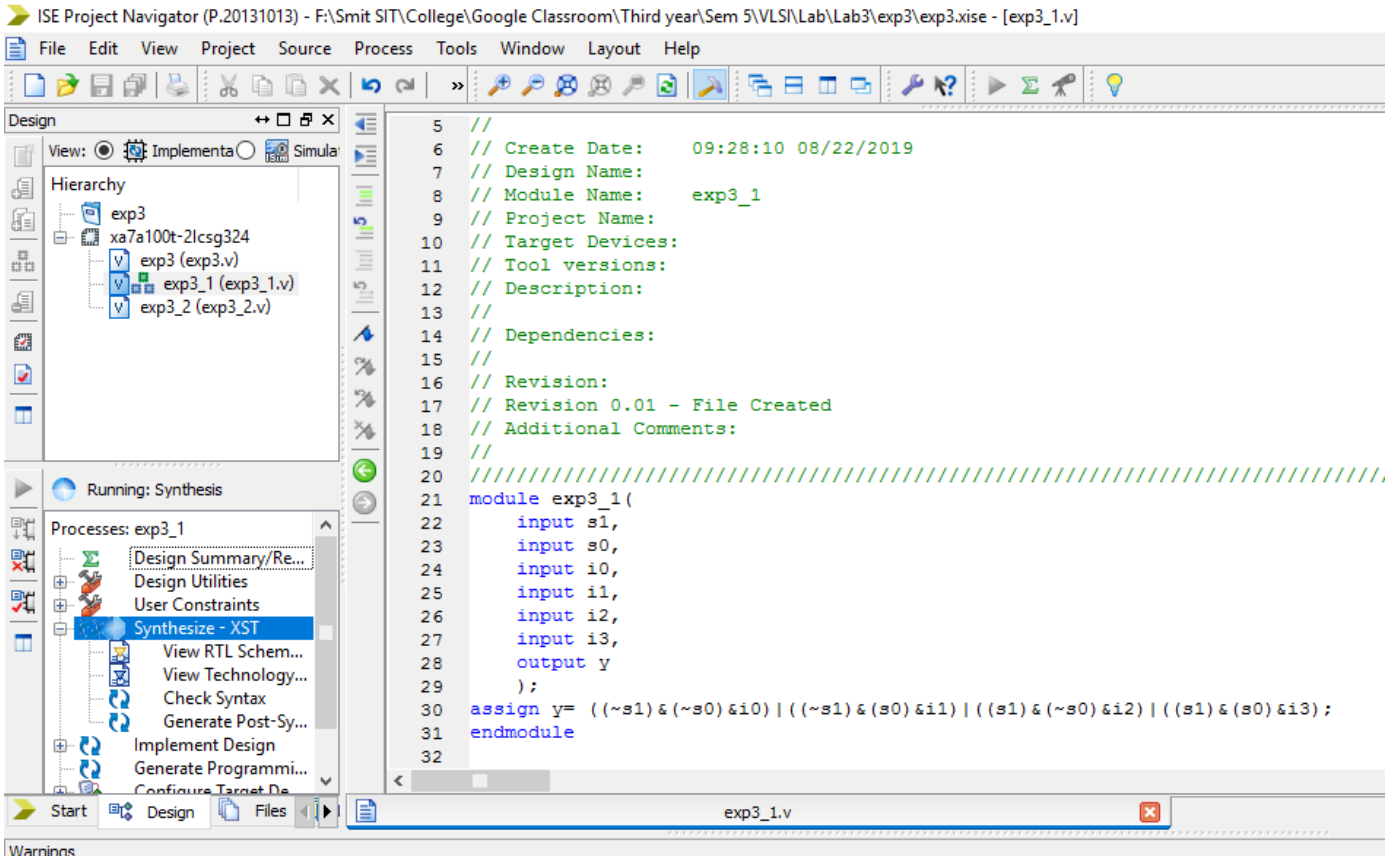
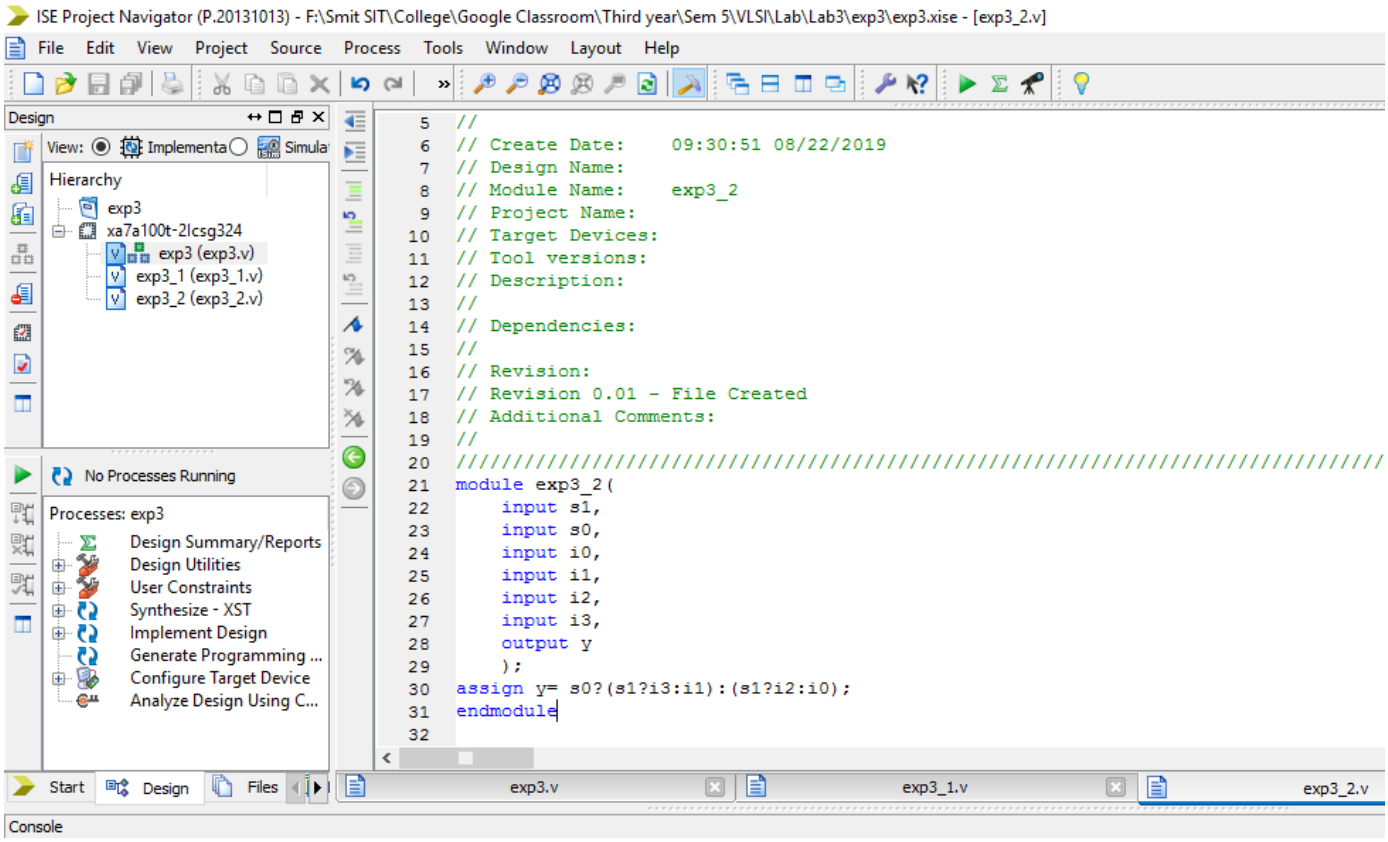
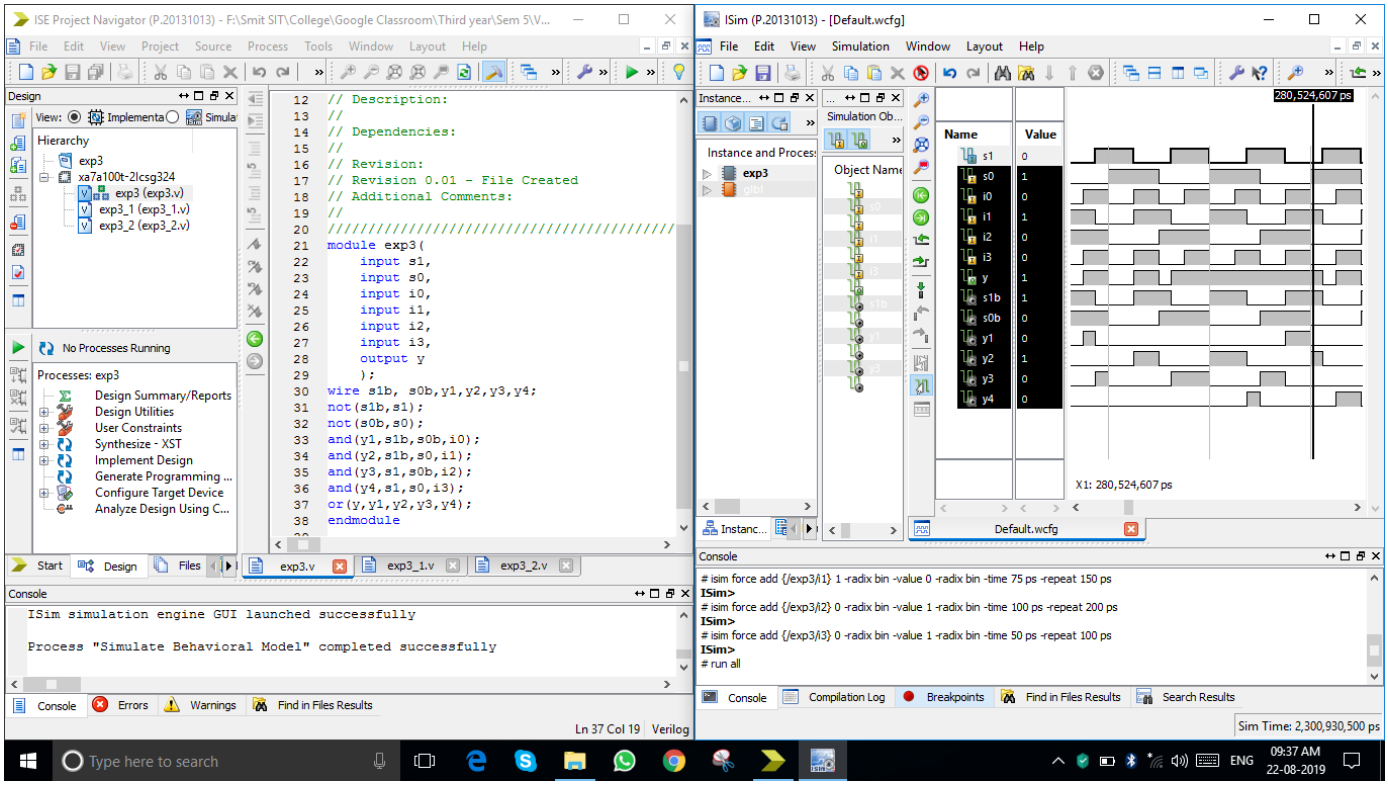
4:1 Multiplexer

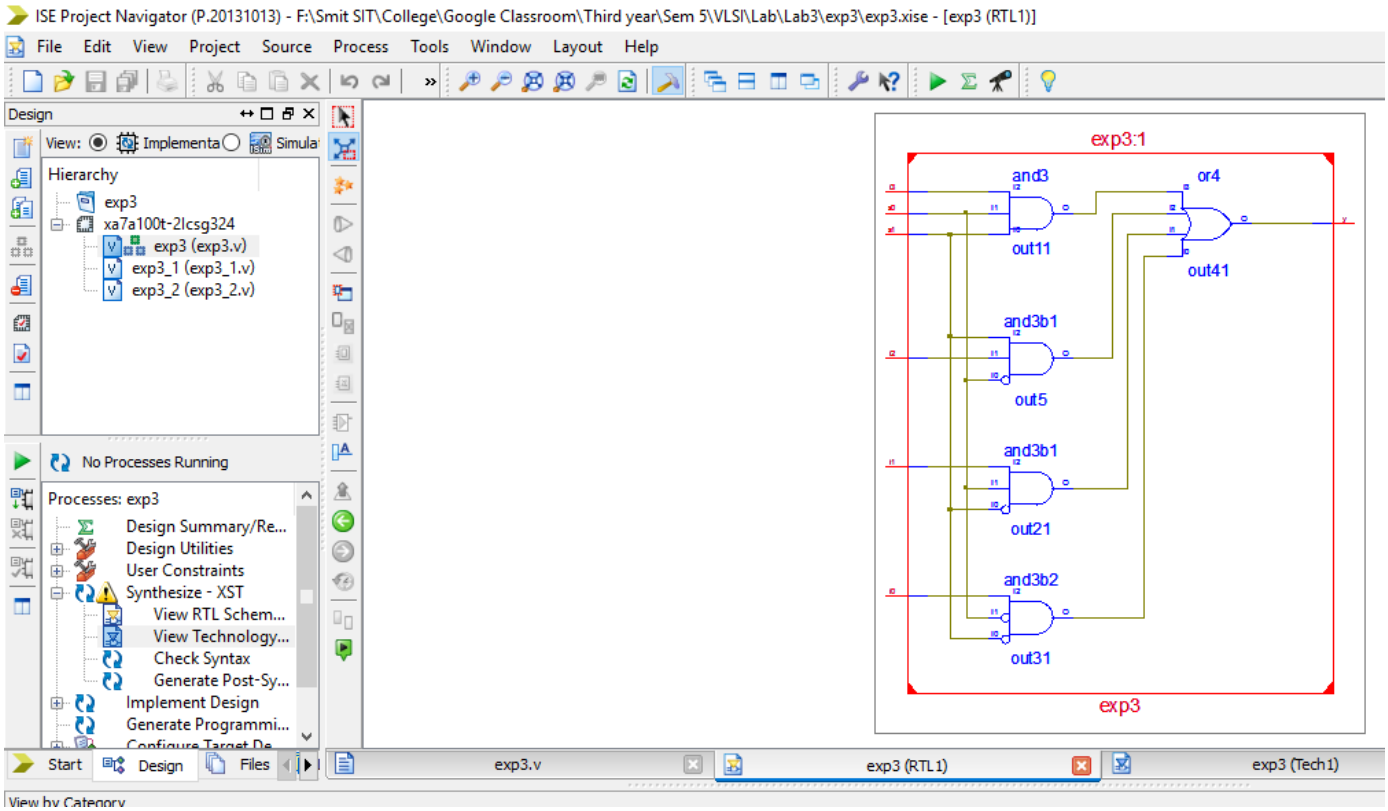
Source Code And Output



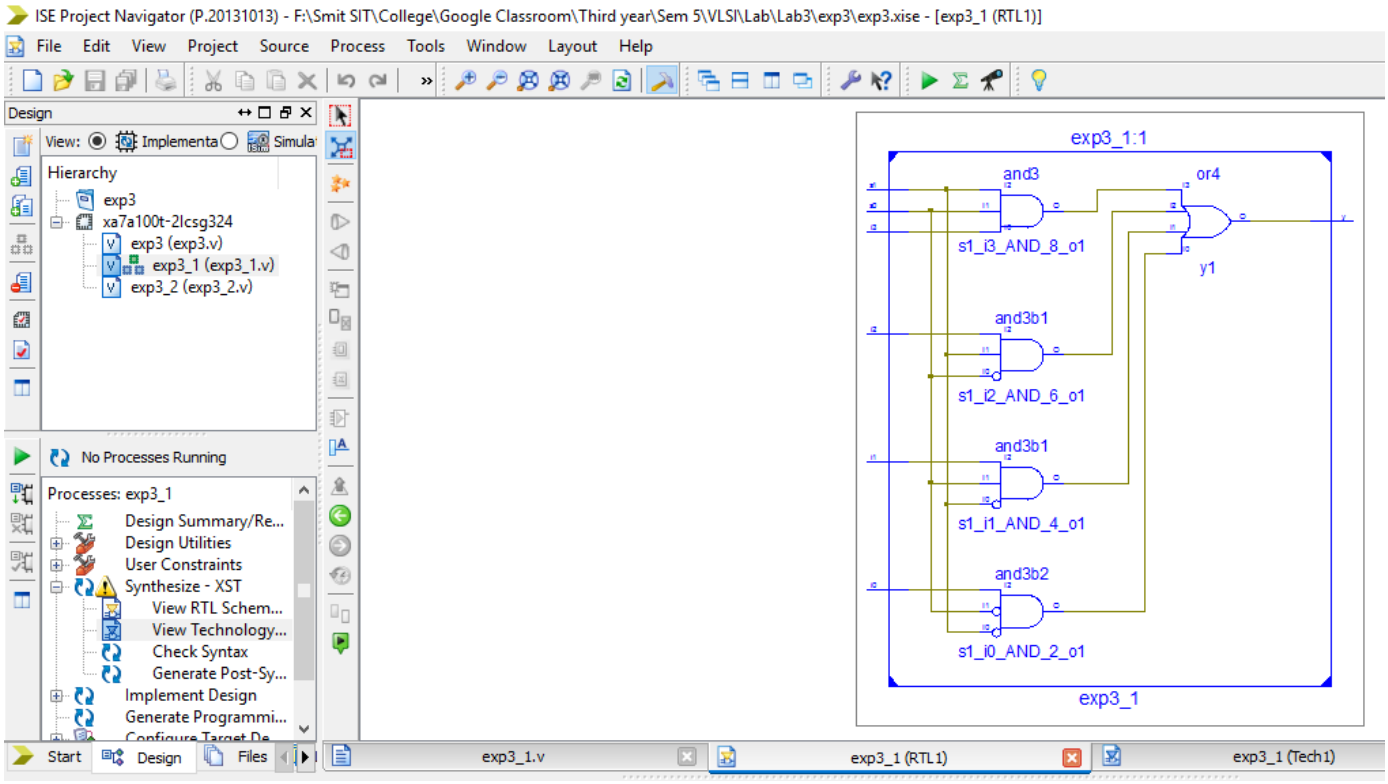




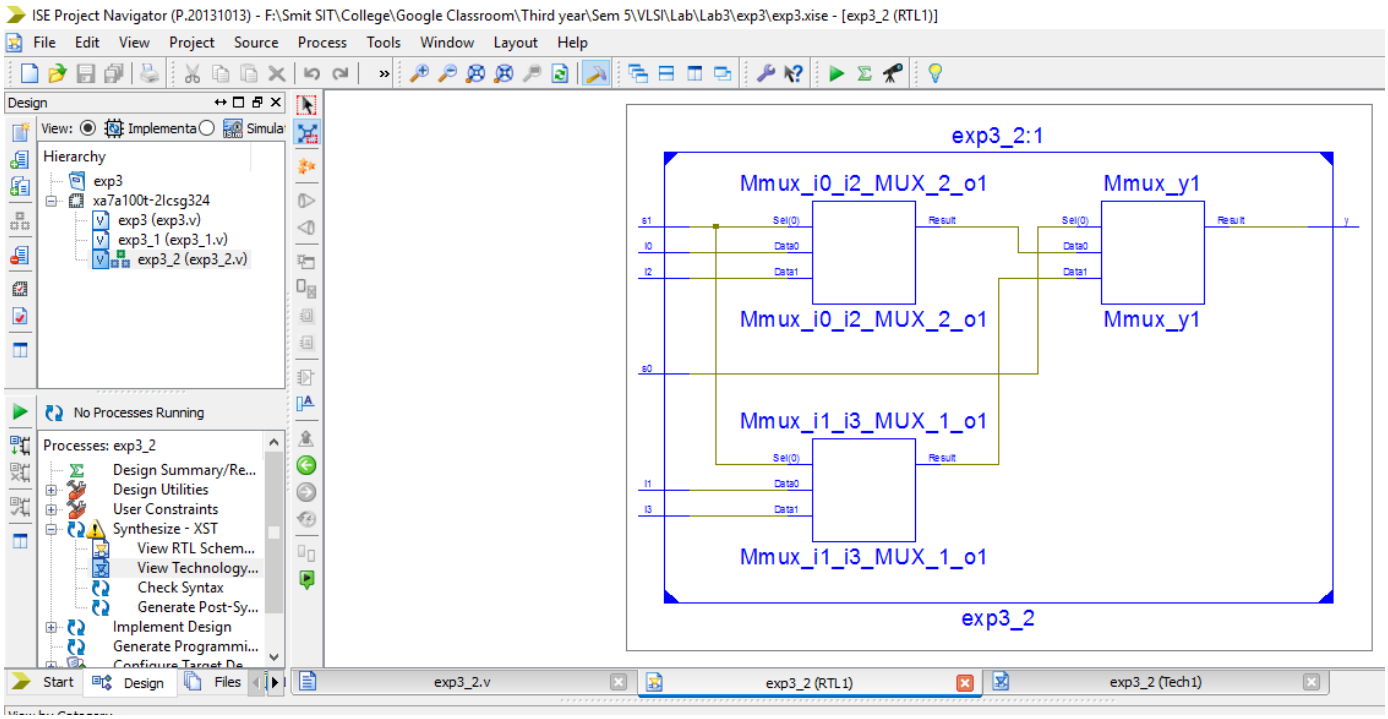
RTL Schematic with Gate Level Modelling



RTL Schematic with Data Flow Modelling



RTL Schematic with Assignment Operator



Technology Schematic