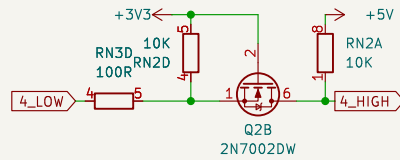
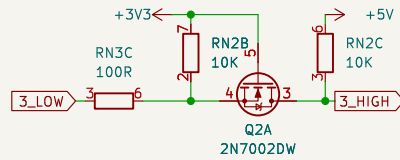
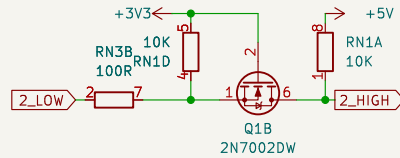
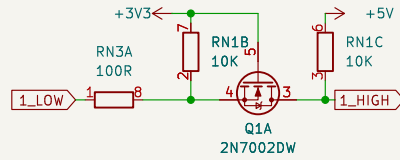
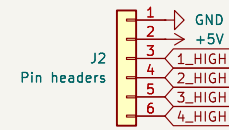
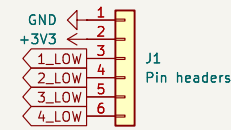


Logic level translation



Pins and connectors



PWR_FLAG → +5V
PWR_FLAG → +3V3
PWR_FLAG → GND



Sheet: /
File: level-shifter-jig.kicad_sch

Title:

Size: A4 Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 1/1