Note: This datasheet may be out of date.

Please download the latest datasheet of CSTCE16M0V53-R0 from the official website of Murata Manufacturing

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CSTCE16M0V53-R0





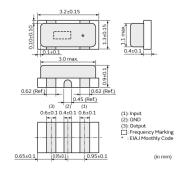






Appearance & Shape







MURATA's package technology expertise has enabled the development of the Chip CERALOCK(R) with built-in load capacitors.

High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available for a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.



Other Usage

Consumer/Industrial



Packaging Information

Packaging	Specifications	Minimum Order Quantity
R0	180mm Embossed Tape	3000

1 of 2

Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering



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Last updated : 2018/08/01



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CSTCE16M0V53-R0



Shape	SMD
Frequency	16.000MHz
Frequency Tolerance	±0.50%
Operating Temperature Range	-20℃ to 80℃
Frequency Shift by Temperature	±0.30%
Frequency Aging	±0.20%
Wash	Not available
Resonant Impedance	40Ω
Built-in Load Capacitance	15pF

2 of 2

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