

Instruction	Format	Opcode	Fn	RegDst	RegWrite	ALUSrc	ALUOp	MemWrite	MemRead	MemToReg
add	R	0x0	0x20	1	1	0	[10]	0	0	0
sub	R	0x0	0x22	1	1	0	[10]	0	0	0
sll	R	0x0	0x0	1	1	0	[10]	0	0	0
srl	R	0x0	0x2	1	1	0	[10]	0	0	0
and	R	0x0	0x24	1	1	0	[10]	0	0	0
nor	R	0x0	0x27	1	1	0	[10]	0	0	0
jr	R	0x0	0x8	X	0	X	[10]	0	0	0
slt	R	0x0	0x2A	1	1	0	[10]	0	0	0
sltu	R	0x0	0x2B	1	1	0	[10]	0	0	0
addi	I	0x8	-	0	1	1	[00]	0	0	0
lw	I	0x23	-	0	1	1	[00]	1	0	1
lb	I	0x20	-	0	1	1	[00]	1	0	1
lbu	I	0x24	-	0	1	1	[00]	1	0	1
sw	I	0x2B	-	X	0	1	[00]	0	1	0
sb	I	0x28	-	0	0	1	[00]	0	1	0
lui	I	0xF	-	0	1	1	[00]	0	0	0
beq	I	0x4	-	X	0	1	[01]	0	0	0
bne	I	0x5	-	X	0	1	[01]	0	0	0
j	J	0x2	-	0	0	X	??	0	0	0
jal	J	0x3	-	0	1	X	??	0	0	0