

Parallelism and Concurrency

Parallelism: Evaluate on multiple processors to speed up computation
Concurrency: Use multiple threads sharing resources (may/may not be parallel)

$$e ::= \dots \mid e \parallel e$$

$$\frac{\Gamma \vdash e_1 : \tau_1 \quad \Gamma \vdash e_2 : \tau_2}{\Gamma \vdash e_1 \parallel e_2 : \tau_1 \times \tau_2}$$

$$\frac{e_1 \mapsto e_1'}{e_1 \parallel e_2 \mapsto e_1' \parallel e_2}$$

$$\frac{e_2 \mapsto e_2'}{e_1 \parallel e_2 \mapsto e_1 \parallel e_2'}$$

"interleaving"

$$\frac{e_1 \text{ val} \quad e_2 \text{ val}}{e_1 \parallel e_2 \mapsto (e_1, e_2)}$$

Why not $\frac{e_1 \mapsto e_1' \quad e_2 \mapsto e_2'}{e_1 \parallel e_2 \mapsto e_1' \parallel e_2'}$?

$$\begin{aligned} 1+2 \parallel 3+4 &\mapsto 3 \parallel 3+4 \mapsto 3 \parallel 7 \\ &\mapsto 1+2 \parallel 7 \mapsto 3 \parallel 7 \end{aligned} \quad \begin{array}{l} \text{Same answer!} \\ \text{(always true for STLC)} \end{array}$$

$$\frac{e_1 \Downarrow v_1 \quad e_2 \Downarrow v_2}{e_1 \parallel e_2 \Downarrow (v_1, v_2)} \quad \text{can't capture diff. interleavings but that's OK.}$$

"Nested" parallelism

fix fib = $\lambda n.$ if $n \leq 1$ then n

else

let $p = \text{fib } (n-1) \parallel \text{fib } (n-2)$

in

$(\text{fst } p) + (\text{snd } p)$

What about IMP?

$s ::= x := e \mid \text{if } e \text{ then } s \text{ else } s' \mid \text{while } e \text{ do } s \text{ od}$
 $\mid s_1 ; s_2 \mid \text{skip} \mid s \parallel s'$

$$\frac{\langle s_1, \sigma \rangle \mapsto \langle s_1', \sigma' \rangle}{\langle s_1 \parallel s_2, \sigma \rangle \mapsto \langle s_1' \parallel s_2, \sigma' \rangle}$$

$$\frac{\langle s_2, \sigma \rangle \mapsto \langle s_2', \sigma' \rangle}{\langle s_1 \parallel s_2, \sigma \rangle \mapsto \langle s_1 \parallel s_2', \sigma' \rangle}$$

$$\langle \text{skip} \parallel \text{skip}, \sigma \rangle \mapsto \langle \text{skip}, \sigma \rangle$$

$$\langle x := x+1 \parallel x := x+2, \{x=1\} \rangle \mapsto^* \langle \text{skip} \parallel x := x+2, \{x=2\} \rangle \mapsto^* \langle \text{skip}, \{x=4\} \rangle$$

$$\mapsto^* \langle x := x+1 \parallel \text{skip}, \{x=2\} \rangle \mapsto^* \langle \text{skip}, \{x=3\} \rangle \quad \text{!}$$

Except there are also more!

$$\langle x := x+1 \parallel x := x+2, \{x=1\} \rangle$$

$$\mapsto \langle x := 1+1 \parallel x := x+2, \{x=1\} \rangle$$

$$\mapsto \langle x := 1+1 \parallel x := 1+2, \{x=1\} \rangle$$

$$\mapsto^* \langle x := 2 \parallel x := 2, \{x=1\} \rangle$$

$$\mapsto^* \langle \text{skip}, \{x=2\} \rangle$$

$$\langle \text{while } x \text{ do skip od} \parallel x := 1, \{x=0\} \rangle$$

$\mapsto^* \dots$

or $\mapsto^* \dots$ (forever)

$$\mapsto^* \langle \text{while } x \text{ do skip od} \parallel \text{skip}, \{x=1\} \rangle$$

$$\mapsto^* \langle \text{skip}, \{x=1\} \rangle$$

A more realistic model

$s ::= \dots \mid x := \dots \mid \text{spawn } s \mid \text{wait}(a)$
 $e ::= \dots \mid a$
 \uparrow
 thread "name"

a fresh \nwarrow not used before

$\langle \text{spawn } s, \sigma \rangle \mapsto \langle \text{skip}, \sigma[a \mapsto s] \rangle$

$\sigma(a) = \text{skip}$

$\langle \text{wait}(a), \sigma \rangle \mapsto \langle \text{skip}, \sigma \rangle$

(*) \nwarrow If $\sigma(a) \neq \text{skip}$, $\text{wait}(a)$ is stuck

$\langle s_t, \sigma \rangle \mapsto \langle s_t', \sigma' \rangle$

$\langle s, \sigma[a \mapsto s_t] \rangle \mapsto \langle s, \sigma'[a \mapsto s_t'] \rangle$

Why is the main thread special?

Instead of $\langle s, \sigma \rangle$, start with $\sigma[\text{main} \mapsto s]$

$\langle s, \sigma \rangle \mapsto \langle s', \sigma' \rangle$

$\sigma[a \mapsto s] \mapsto \sigma'[a \mapsto s']$

- Choose any thread

"Actual" parallelism - run n threads at once

$\forall i \in [1, n]. \langle s_i, \sigma \rangle \mapsto \langle s_i', \sigma_i' \rangle$ $n \leq P$ \nwarrow # of processors \nwarrow resolve data races
 $\sigma[a_1 \mapsto s_1] \dots [a_n \mapsto s_n] \mapsto \sigma'[a_1 \mapsto s_1'] \dots [a_n \mapsto s_n']$ $\sigma' = \text{merge}(\sigma_1', \dots, \sigma_n')$

This model subsumes nested parallelism

$s_1 || s_2 \stackrel{x}{=} \text{spawn } s_1; s_2; \text{wait}(x)$

And more:

while 1

do

 client := listen();

 _ := spawn (handle_conn client)

od

(*) Progress need ~~s~~ to change!

Local progress: If $P \vdash \sigma$ and $P \vdash s \text{ ok}$ then $s = \text{skip}$ or $\langle s, \sigma \rangle \mapsto \langle s', \sigma' \rangle$
or $s = \text{wait}(a)$ and $\sigma(a) \neq \text{skip}$

So then do we know that $\langle \sigma(a), \sigma \rangle \mapsto \langle s_a', \sigma' \rangle$? Maybe not!

$X := \text{spawn skip}$ *Dummy thread to bind X

$Y := \text{spawn}(\text{wait}(X))$

$Z := \text{spawn}(\text{wait}(Y))$ could deadlock!