

Getting Your Priorities Right

Fine-grained threading

Computation

e.g. sort, compress

Interaction
e.g. GUI

E-mail client

Need priorities - Millions of threads, no way to distinguish

2 problems w/ most ways of handling priorities

1. Fixed order \rightarrow Anti-modular

2. Priority inversions - high-prio thread waiting for low-prio

Solution: PriML

Partially ordered prior - prioritized order decks

spawn/sync + priorities

Qsort code

Type sys. prevents priority inversions

$\tau ::= \dots | \tau \text{ thread}(p) | \tau \text{ cmd}(p) | \forall \pi : C. \tau$

$p ::= p | \pi$

$e ::= \dots | \Lambda \pi : C. e$

$C ::= p \leq p | C \wedge C$

$m ::= x \leftarrow e ; m | \text{spawn}(p; \tau) \{ m \} | \text{sync } e | \text{ret } e$

$\Gamma \vdash m : \tau @ p$: m returns a τ , runs @ prio. p

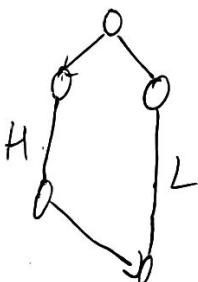
$$\frac{\Gamma \vdash m : \tau @ p'}{\Gamma \vdash \text{spawn}[p'; \tau] \{ m \} : \tau \text{ thread}[p] @ p} \quad (\text{SPAWN})$$

$$\frac{\Gamma \vdash e : \tau \text{ thread}[p] \quad \Gamma \vdash p \leq p'}{\Gamma \vdash \text{sync } e : \tau @ p} \quad (\text{SYNC})$$

$$\frac{\Gamma, \Pi, C \vdash e : \tau}{\Gamma \vdash \Lambda_{\Pi} : C. e : \text{Var}. C. \tau}$$

Cost Semantics

Model program as a DAG - label threads w/priorities



Greedy schedule - Assign vertices to procs so no procs are idle unless necessary.
 $T \leq \frac{W}{P} + S$

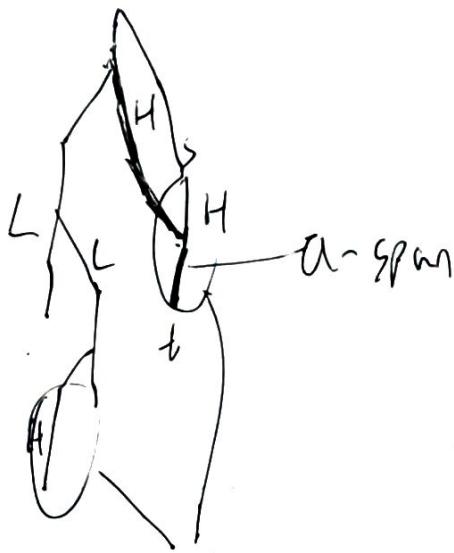
Prompt schedule - Greedy + run highest prio vertices possible

Bound response time: How long from when a thread is spawned until it completes.

Let $a = s \dots t$

$$RT(a) \leq \frac{w_{tp}(a)}{p} + S_a$$

Competitor work a -span (longest path ending at t)



Bound holds unless there is a prio. inv.



Comp. Work

Cost semantics: $m \downarrow v; g \downarrow^{\text{graph}}$

Theorem: If $\bullet t \vdash m \approx t @ p$, and $m \downarrow v; g$, then
g does not have a prio. inversion.

Dynamic semantics: $m \Rightarrow^{*} m'$
 \uparrow
 thread pool
 $a \hookrightarrow m$

Thm: If $\bullet t \vdash m \approx t @ p$ and $a \hookrightarrow_p m \Rightarrow^{*} m'$ and m' final and a
 thread a is active for T transitions. Then $m \downarrow v; g$ and \exists a
 prompt schedule of g in which $RT(a) = T$.