Scott T. Myers

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EDUCATION

Bachelor of Science in Electrical Engineering

The Pennsylvania State University, University Park, PA.
Graduation: May 2018

WORK EXPERIENCE

Electronics Engineer (*QorTek Inc)* …….. June 2018 – Present

- Write FPGA test benches in SystemVerilog using Vivado, debug code, and perform system integration. Work closely with team to meet project goals.
- Create Octave script to analyze, verify, and validate system performance.
- Utilize APIs in various languages to create automated testing programs.
- Design processing algorithms for wide ranges of hardware and software.
- Independently created automated test control system using LabVIEW and NI TestStand. System interfaces with hardware using MODBUS and SCPI commands.
- Debug, prototype, and modify circuits for DC-DC converters and power supplies.
- Simulation, schematic capture, and PCB design for high-density power electronics.
- Create test plans and block diagrams for internal and external communications.
- Communicate with contractors and customers to ensure needs are being met.

Engineering Intern (*Solid State Ceramics*) …….. August 2017 – May 2018

- Created automated testing program with closed-loop controls using LabVIEW which increased manufacturing capabilities by 400%.
- Independently devised, created, tested, and integrated new hardware into test setup to aid in R&D initiatives.
- Improved testing procedures to characterize ceramic piezoelectric transformers.

Engineering Intern *(QorTek Inc)* …….. May 2017 – August 2017

- Designed and tested attitude control system for small satellites. Tasks included writing a C# GUI with closed-loop controls and thorough hardware debugging.
- Designed PCBs in Altium to increase efficiency and accuracy of testing.
- Utilized Bluetooth and UART communication to transmit and receive data.
- Worked with coworkers to review code, debug systems, and create circuit boards.
- Wrote technical reports and documentation, and lead meetings with staff.

PROJECT EXPERIENCE

Personal Website…….. Summer 2018 - Present

- Ongoing project to learn web design and additional coding platforms as needed.
- Hosted using GitHub Pages and based off Jekyll.
- Documenting progress, making regular updates, and learning necessary skills.
- Intended to be long-term hub for self-taught projects in various coding languages.

OSIRIS-3U CubeSat…….. Spring 2017

- Goal: Study space weather's impact on communication networks using a CubeSat.
- Schematic and PCB, wrote documentation, created diagrams, ensured compliance.

SKILLS

Programming – Verilog, C#, MATLAB, BGScript (Bluetooth), C, C++, Python**.**

Areas of Interest - Full stack development, automation, FPGAs, debugging.
Software – Vivado, SVN, Git, LabVIEW, Altium, SOLIDWORKS, Visual Studio.

Circuit Design –Schematic creation, PCB design, digital and analog debugging.

EE – Control systems, analog and digital signal processing, Xilinx FPGA.
Administrative – Conducting interviews, mentoring interns, managing IT, writing and editing reports, preparing presentations, customer communication.

CERTIFICATIONS

Current: ISO 9001:2015 Internal Auditor

Former: NI CLAD (Certified LabVIEW Associate Developer)