
























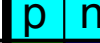


























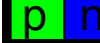











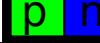

































HDILED/NSLEDS Array Writes DACMode 0

Y Address	DAC Card	Array Pixels		Write L=0, Y=n		Write L=1, Y=n	
(n<<4)+0	0	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+1	1	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+2	2	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+3	3	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+4	4	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+5	5	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+6	6	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+7	7	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+8	8	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+9	9	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+10	10	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+11	11	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+12	12	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+13	13	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+14	14	L=0		L=0		L=0	
		L=1		L=1		L=1	
(n<<4)+15	15	L=0		L=0		L=0	
		L=1		L=1		L=1	

Note: Y
addressing uses
only the needed
MSBs of line