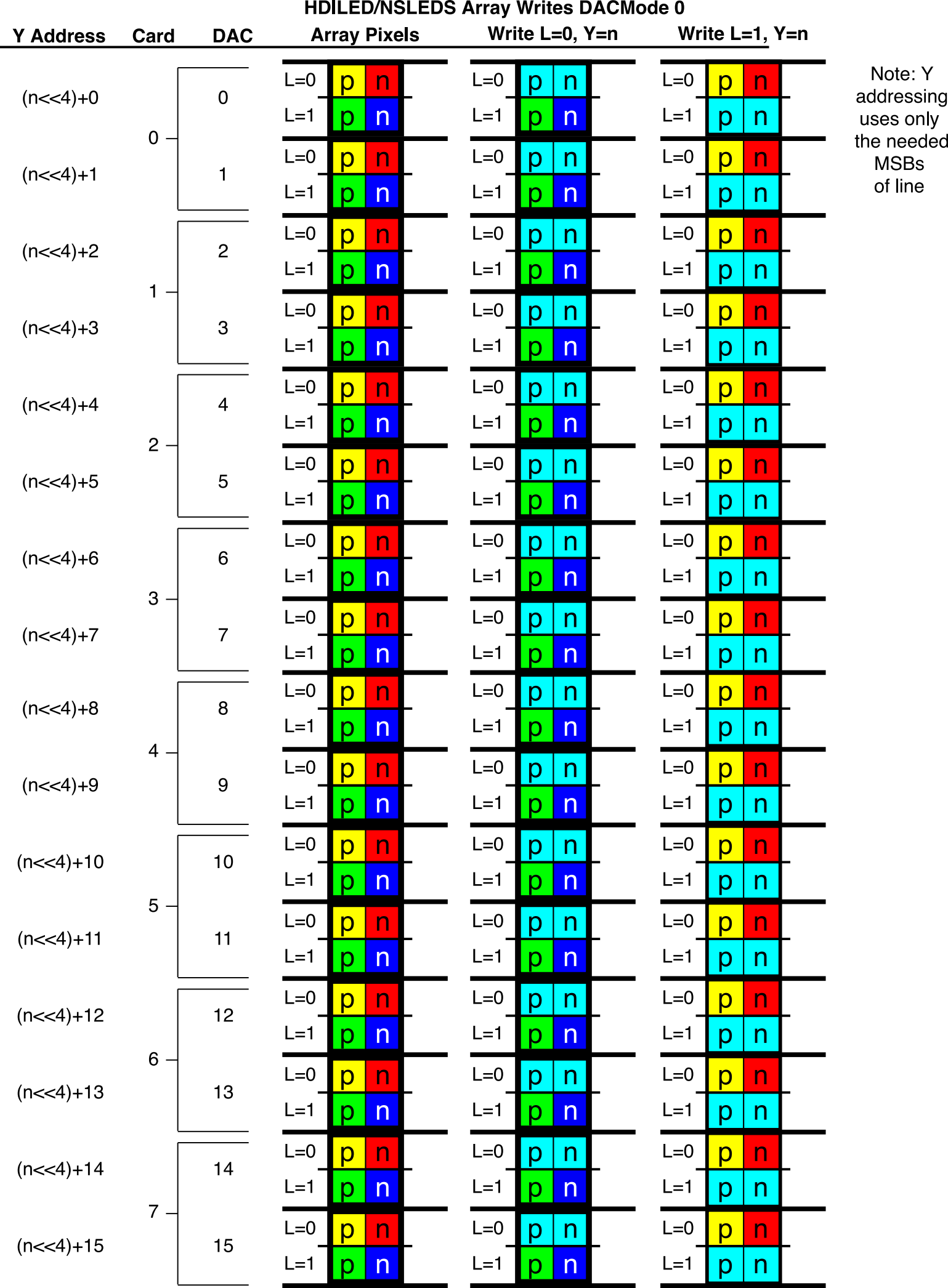
HDILED/NSLEDS Array Writes DACMode 0
Y Address
Card
DAC
Array Pixels
Write L=0, Y=n
Write L=1, Y=n
(n<<4)+0
(n<<4)+1
(n<<4)+2
(n<<4)+3
(n<<4)+4
(n<<4)+5
(n<<4)+6
(n<<4)+7
(n<<4)+8
(n<<4)+9
(n<<4)+10
(n<<4)+11
(n<<4)+12
(n<<4)+13
(n<<4)+14
(n<<4)+15
0
1
2
3
4
5
6
7
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
L=0
L=1
p
n
Note: Y addressing uses only the needed MSBs of line