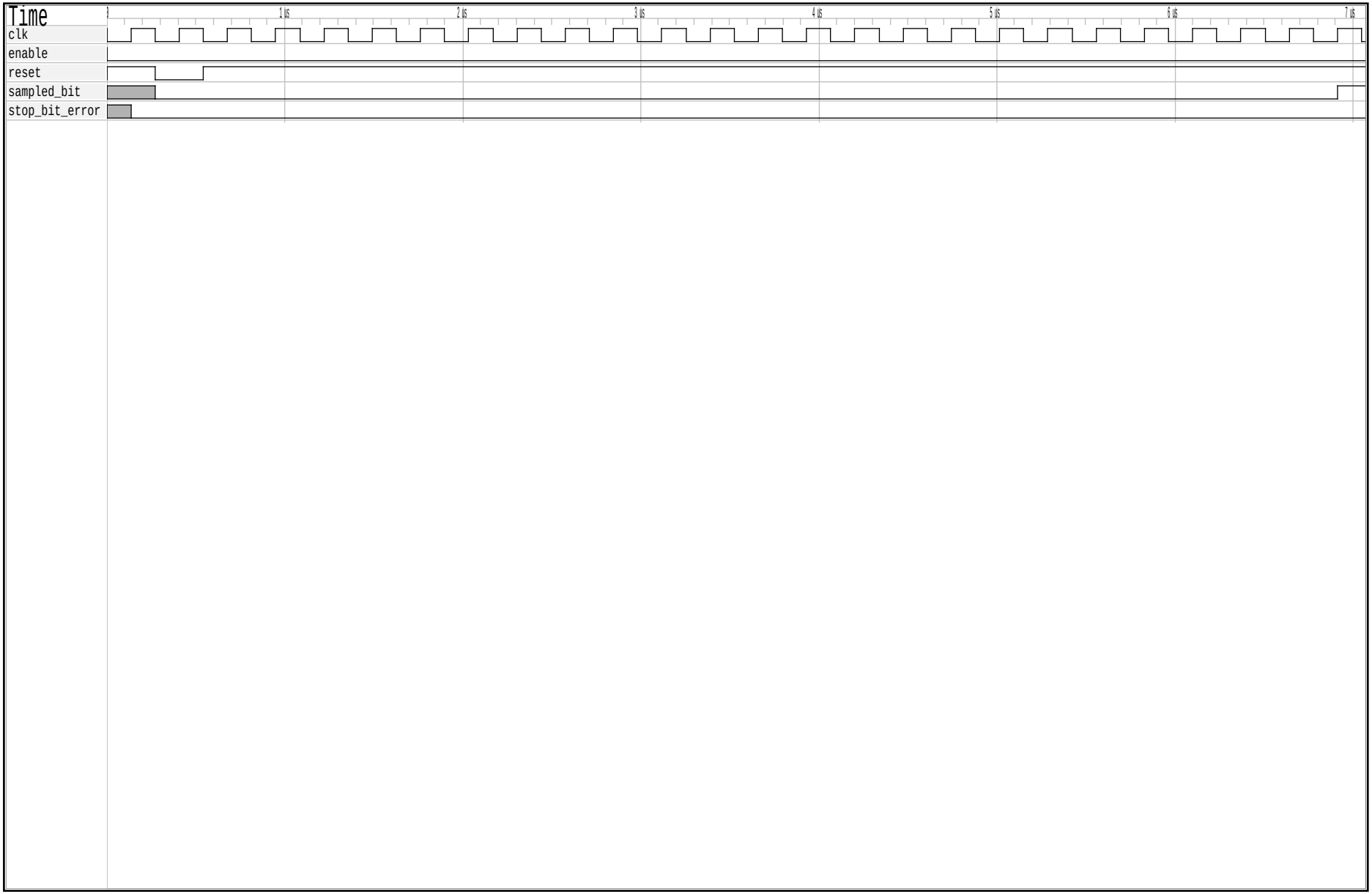
Time
1 us
2 us
3 us
4 us
5 us
6 us
7 us
clk
enable
reset
sampled_bit
stop_bit_error