Guangzhou HC Information Technology Co., Ltd.

Product Data Sheet

Module Data Sheet

Rev 1

1. 0	1.01			
2010/5/15	2011/4/6			

DRAWN BY:	Ling Xin		MODEL	: HC-05	
CHECKED BY:	Eric Huang			n: xternal 8M Flash and ED dustrial, and compatible	
APPD. BY:	Simon Mok		REV:	2.0	PAGES:
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introduction					

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Contents

- 1. Product's picture
- 2. Feature
- 3. Pins description
- 4. The parameters and mode of product
- 5. Block diagram
- 6. Debugging device
- 7. Characteristic of test
- 8. Test diagram
- 9. AT command set

1.Product's picture

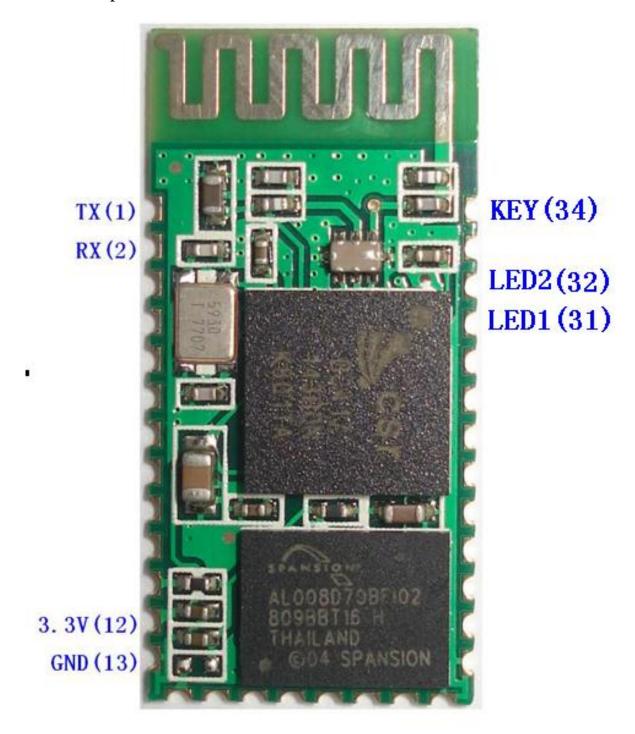


Figure 1. A Bluetooth module

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Figure 2 50 pieces chips in an anti-static blister package

2. Feature

- Wireless transceiver
 - Sensitivity (Bit error rate) can reach -80dBm.
 - ➤ The change range of output's power: -4 +6dBm.
- Function description (perfect Bluetooth solution)
 - ► Has an EDR module; and the change range of modulation depth: 2Mbps 3Mbps.
 - ➤ Has a build-in 2.4GHz antenna; user needn't test antenna.

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- ➤ Has the external 8Mbit FLASH
- ➤ Can work at the low voltage (3.1V~4.2V). The current in pairing is in the range of 30~40mA. The current in communication is 8mA.
- > PIO control can be switched.
- ➤ Has the standard HCI Port (UART or USB)
- ➤ The USB protocol is Full Speed USB1.1, and compliant with 2.0.
- This module can be used in the SMD.
- ➤ It's made through RoHS process.
- The board PIN is half hole size.
- ➤ Has a 2.4GHz digital wireless transceiver.
- ➤ Bases at CSR BC04 Bluetooth technology.
- ➤ Has the function of adaptive frequency hopping.
- \triangleright Small (27mm \times 13mm \times 2mm).
- Peripheral circuit is simple.
- ➤ It's at the Bluetooth class 2 power level.
- ► Storage temperature range: -40 °C 85 °C, operating temperature range: -25 °C +75 °C
- Any wave inter Interference: 2.4MHz, the power of emitting: 3 dBm.
- ➤ Bit error rate: 0. Only the signal decays at the transmission link, bit error may be produced. For example, when RS232 or TTL is being processed, some signals may decay.
- Low power consumption
- Has high-performance wireless transceiver system
- Low Cost
- Application fields:
 - Bluetooth Car Handsfree Device
 - Bluetooth GPS
 - ➤ Bluetooth PCMCIA, USB Dongle
 - ➤ Bluetooth Data Transfer
 - Software
 - > CSR

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3. PINs description

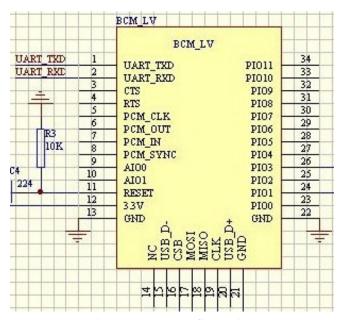


Figure 3 PIN configuration

The PINs at this block diagram is same as the physical one.

PIN Name	PIN#	Pad type	Description	Note
GND	13 21 22	VSS	Ground pot	
			Integrated 1.8V (+)	
1V8	14	VDD	supply with On-chip linear	
1 V O	14	۷۵۵	regulator output within	
			1.7-1.9V	
VCC	12	3.3V		
AIOO	AIOO D' D' ma' and		Programmable	
AIO0	9	Bi-Directional	input/output line	
A IO1	10	10 Di Directional	Programmable	
AlOI	AIO1 10 Bi-Directional		input/output line	
		Bi-Directional	Programmable	
PIO0	23		input/output line, control	
		RX EN	output for LNA(if fitted)	
	n: r		Programmable	
PIO1	24	Bi-Directional TX EN	input/output line, control	
	IXEN	IA DIN	output for PA(if fitted)	
PIO2	25	Bi-Directional	Programmable	

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			input/output line	
			Programmable	
PIO3	26	Bi-Directional	input/output line	
			Programmable	
PIO4	27	Bi-Directional	input/output line	
			Programmable	
PIO5	28	Bi-Directional	input/output line	
DVO 6	20	2121	Programmable	
PIO6	29	Bi-Directional	input/output line	CLK_REQ
DIO7	20	D' D' (' 1	Programmable	CL IZ OLUT
PIO7	30	Bi-Directional	input/output line	CLK_OUT
DIO	21	Di Dinastianal	Programmable	
PIO8	31	Bi-Directional	input/output line	
DIOO	22	Bi-Directional	Programmable	
PIO9	32	Bi-Directional	input/output line	
PIO10	33	Bi-Directional	Programmable	
PIOIU	33	DI-Directional	input/output line	
PIO11	34	Bi-Directional	Programmable	
FIOTI		Di-Directional	input/output line	
		CMOS Input with weak		
RESETB	11	intemal pull-down		
		-	***	
UART_RTS	4	CMOS output, tri-stable with	UART request to send,	
		weak internal pull-up	active low	
UART_CTS	3	CMOS input with weak	UART clear to send, active	
		internal pull-down	low	
UART_RX	2	CMOS input with weak	UART Data input	
		internal pull-down	_	
UART_TX	1	CMOS output, Tri-stable	UART Data output	
		with weak internal pull-up	G : 1 : 1 1: . C	
SPI_MOSI	17	CMOS input with weak	Serial peripheral interface	
		internal pull-down	data input	
SPI_CSB	3 16	CMOS input with weak	Chip select for serial	
		internal pull-up	peripheral interface, active	
CDI CI V	10	CMOS input with west	low	
SPI_CLK	19	CMOS input with weak	Serial peripheral interface	

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		internal pull-down	clock	
SPI_MISO	18	CMOS input with weak internal pull-down	Serial peripheral interface data Output	
USB	15	Bi-Directional		
USB_+	20	Bi-Directional		
1.8V	14		1.8V external power supply input	Default: 1.8V internal power supply.
PCM_CLK	5	Bi-Directional		
PCM_OUT	6	CMOS output		
PCM_IN	7	CMOS Input		
PCM_SYNC	8	Bi-Directional		

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4. The parameters and mode of product

LINVOR BLUE T



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CSR,BC417143B V 2.0 2006/09/6

蓝牙 RF 模块

- 1. 采用 CSR BC4 +8M FLASH 方案
- 具有 PIO0-PIO11、AIO0、AIO1、 USB、PCM、UART 及 SPI 接口, 模块内置 8MFLASH,功能强大, 用户可定制软件,适用于各种蓝牙 设备,内置 RF 天线,便于调试。

蓝牙协议版本	Bluetooth Specification V2.0 With EDR
USB 协议	Full Speed USB V1.1
USB Protocol	Compliant With USB V2.0
頻率	2.4Ghz ISM band
调制方式	GFSK(Gaussian Frequency Shift Keying)
发射功率	-4 ->4 dBm, Class 2
灵敏度	≤-80dBm at 0.1% BER
通讯速率	Asynchronous:2Mbps(Max)
供电电源	3.3V
工作温度	-20~+55 Centigrade
封装尺寸	27mmX13mmX2mm

Page 1of 2

If you want more information, please visit www.wavesen.com.

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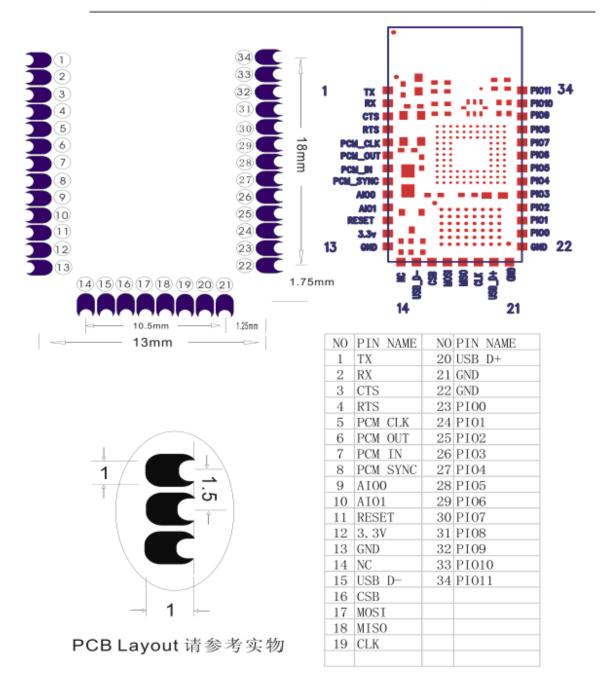
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LV-BC-2.0

单位: mm



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5. Block diagram

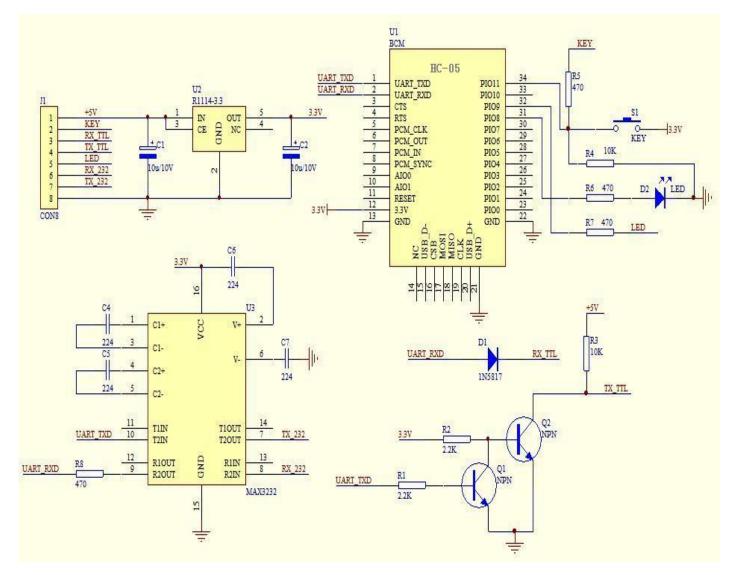


Figure 5 Block diagram

6. Debugging device

6.1 Device

PC, hardware, 3G, 3G Frequency Counter (SP3386), 3.15V DC power supply, Shielding, Bluetooth Test box.

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6.2 Software

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7. Characteristic of test

Test (Condition	25℃	RH	65%
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		Min	Тур	Max	Unit
<u>1.</u>	Carrier Freq. (ISM Band)	2.4		2.4835	MHz
2.	RF O/P Power	-6	2	4	dBm
<u>3.</u>	Step size of Power control	2		8	dB
<u>4.</u>	Freq. Offset (Typical Carrier freq.)	-75		75	KHz
<u>5.</u>	Carrier Freq. drift (Hopping on, drift rate/50uS)	-20		20	KHz
	1 slot packet	-25		25	KHz
	3 slot packet	-40		-40	KHz
6.	Average Freq. Deviations (Hopping off, modulation)	140		175	KHz
	Freq. Deviation	115			KHz
	Ratio of Freq. Deviation	0.8			
<u>7.</u>	Receive Sensitivity @< 0.1% BER(Bit error rate)	-83			dBm

8. Test diagram

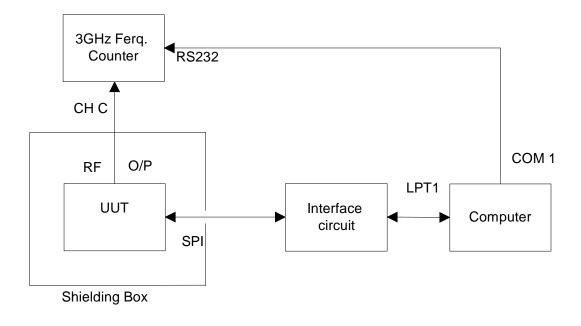


Fig 1. Programming and Freq. Alignment

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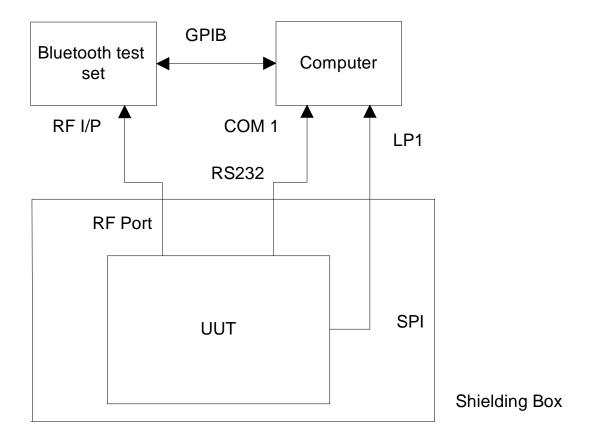


Fig 2 RF parameter Test Procedure

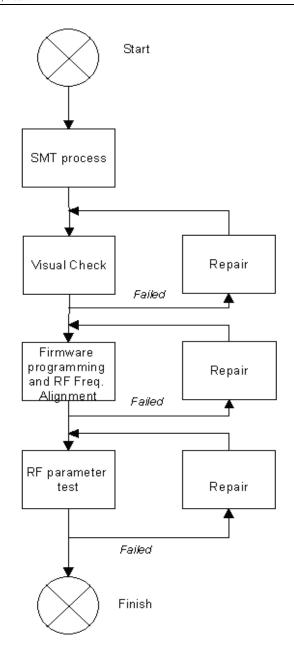


Fig 3 Assemble/Alignment/Testing Flow Chart

9. AT command set

More information about command set is provided at HC-05 master-salve bluetooth serial module command set.pdf. Please download it from our company website www.wavesen.com.

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