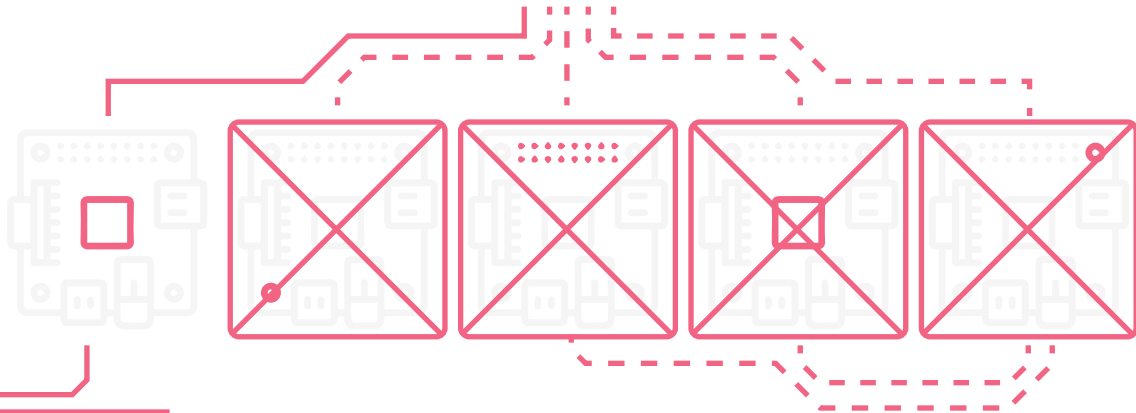




compile state
machine



I/O module

control hardware with additional modules