RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

New Scheme Based On AICTE Flexible Curricula

Information Technology, VI-Semester

Departmental Elective IT 603(A) Compiler Design

Course Objectives:

1. To teach the students the basic concepts of Compiler, programming languages and develop an understanding of the compilation phases
2. To make students understand what is syntax analysis and various types of parsers
3. To introduce syntax trees and dependency graphs
4. To introduce intermediate code generation, memory management and the role of symbol table and its organization
5. To introduce Code generation and code optimization

UNIT-I:
Introduction to Compiler, analysis of source program, phases and passes, Bootstrapping, lexical analyzers, data structures in compilation – LEX: lexical analyzer generator, Input buffering, Specification and Recognition of tokens, YACC, The syntactic specification of programming languages: Context free grammars, derivation and parse trees, capabilities of CFG.

UNIT-II:
Syntax Analysis: working of Parser, Top down parsing, Bottom-up parsing, Operator precedence parsing, predictive parsers, LR parsers (SLR, Canonical LR, LALR), constructing SLR parsing tables, constructing Canonical LR parsing tables, Constructing LALR parsing tables, using ambiguous grammars, an automatic parser generator.

UNIT-III:
Syntax Directed Translation: Definitions, Inherited Attributes, L-attributed definitions, S-attributed definitions, Dependency graph, Construction of syntax trees, Top down translation, postfix notation, bottom up evaluation.

UNIT-IV:
Intermediate Code Generation: Three address code, quadruple & triples, translation of assignment statements, Boolean expression and control structures, Backpatching, Run Time Memory Management: Static and Dynamic storage allocation, stack based memory allocation schemes, Symbol Table management.

UNIT-V:
Code Optimization and Generation: organization of code optimizer, basic blocks and flow graphs, DAG representation of basic blocks, loops in flow graph, peephole optimization, Basic of block optimization.

References:-

1. A. V. Aho, R. Sethi & J. D. Ullman, Compilers: Principles, Techniques and Tools, Pearson Ed.
2. Alfred V. Aho, Jeffrey D. Ullman, Principles of Compiler Design, Narosa Publishing House.
3. Ronald Mak, Writing Compilers and Interpreters, Wiley IndiacEdition.
4. Louden, Compiler Construction, Cengage learning.

Course Outcomes:

Upon completion of this course, students will be able to-

1. Demonstrate an understanding of the compilation phases.
2. Specify and analyze the lexical, syntactic and semantic structures of advanced language features.
3. Write a scanner, parser, and semantic analyser without the aid of automatic generators.
4. Describe techniques for intermediate code and machine code optimization.
5. Design the structures and support required for compiling advanced language features.

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New Scheme Based On AICTE Flexible Curricula

Information Technology, VI-

Semester

Departmental Elective IT 603(B) Data Mining

Course Objectives:

1. To introduce data warehouse and its components
2. To introduce knowledge discovery process, data mining and its functionalities
3. To develop understanding of various algorithms for association rule mining and their differences
4. To introduce various classification techniques
5. To introduce various clustering algorithms.

Unit I:

Data Warehousing**:** Need for data warehousing , Basic elements of data warehousing, Data Mart, Data Warehouse Architecture, extract and load Process, Clean and Transform data, Star ,Snowflake and Galaxy Schemas for Multidimensional databases, Fact and dimension data, Partitioning Strategy-Horizontal and Vertical Partitioning, Data Warehouse and OLAP technology, Multidimensional data models and different OLAP Operations, OLAPServer: ROLAP, MOLAP, Data Warehouse implementation, Efficient Computation of Data Cubes, Processing of OLAP queries, Indexing data.

Unit II:

Data Mining**:** Data Preprocessing, Data Integration and Transformation, Data Reduction, Discretizaion and Concept Hierarchy Generation, Basics of data mining, Data mining techniques, KDP (Knowledge Discovery Process), Application and Challenges of Data Mining

Unit III:

Mining Association Rules in Large Databases**:** Association Rule Mining, Single-Dimensional Boolean Association Rules, Multi-Level Association Rule, Apriori Algorithm, Fp- Growth Algorithm, Time series mining association rules, latest trends in association rules mining.

Unit IV:

Classification and Clustering: Distance Measures, Types of Clustering Algorithms, K-Means Algorithm, Decision Tree, Bayesian Classification, Other Classification Methods, Prediction, Classifier Accuracy, Categorization of methods, Outlier Analysis.

Unit V:

Introduction of Web Mining and its types, Spatial Mining, Temporal Mining, Text Mining, Security Issue, Privacy Issue, Ethical Issue.

References:-

1. Arun k Pujari “Data Mining Technique” University Press
2. Han,Kamber, “Data Mining Concepts & Techniques”,
3. M.Kaufman., P.Ponnian, “Data Warehousing Fundamentals”, John Wiley.
4, M.H.Dunham, “Data Mining Introductory & Advanced Topics”, Pearson Education.
5. Ralph Kimball, “The Data Warehouse Lifecycle Tool Kit”, John Wiley.
6. E.G. Mallach , “The Decision Support & Data Warehouse Systems”, TMH

Course Outcomes:

Upon completion of this course, students will be able to-

1. Demonstrate an understanding of the importance of data warehousing and OLAP technology
2. Organize and Prepare the data needed for data mining using pre preprocessing techniques
3. Implement the appropriate data mining methods like classification, clustering or Frequent Pattern mining on various data sets.
4. Define and apply metrics to measure the performance of various data mining algorithms.
5. Demonstrate an understanding of data mining on various types of data like web data and spatial data

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Information Technology, VI-Semester

Departmental Elective IT 603(C) Embedded Systems

Course Objectives:

1. To introduce students with knowledge about the basic functions and applications of embedded systems
2. To introduce the architecture of embedded systems
3. To introduce the various communication protocols
4. To enable students to have knowledge of the memory types and supporting technologies of embedded systems.
5. To enable students to have knowledge about the development of embedded software

UNIT-I Introduction to Embedded Systems: Definition of embedded system, embedded systems vs. general computing systems, history of embedded systems, classification, major application areas, purpose of embedded systems, characteristics and quality attributes of embedded systems, common design metrics, and processor technology: general purpose processor, application specific processor, single purpose processor.

UNIT-II Embedded System Architecture: Von Neumann v/s Harvard architecture, instruction set architecture, CISC and RISC instructions set architecture, basic embedded processor, microcontroller architecture, CISC & RISC examples: 8051, ARM, DSP processors.

UNIT-III Input Output and Peripheral Devices Timers and counters, watchdog timers, interrupt controllers, PWM, keyboard controller, analog to digital converters, real time clock. Introduction to communication protocols: basic terminologies, concepts, serial protocol: I2C, CAN, firewire, USB. Parallel protocols: PCI bus, IrDA, bluetooth, IEEE 802.11, wireless protocols.

UNIT-IV Memory System Architecture Caches, virtual memory, MMU, address translation, memory and interfacing, memory write ability and storage performance. Memory types, composing memory – advance RAM interfacing, microprocessor interfacing I/O addressing, interrupts, direct memory access, arbitration multilevel bus architecture.

UNIT-V Embedded System Supporting Technologies Difference between normal OS and RTOS, scheduling algorithms. Case study: Tiny OS, VxWorks, QNX. Overview of VLSI technology, introduction to device drivers. Case studies: washing machine, air-conditioning, auto focus camera.

References:

1. F Vahid, T Giogarvis, Embedded systems: A unified hardware/software approach, Wiley, 1999.

2. Raj Kamal, Embedded Systems Introduction, 2nd Ed., TMH publication, 2015.

3.David E Simons, An Embedded Software Primer, Pearson, 1999.

Course Outcomes:

Upon completion of this course, students will be able to-

1. Explain the embedded system concepts and architecture of embedded systems
2. Describe the architecture of 8051 microcontroller and write embedded program for 8051 microcontroller
3. Select elements for an embedded systems tool.
4. Understand the memory types used in embedded systems
5. Design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability