1. Realize MCP model for logic OR gate.

2. Realize MCP model for logic XOR gate.

3. Implement Hebb learning rule for AND logic gate.

4. Implement Perceptron learning rule for NOT AND Logic gate.

5. Use Adaline networks to implement AND/OR Logic gate.

6. Implement backpropagation algorithm to classify a dataset. (Select a binary classification dataset from UCI repository).

7. Implement Associative and Heteroassociative memory for pattern association.

8. Implement bidirectional associative memory for pattern association.

9. Implement Hopfield network for any given problem.

10. Implement Self organizing maps for any given problem.