

# Lab 4- Report:

## **1)descending.asm:**

Number of cycles taken = 637

Number of data hazard stalls = 74

Number of times instruction on wrong branch path is taken= 88

## **2)evenorOdd.asm:**

Number of cycles taken = 23

Number of data hazard stalls = 6

Number of times instruction on wrong branch path is taken= 0

## **3)1-even.asm:**

Number of cycles taken = 139

Number of data hazard stalls = 40

Number of times instruction on wrong branch path is taken= 6

## **4)2-prime.asm:**

Number of cycles taken = 27

Number of data hazard stalls = 4

Number of times instruction on wrong branch path is taken= 1

## **5)3-descending.asm:**

Number of cycles taken = 621

Number of data hazard stalls = 142

Number of times instruction on wrong branch path is taken= 50

## **6)4-histogram.asm:**

Number of cycles taken = 453

Number of data hazard stalls = 160

Number of times instruction on wrong branch path is taken= 20

**7)5-fibonacci.asm:**

Number of cycles taken = 177

Number of data hazard stalls = 36

Number of times instruction on wrong branch path is taken= 8

**8)6-arithmetic.asm:**

Number of cycles taken = 249

Number of data hazard stalls = 60

Number of times instruction on wrong branch path is taken= 11