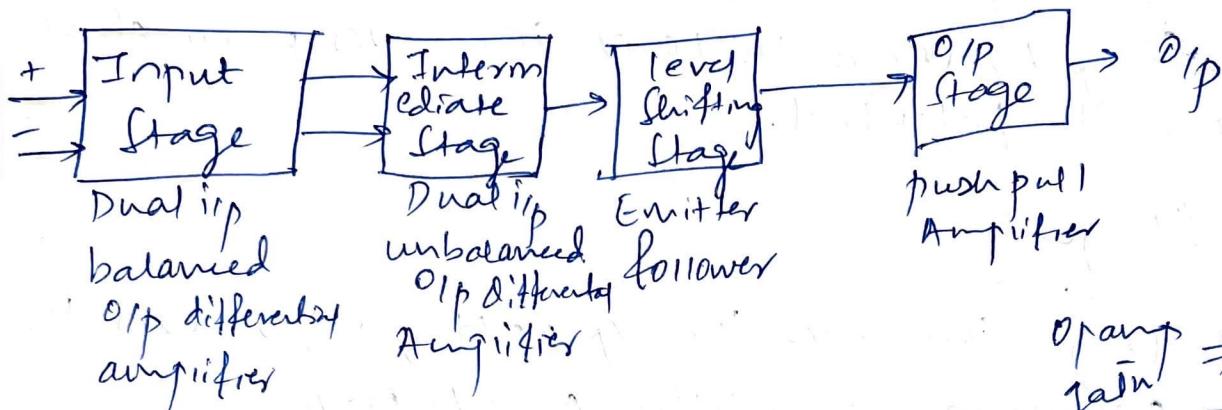


# Block Diagram of OPAMP

4 - Stages



$$\text{Opamp} \Rightarrow A \approx 10^5 - 10^6$$

## 1) Input Stage:

- \* This stage consists of two i/p's (Non inverting i/p & Inverting i/p) and dual O/p's.
- \* This stage provides most of the voltage gain of op-amp.
- \* This stage decides the value of i/p Resistances ( $R_i$ ).

$$\begin{array}{c} \text{SV} \\ \text{---} \\ \text{IS} \\ \text{---} \\ \text{3V} \end{array} \quad A = \frac{\text{SV}}{\text{3V}} = \frac{10}{3} \approx 3.33 \quad \text{Assume } A=5 \quad \text{A} \rightarrow \text{gain of opamp}$$

$5 \times 2 \Rightarrow 10$   $\because 2\text{V} = \text{diff. Vol.}$

## 2) Intermediate Stage: differential Amplifier.

- \* It consists of dual i/p's & Single O/p.
- \* It is driven by the O/p of i/p stage.
- \* i/p stage alone can't provide high gain, so intermediate stage provide the required additional voltage gain.

## 3) Level Shifting Stage:

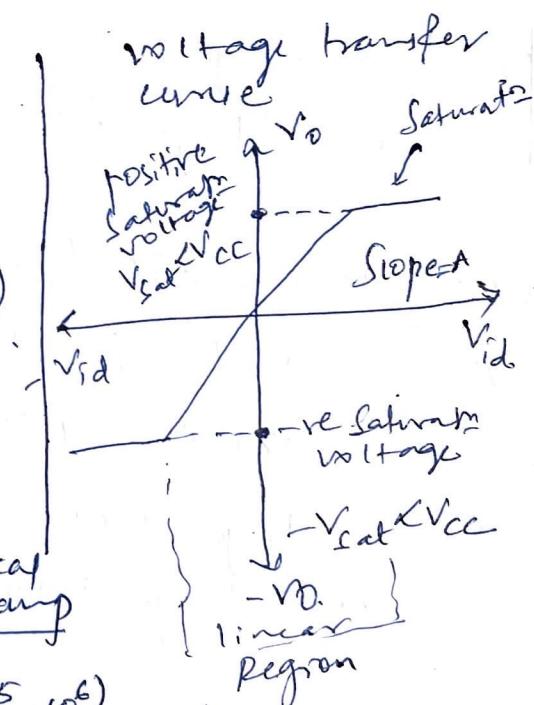
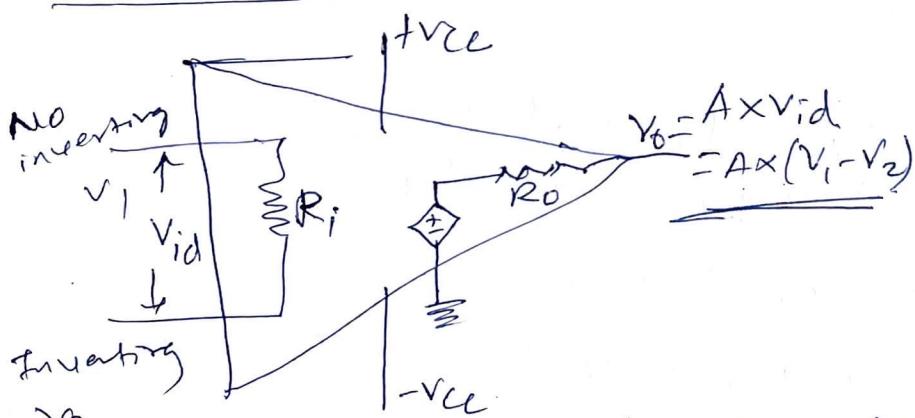
- \* Level Shifting Stage is used after the intermediate stage to shift the DC level to zero volts w.r.t ground.
- \* O/p level of intermediate stage is above  $\text{SV}$  direct coupling is used.

\* As op-amp amplifies the DC signals also & in different cascading stages coupling capacitors are not used hence stage by stage DC level increases well above the ground potential.

### O/P Stage

- \* this stage increases the O/p voltages & raises the current carrying capability of op-amp.
- \* As push-pull complementary amplifiers meets all these requirements & hence used as an O/p stage.
- \* provide low O/p resistance.

### Equivalent Ckt of Op-amp

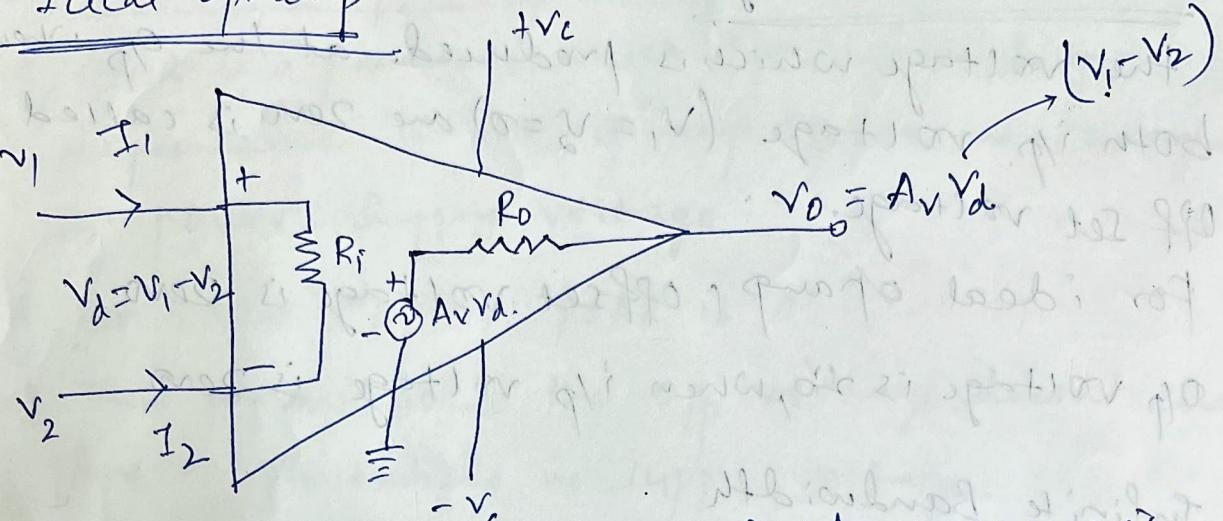


### Comparison of an ideal & practical opamp

	Ideal	Practical
Open loop gain	Infinite	very high ( $10^5$ - $10^6$ )
Open loop bandwidth	Infinite	Dominant pole ( $10\text{Hz}$ )
(+A)R <sub>i</sub> Resistance	Infinite	High ( $> 60\text{dB}$ )
O/p Resistance	<del>infinite</del> zero	High ( $> 1M\Omega$ )
O/p Bias Current	zero	low ( $< 100\text{nA}$ )
Offset voltage	zero	low ( $< 10\text{mV}$ )

offset currents	zero	low ( $\mu$ A)
gain rate	Infinite	A few V/MS
drift	zero	low.

## Ideal Op-amp



- \* The important characteristics of ideal opamp are:
  - 1) Infinite voltage gain ( $A_v = \infty$ ):  $A_{v(\text{dc})} = \frac{V_{\text{out}}}{V_{\text{in}}}$
  - 2) Infinite input Resistance ( $R_i = \infty$ ):  $A_{v(\text{dc})} = \frac{V_{\text{out}}}{V_{\text{in}}} = 20 \log \frac{V_{\text{out}}}{V_{\text{in}}}$
  - 3) zero O/p Resistance ( $R_o = 0$ )
- \* The input resistance of ideal opamp is  $(R_i = \infty)$ .
- \* Due to which the current flowing in each input terminal will be zero i.e.  $I_1 = I_2 = 0$ .
- \* The O/p resistance of Op-amp is the resistance viewed from its O/p terminal.
- \* The O/p resistance of an ideal op-amp is "zero".

#### 4) Zero offset voltage

- \* the voltage which is produced at the O/P when both i/p voltage ( $V_1 = V_2 = 0$ ) are zero is called off set voltage.
- \* for ideal opamp, off set voltage is zero.
- \* O/P voltage is  $\infty$ , when i/p voltage is zero.

#### 5) Infinite Bandwidth

- \* Bandwidth of an amplifier is the range of frequencies over which all the signal frequencies are amplified almost equally.
- \* the bandwidth of an Ideal op-amp is  $\infty$ .  
So it can amplify any freq. Signal from  $0$  to  $\infty$  Hz.

#### 6) Infinite CMRR

- \* the CMRR is the ability of opamp to reject common mode signal successfully

$$CMRR = \frac{|Av|}{|A_c|}$$

- \* For an ideal op-amp CMRR is  $\infty$ .

this arise due to noise present at the i/p terminal of opamp

#### 7) Infinite Slew Rate ( $S = \infty$ )

- Slew Rate is defined as the maximum rate of change of O/P voltage with time i.e.  $S = \frac{dV_o}{dt}_{\text{max}}$

$$S = \frac{dV_o}{dt}_{\text{max}} \quad \boxed{V_o = A_v V_d} \quad \text{so } V_o$$

- \* For an ideal op-amp, Slew Rate ( $S$ ) is  $\infty$ .

- 8) zero power-supply rejection ratio
- \* PSRR is a parameter which specifies the degree of dependence of the op-amp O/p on the changes in power supply voltage.
  - \* For ideal op-amps PSRR is  $\infty$ .
  - \* This means that the O/p voltage doesn't change due to fluctuations in supply voltage.

### Parameters of Op-amp

1) Voltage gain

2) i<sub>p</sub> impedance.

3) O/p

4) i<sub>p</sub> offset voltage

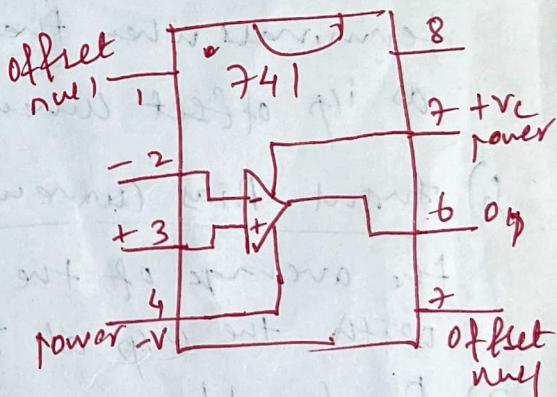
5) " " current

6) " " Bias current

7) Bandwidth

Resistor → opposes both AC & DC

Impedance → opposes only the flow of AC



1) Voltage gain  $\Rightarrow$  Gain =  $\frac{O/p \text{ voltage}}{i/p \text{ voltage}}$ ,  $V_{OOL} = \frac{V_{out}}{V_{in}}$

2) input impedance  $\Rightarrow$

At i<sub>p</sub>, voltage drop is high, so i<sub>p</sub> impedance is always high.

$$R_{IN} = \frac{V_{IN}}{I_{IN}}$$

### 3) Output impedance?

the resistance offered by the O/p of an op-amp is called as O/p impedance

\* voltage drop at O/p must be '0'  $\Rightarrow V_{\text{out}} = I_{\text{out}} \cdot R_{\text{out}}$

\*  $R_{\text{out}}$  must be as low as possible.

### 4) Input offset voltages?

\* When  $i_{\text{ip}}$  is '0',  $O_{\text{ip}}$  has to be zero,  
i.e. in ideal condition, but in practical it is not zero, we need to apply dc voltage at the i/p terminal to force the O/p voltage to be zero.

$$V_{\text{out}} = Z - Z = 0.$$

### 5) Input offset current?

$I_1 - I_2$  or  $I_2 - I_1$ ,  
the difference between the currents into the two i/p terminals when the O/p is held at 20mV is called as i/p offset current

### 6) Input Bias current $\Rightarrow I_1 + I_2$

the average of the currents into the two i/p terminals with the O/p at 20mV is called IBC

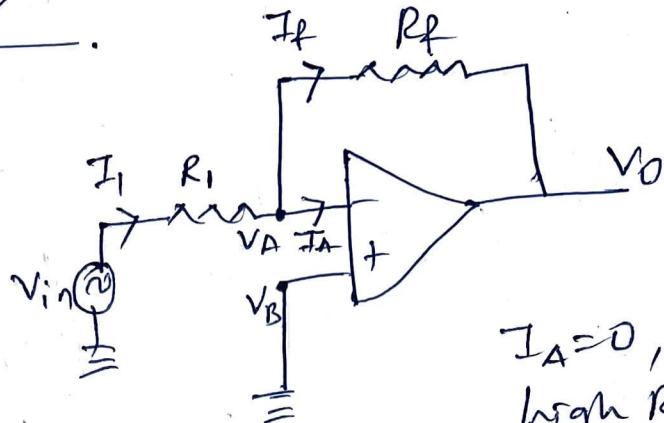
### 7) Bandwidth

the range of frequencies for which an op-amp can be used is called as 'Bandwidth' of an opamp

## Inverting Amplifier

$$I_1 = I_f$$

$$\frac{V_{in} - V_A}{R_1} = \frac{V_A - V_o}{R_f}$$



$$\frac{V_{in} - 0}{R_1} = \frac{0 - V_o}{R_f}$$

$$\frac{V_{in}}{R_1} = -\frac{V_o}{R_f} \Rightarrow \boxed{\frac{V_o}{V_{in}} = -\frac{R_f}{R_1}}$$

$I_A = 0$ , due to high resistance at  $R_1$

$V_A = V_B = 0$  due to virtual ground.

## Non Inverting Amplifier

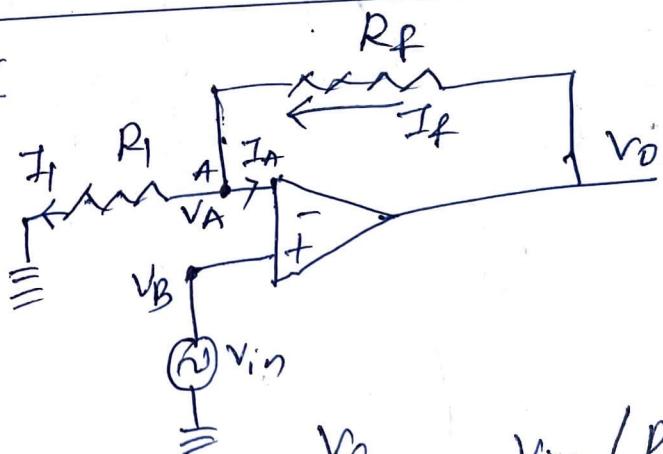
Apply KCL at Node A

$$I_f = I_1 + I_A$$

$I_A \approx 0$  due to high  $i_{op}$

$$I_f = I_1$$

$$\frac{V_o - V_A}{R_f} = \frac{V_A - 0}{R_1}$$



$$\frac{V_o}{R_f} = V_{in} \left( \frac{R_f + R_1}{R_1 R_f} \right)$$

$$\frac{V_o}{V_{in}} = \left( \frac{R_f + R_1}{R_1} \right)$$

$$\frac{V_o}{V_{in}} = \left( 1 + \frac{R_f}{R_1} \right)$$

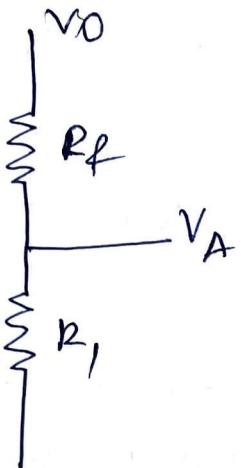
$V_A \approx V_B$ , due to virtual ground

$$V_B = V_{in}$$

$$\frac{V_B - V_{in}}{R_f} = \frac{V_{in}}{R_1} \Rightarrow \frac{V_o}{R_f} - \frac{V_{in}}{R_f} = \frac{V_{in}}{R_1}$$

$$\frac{V_o}{R_f} = V_{in} \left( \frac{R_f + R_1}{R_1 R_f} \right)$$

$$\frac{V_o}{R_f} = \frac{V_{in}}{R_1} + \frac{V_{in}}{R_f}$$



By voltage division rule

$$V_A = \frac{R_1}{R_1 + R_f} \times V_O$$

$$V_A = V_{in}$$

virtual ground

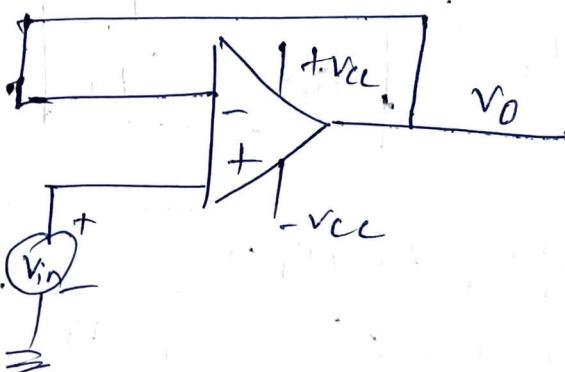
$$V_{in} = \frac{R_1}{R_1 + R_f} \times V_O$$

$$\frac{V_{in}}{V_O} = \frac{R_1}{R_1 + R_f} \Rightarrow \frac{V_{in}}{V_O} = \left( \frac{R_1}{R_f} + 1 \right)$$

3) Op-amp as voltage follower

$$R_f = 0, R_i = \infty$$

$$\frac{V_O}{V_{in}} = \left( 1 + \frac{R_f}{R_f} \right)$$



$$R_f = 0$$

$$A = [1]$$

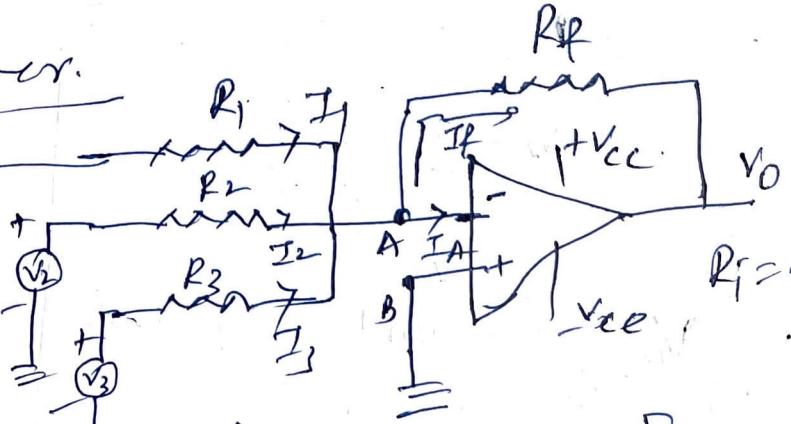
$$V_O = V_{in}$$

4) Op-amp as summing

Applying KVL to node A

$$I_1 + I_2 + I_3 = I_{pt} + I_A$$

$$\frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} = \frac{V_A - V_O}{R_f}$$



$R_i = \infty$  for ideal op-amp

$$-V_O = \frac{R_f}{R_{in}} [V_1 + V_2 + V_3]$$

$$V_O = -\frac{R_f}{R_{in}} [V_1 + V_2 + V_3] \text{ if } (R_f = \infty)$$

$$\frac{1}{R_{in}} [V_1 + V_2 + V_3] = \left( 0 - \frac{V_O}{R_f} \right)$$

$$V_O = -\frac{R_f}{R_{in}} [V_1 + V_2 + V_3]$$