

CS-482 Parallel And Distributed Computing Test 1

Name: _____ Roll No.: _____

1. **Examine** the effectiveness of Instruction-Level Parallelism (ILP), Data-Level Parallelism (DLP), and Task-Level Parallelism (TLP) in microprocessor architectures? Analyze their incorporation and mention strengths and weaknesses of leveraging these architectural paradigms in enhancing computational performance and addressing scalability challenges?(CLO-1)
2. **Analyze** the strengths and limitations of employing vector clocks? Mention which one global state is inconsistent and which is consistent. (CLO-2)

