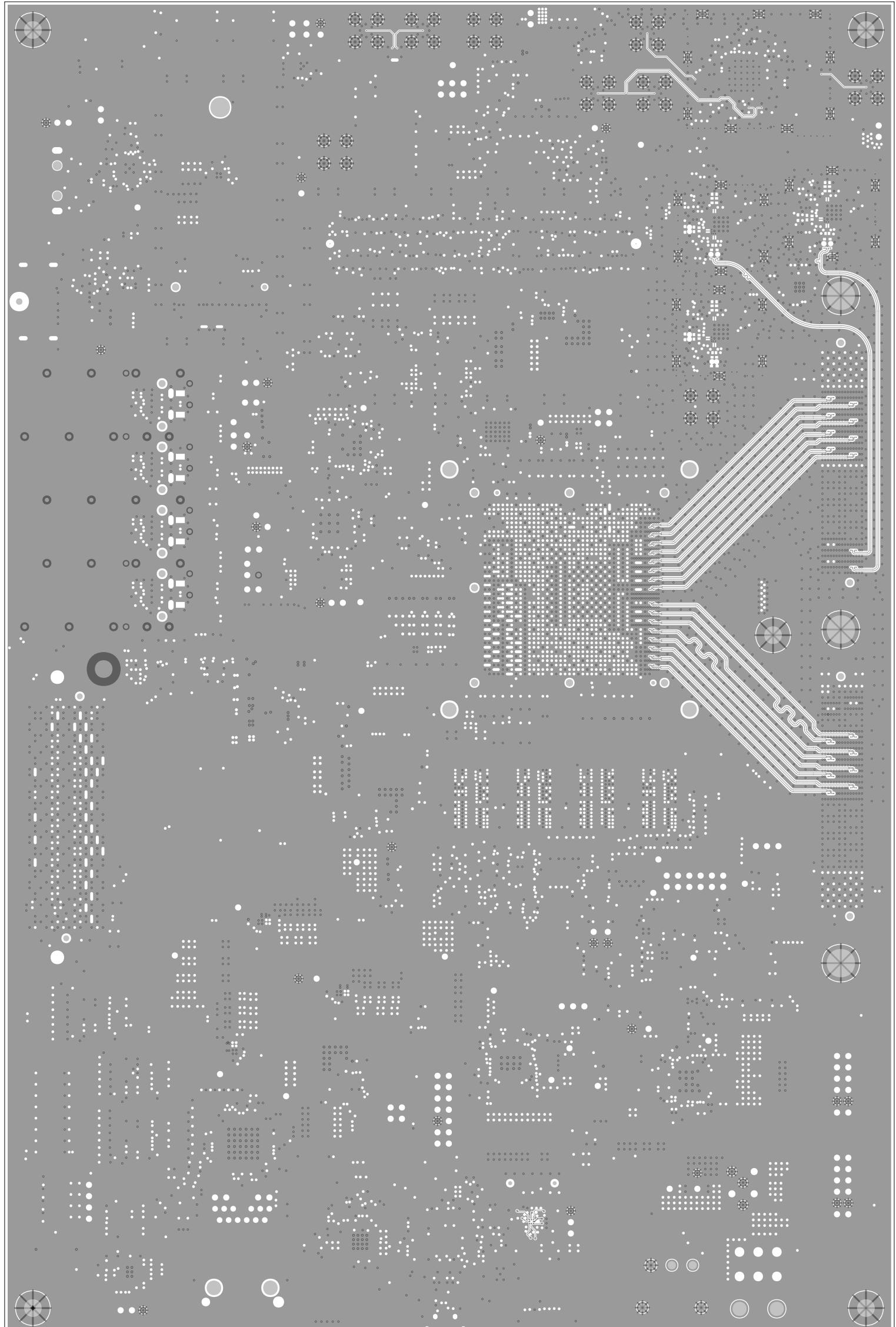
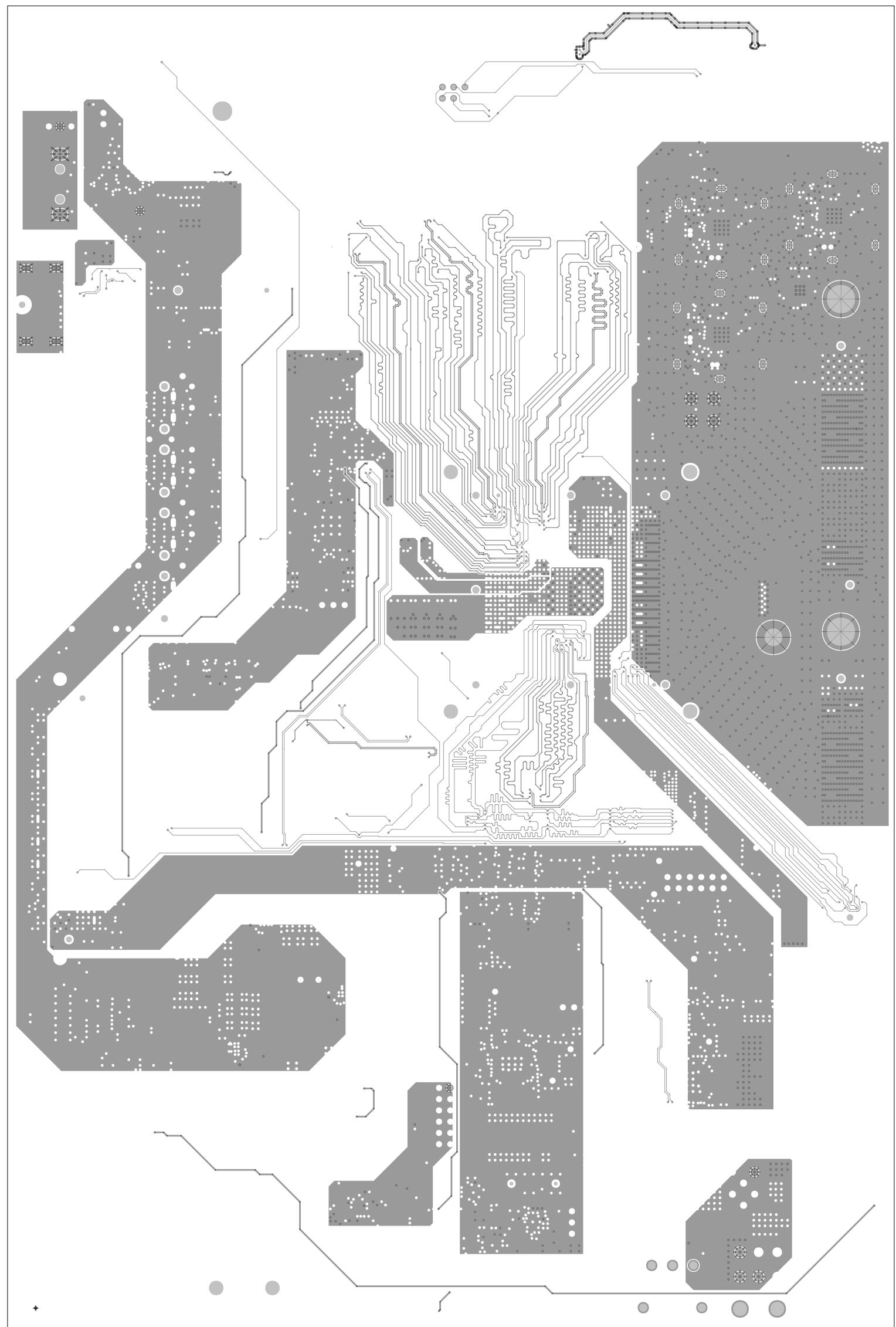


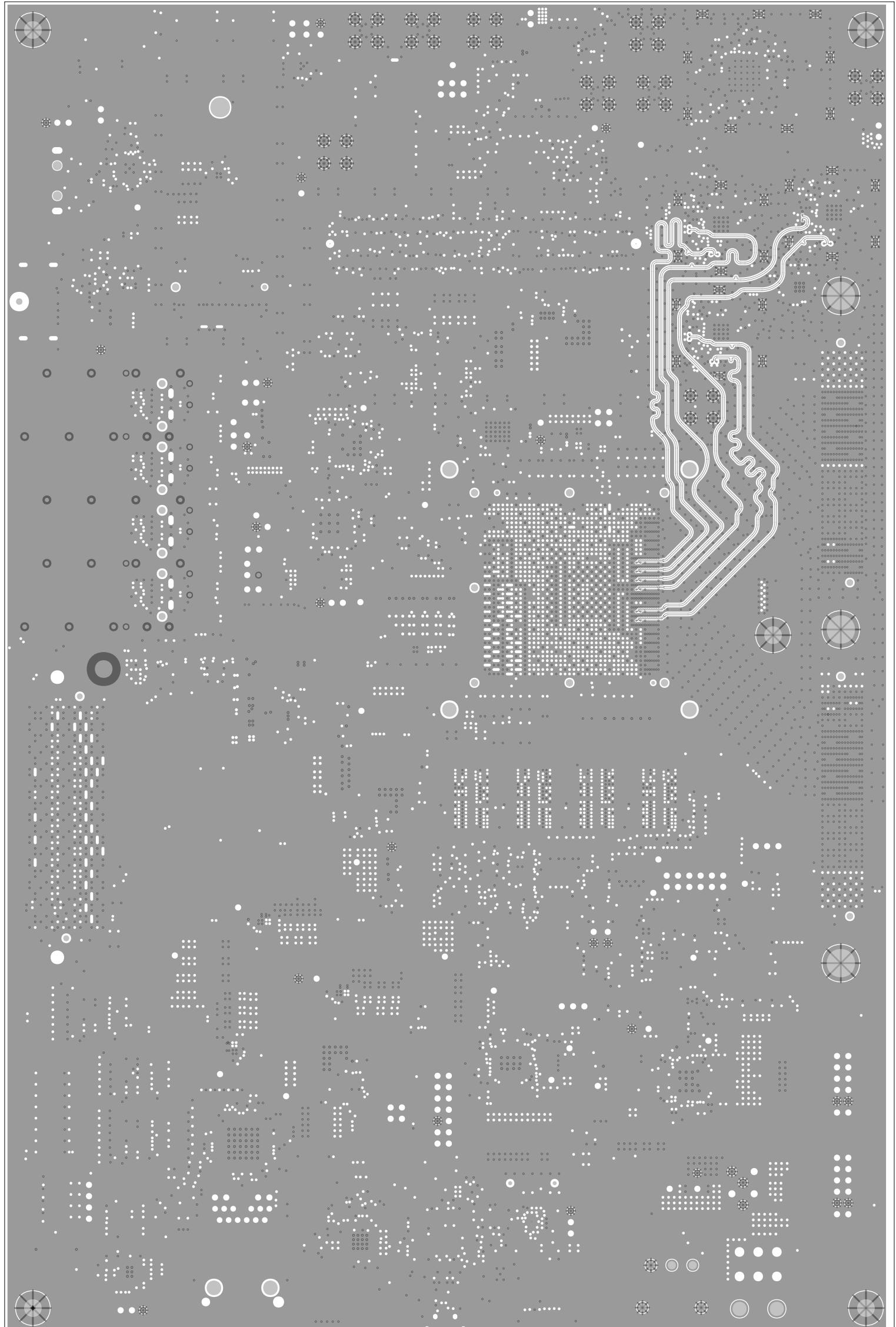
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB #	1280976
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
1	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



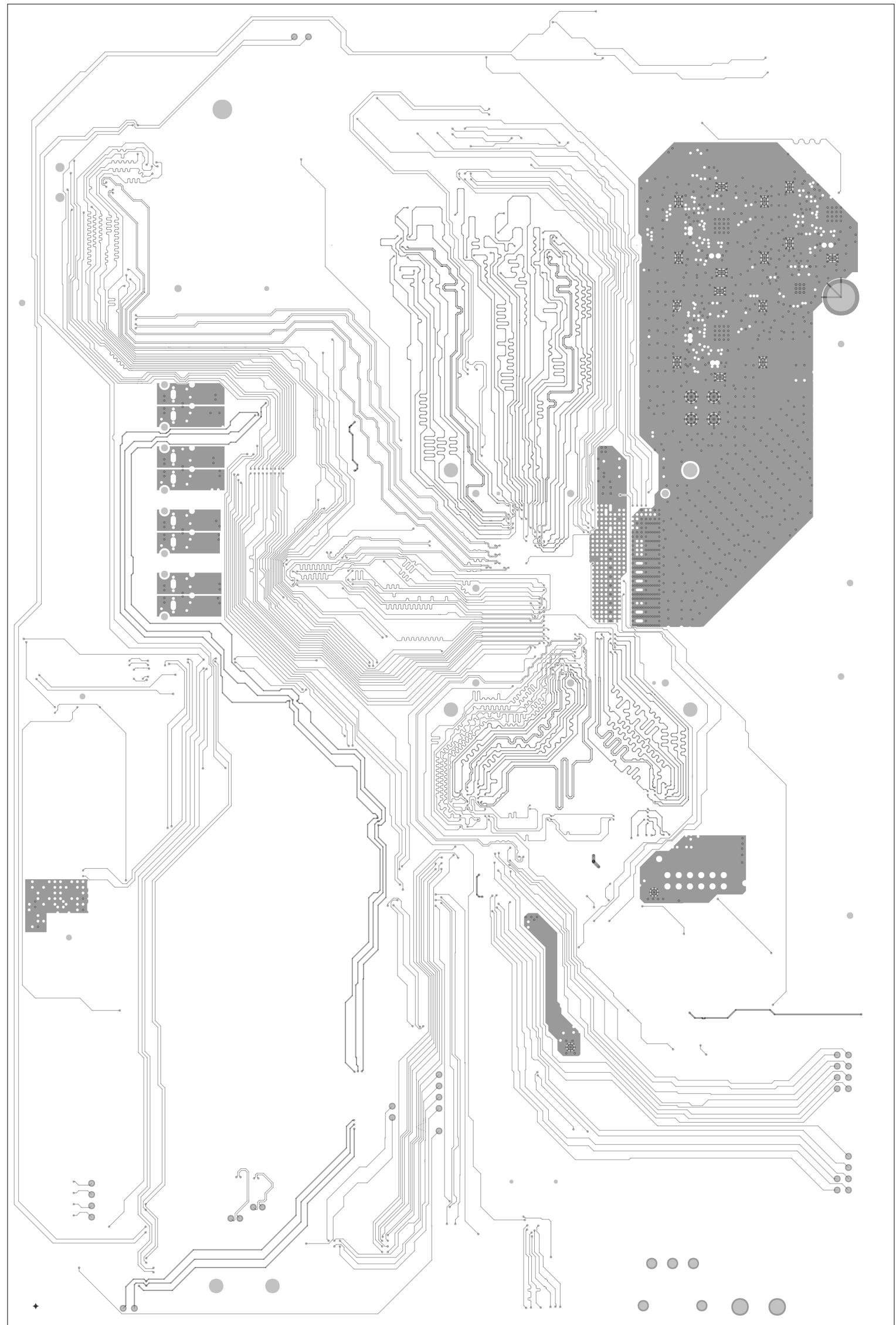
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L02_SIG/GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
2	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



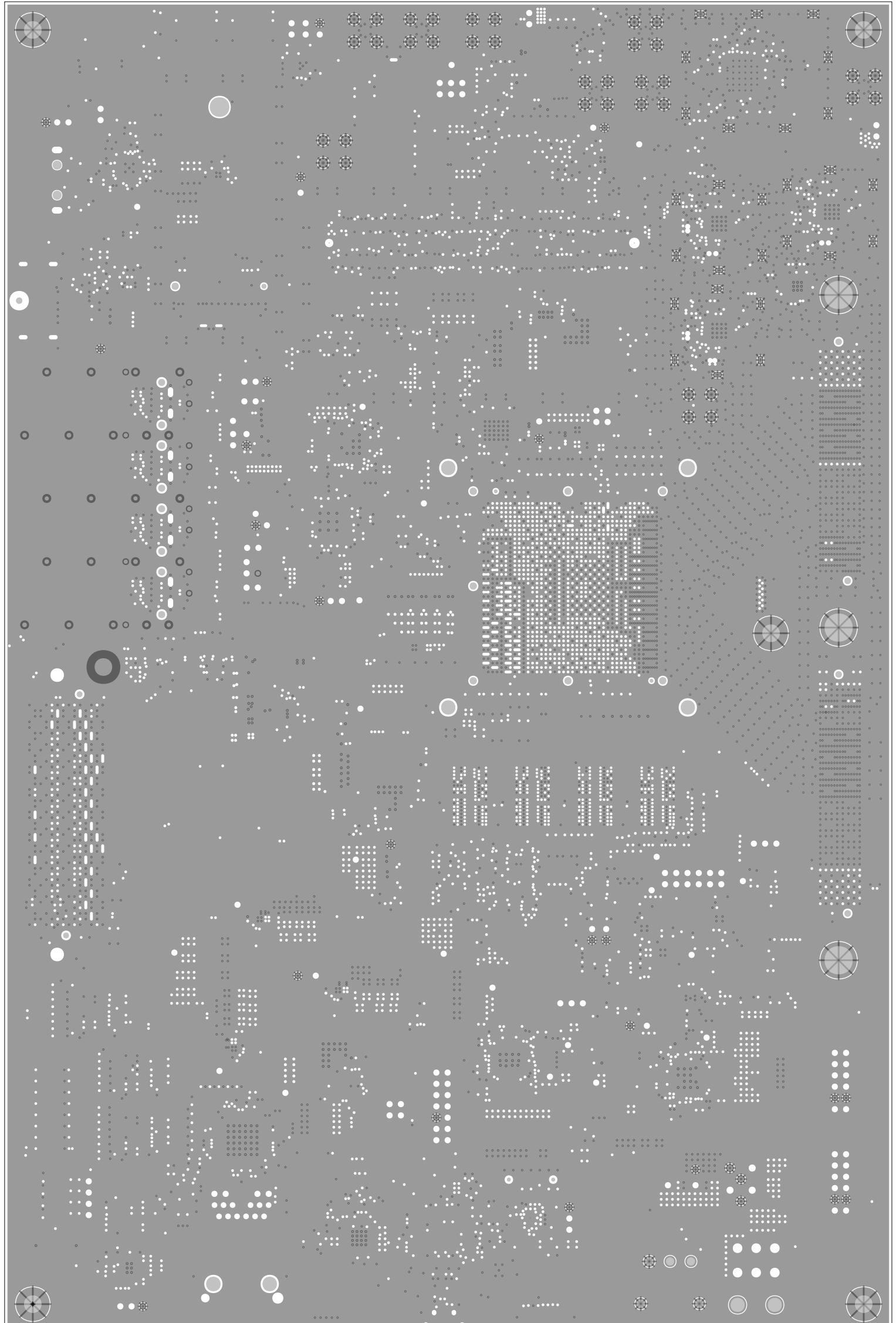
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L03_SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
3	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



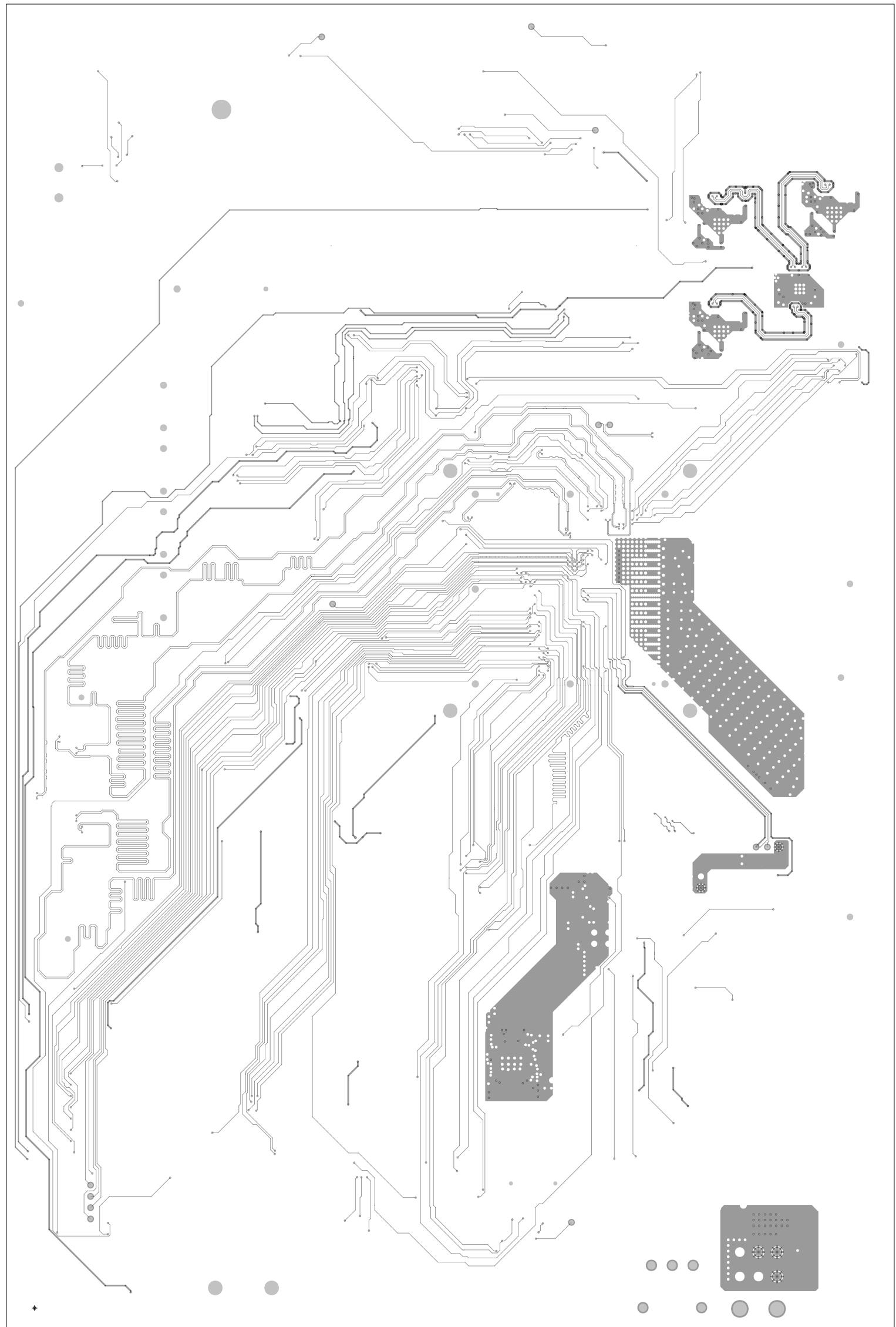
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L04 SIG/GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
4	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



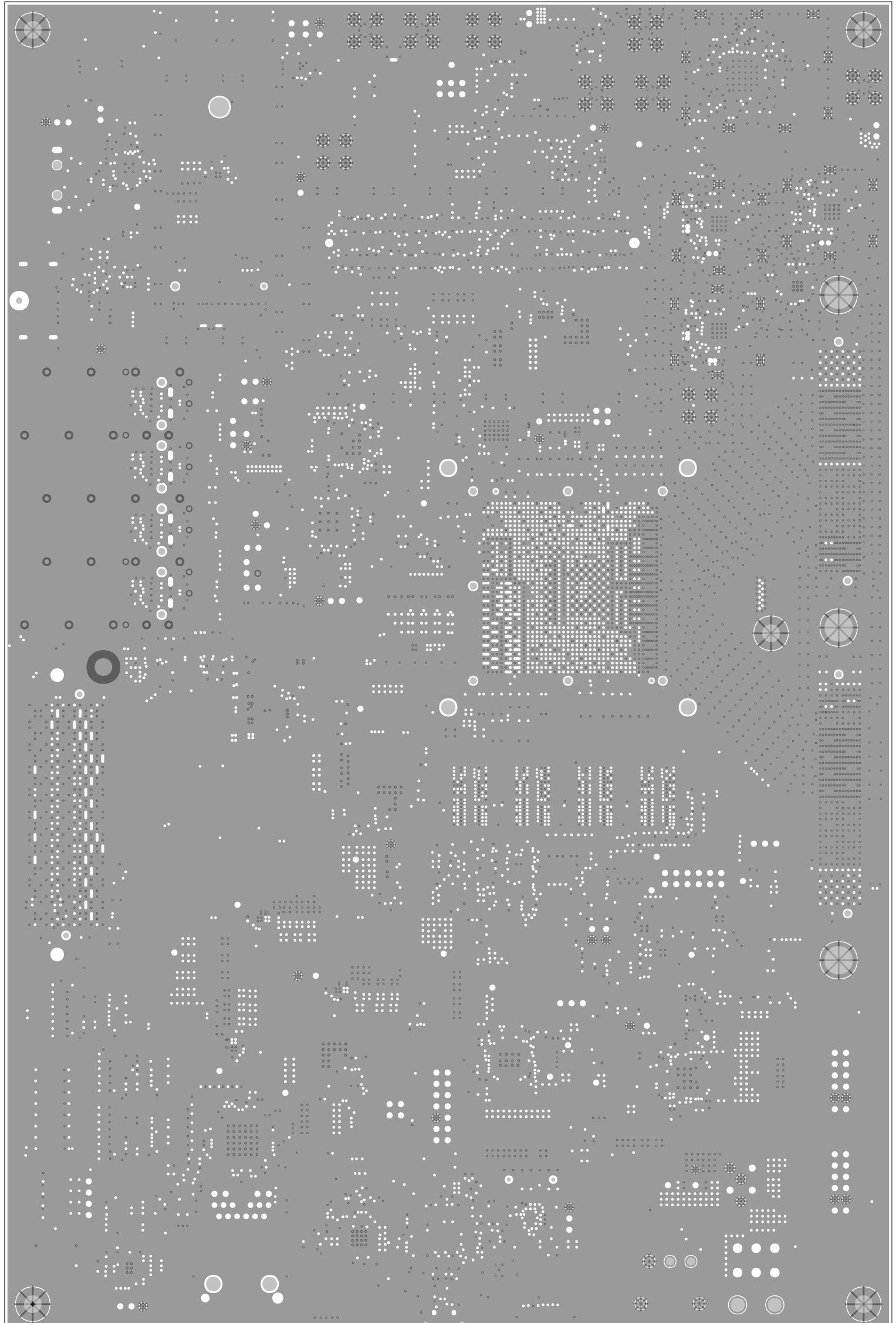
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L05_SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
5	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



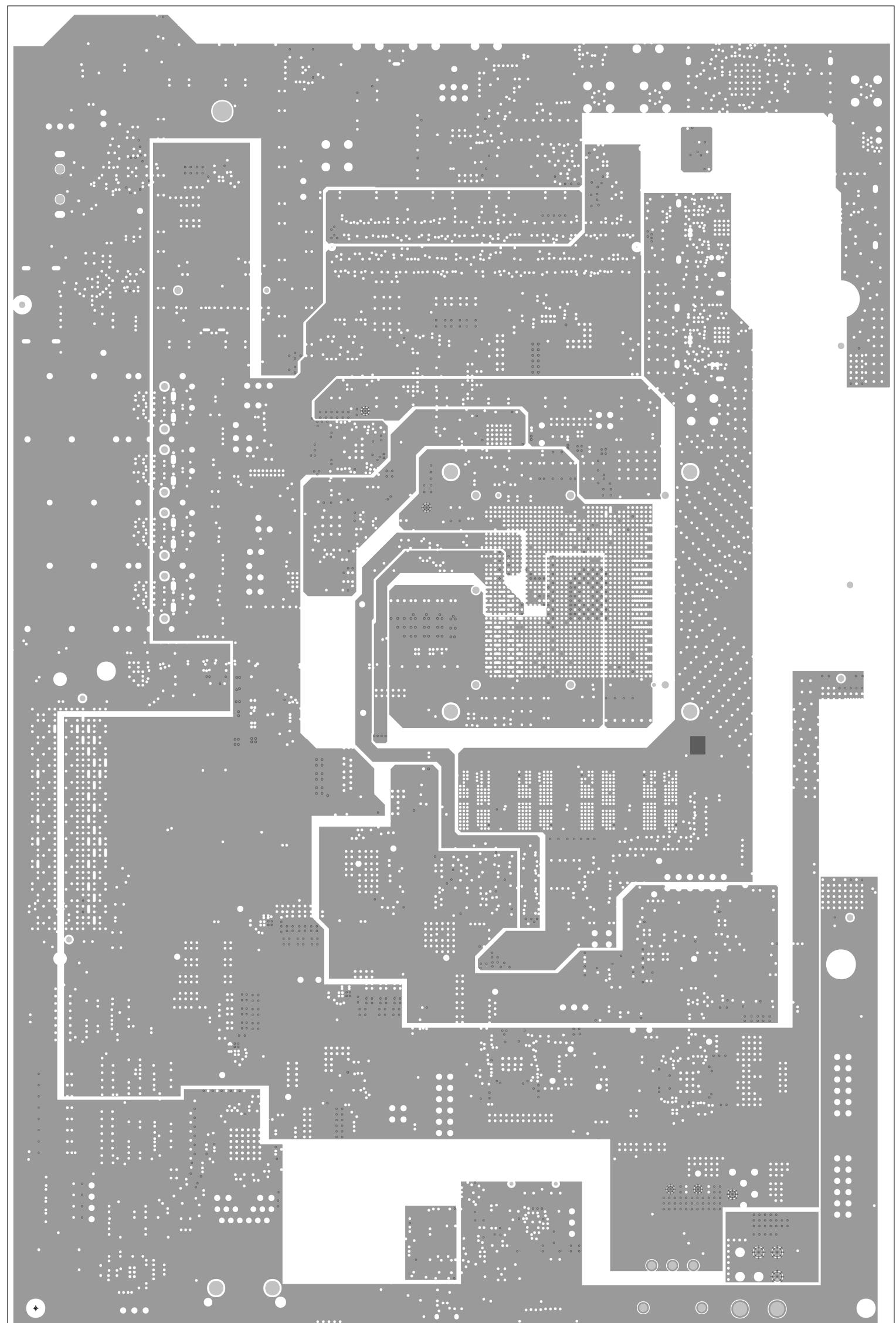
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L06_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
6	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



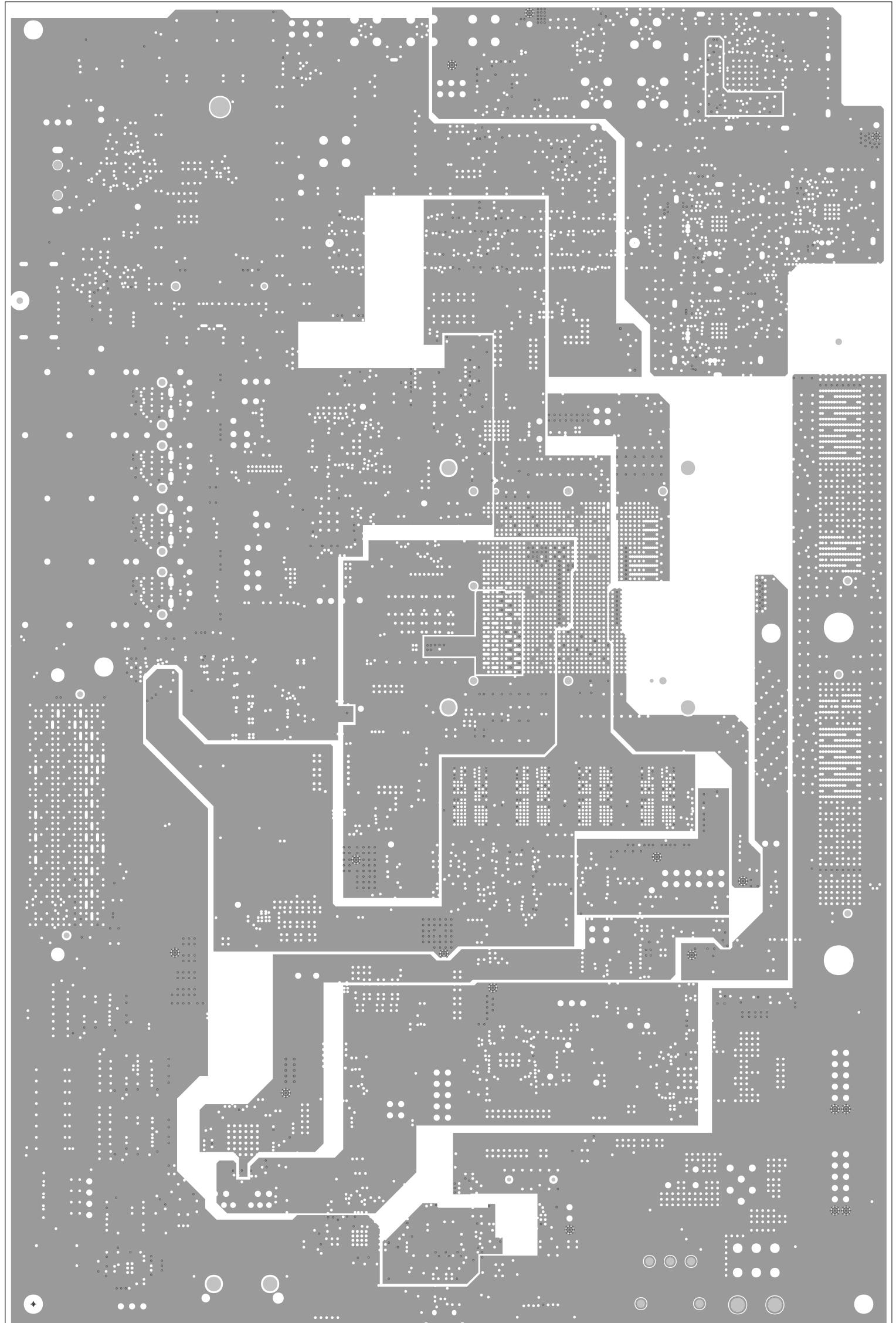
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L07_SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
7	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



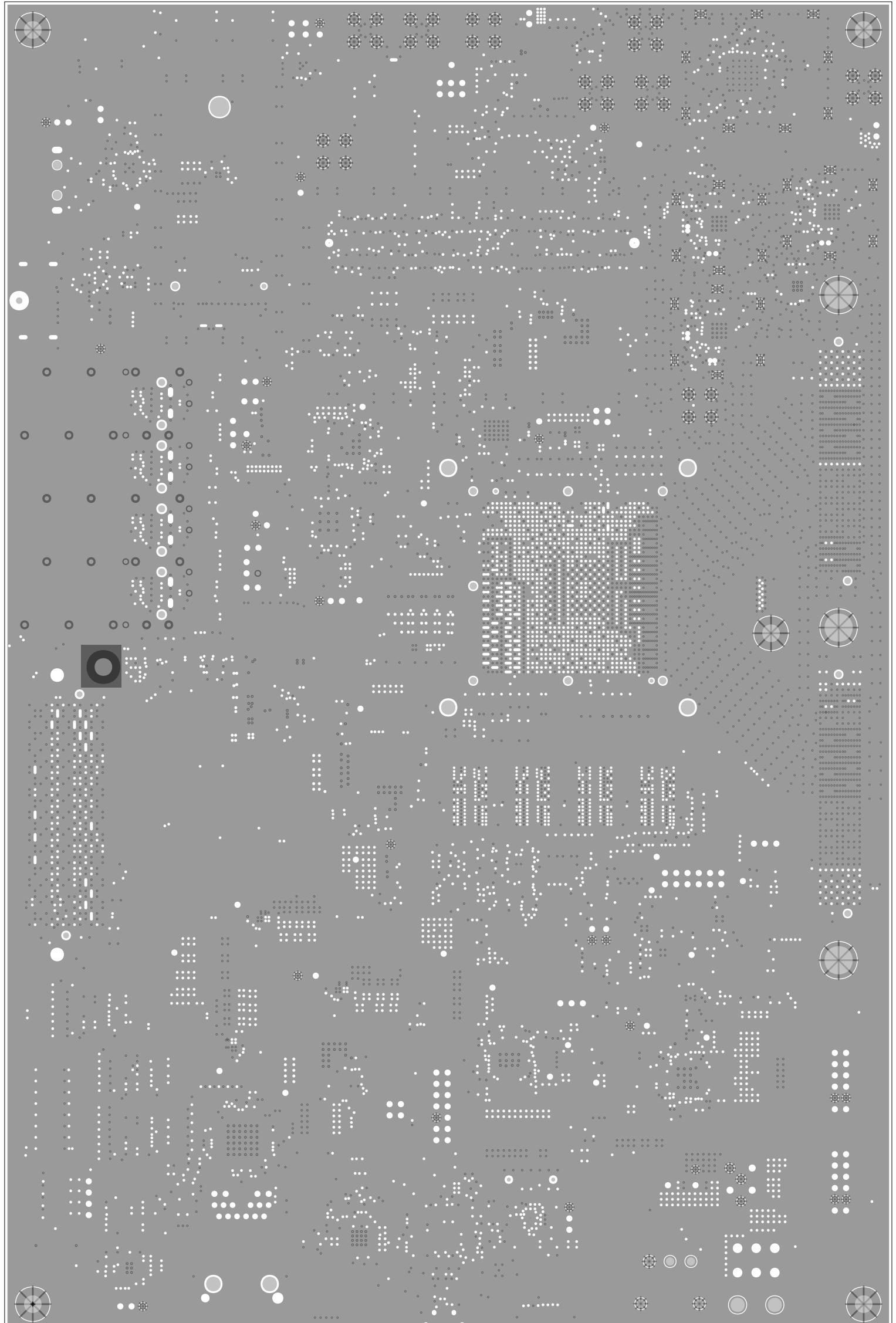
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L08_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
8	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



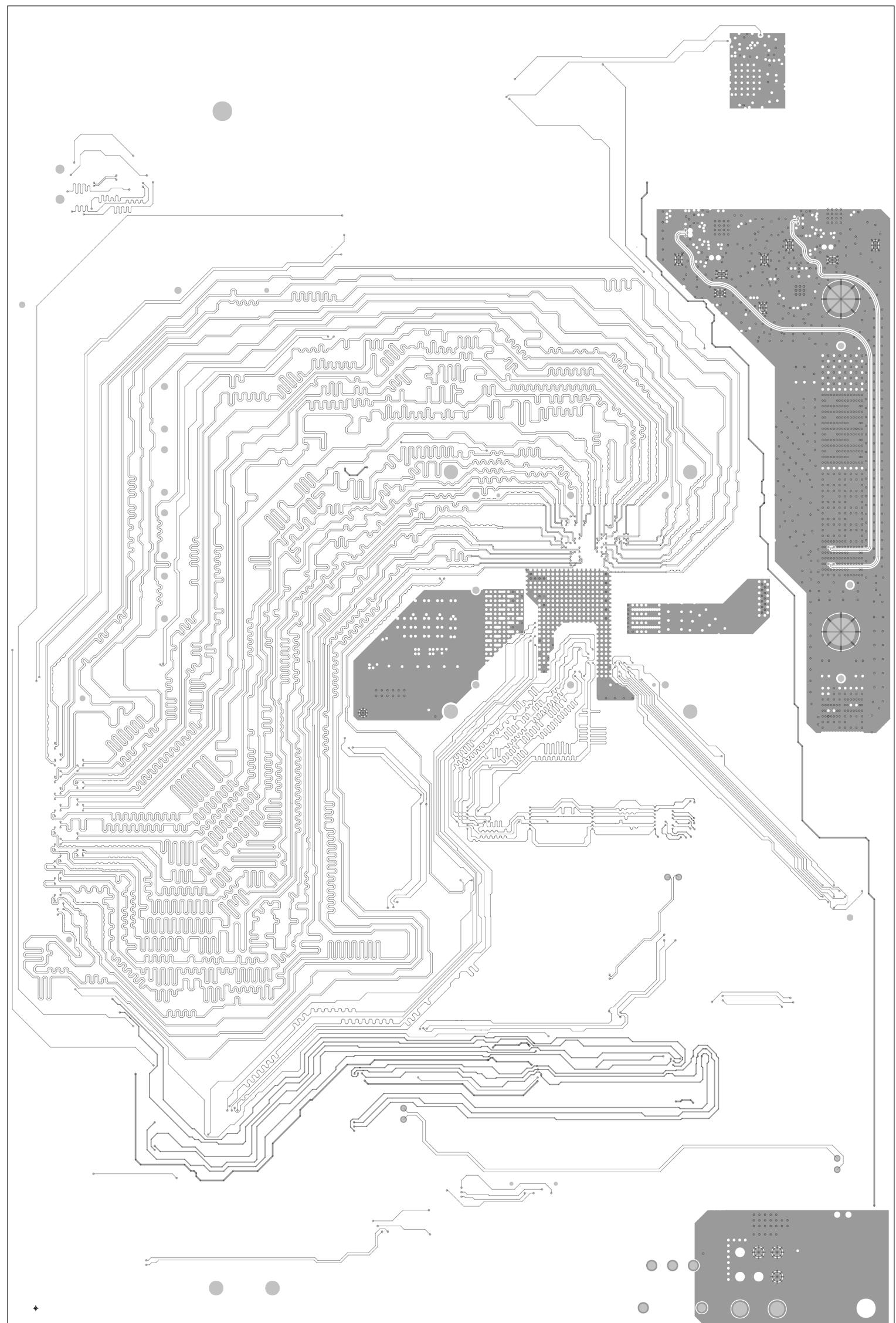
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L09_PWR
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
9	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



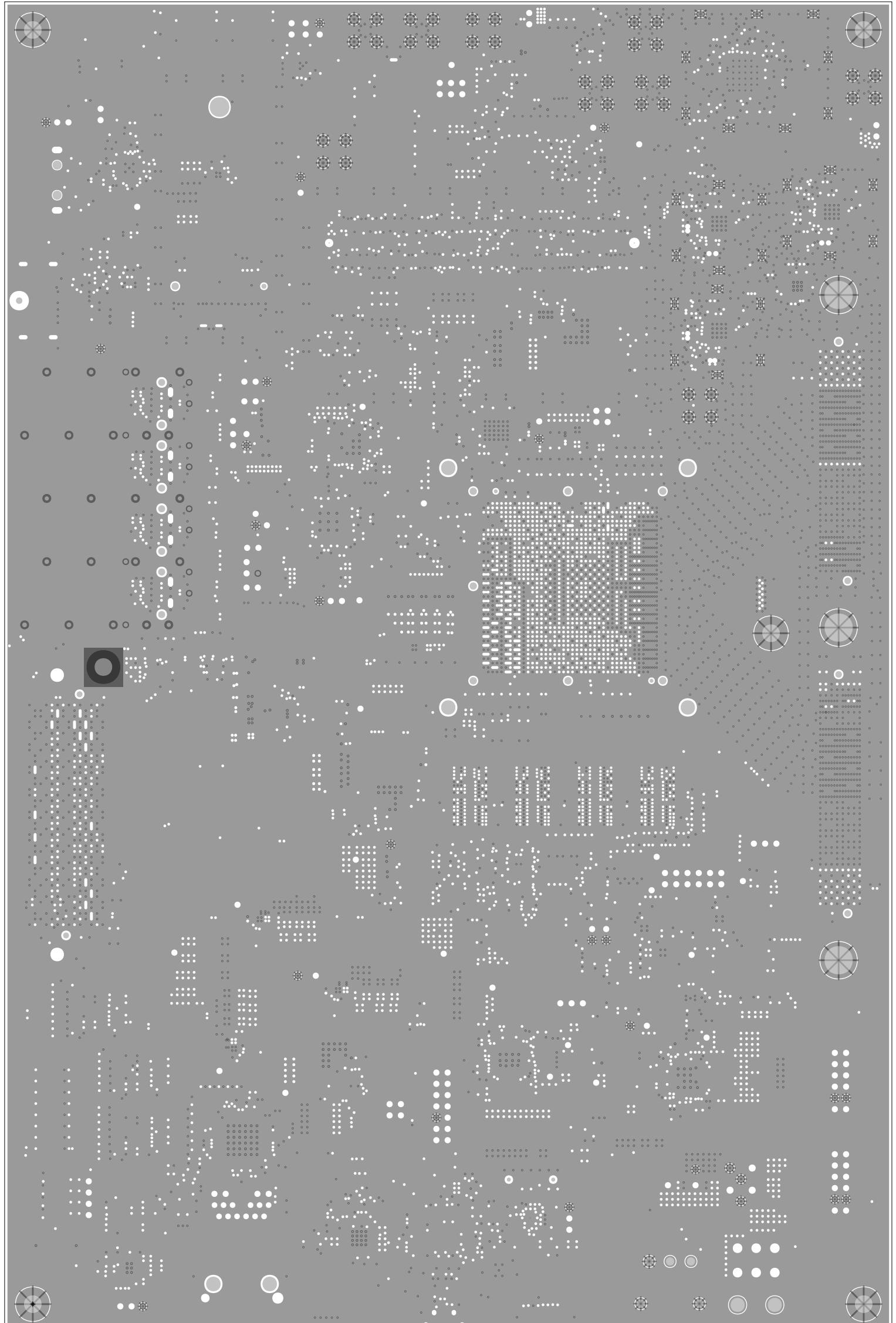
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L10_PWR
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
10	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



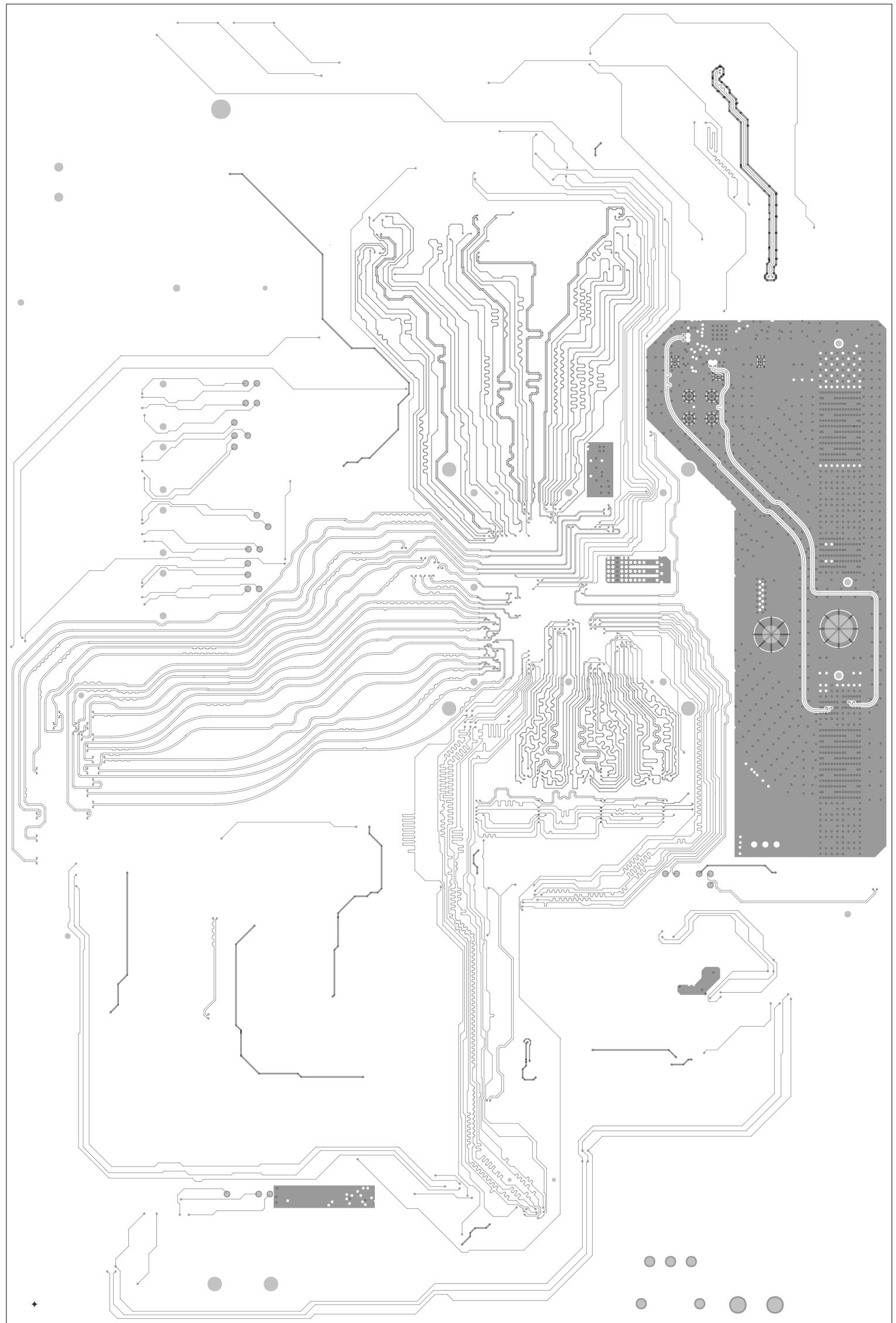
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L11_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
11	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



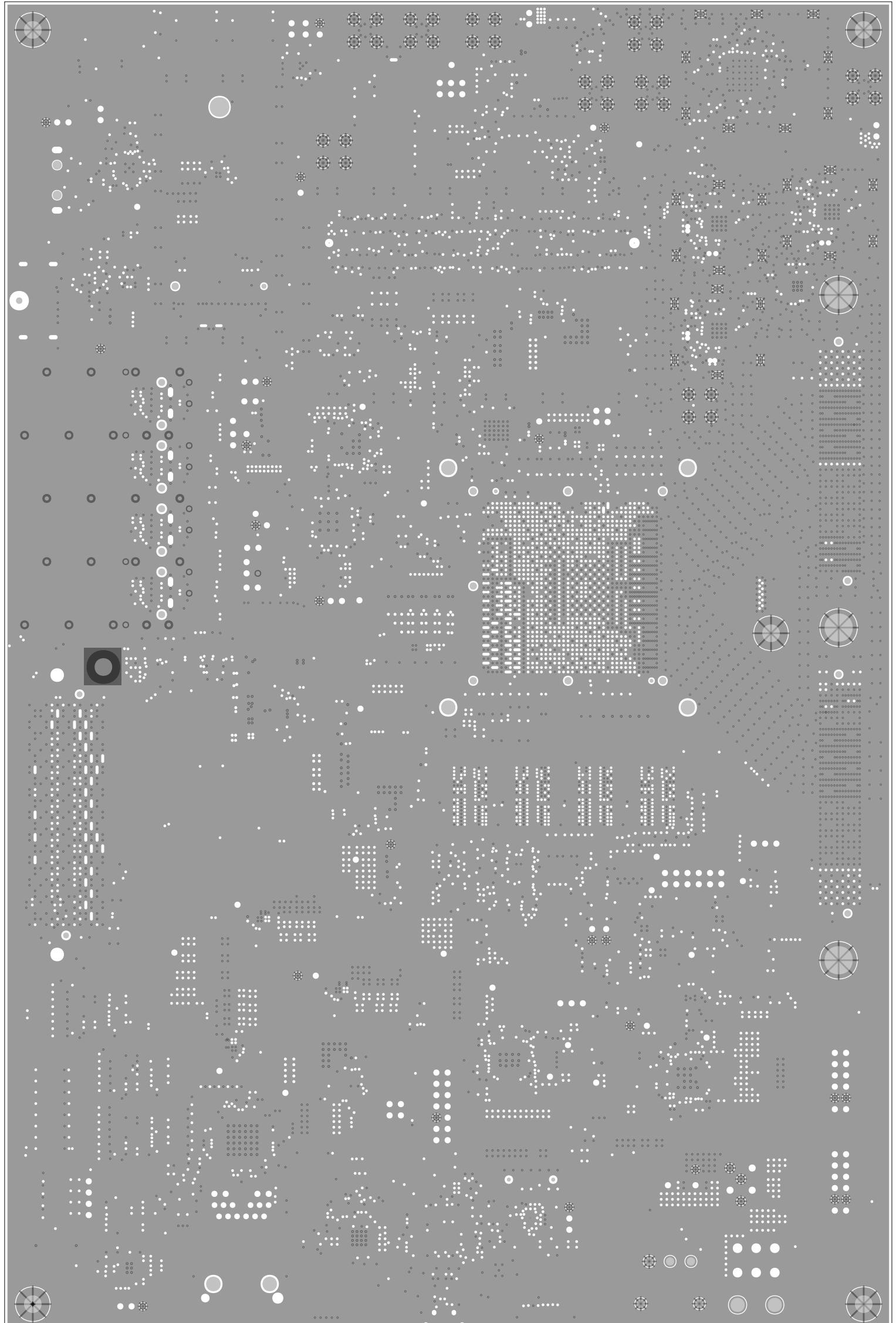
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L12_SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
12	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



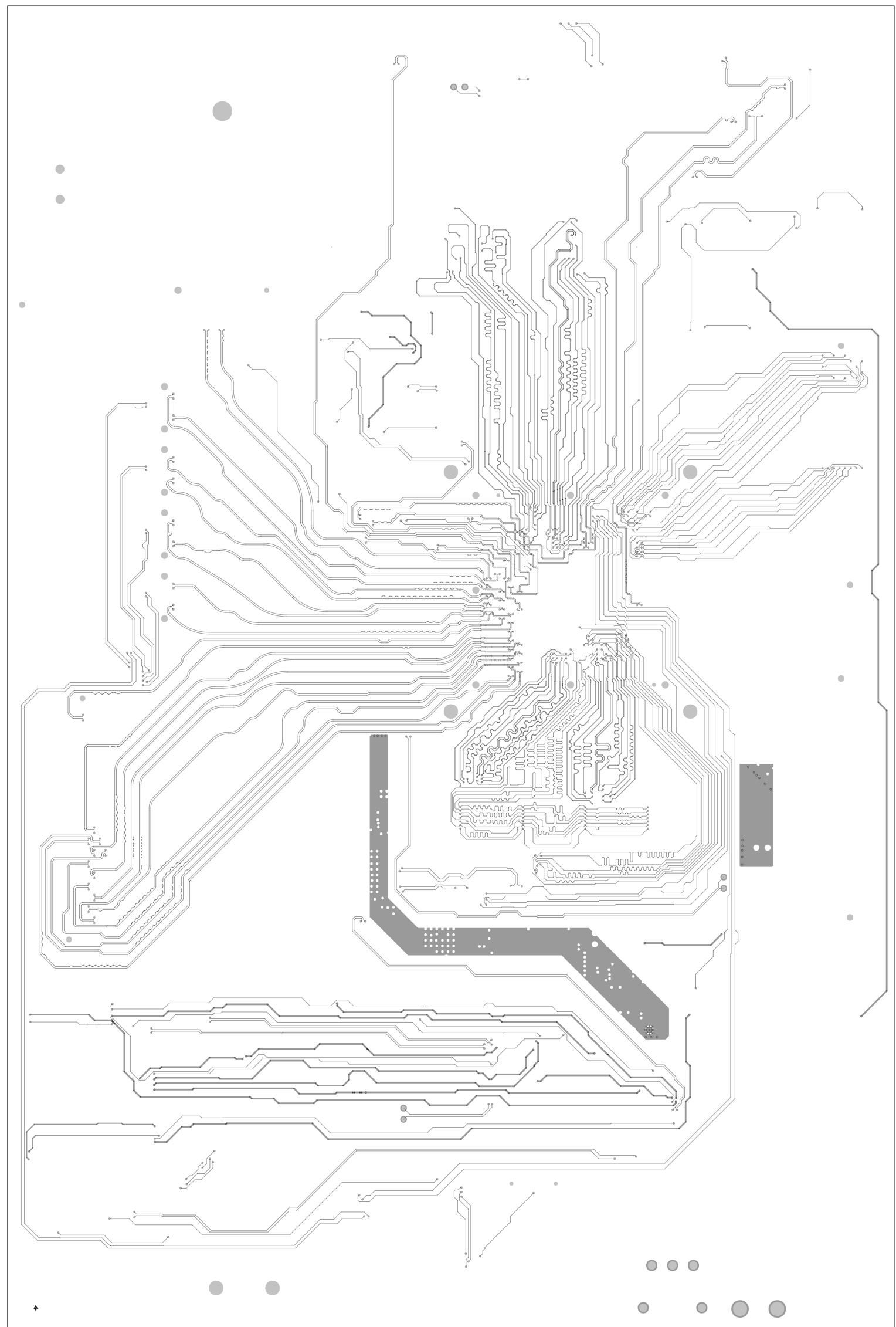
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L13_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
13	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



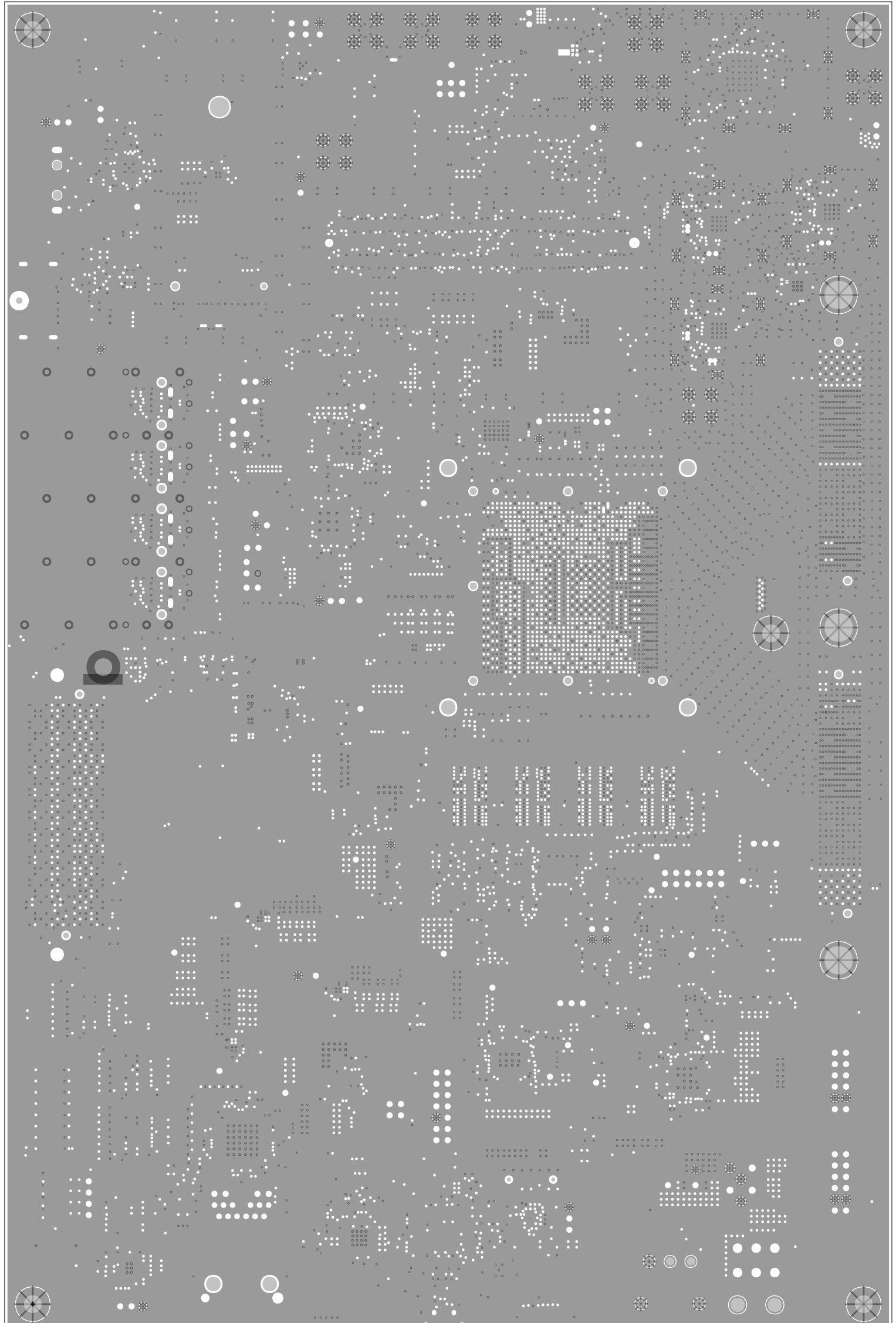
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L14 SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
14	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



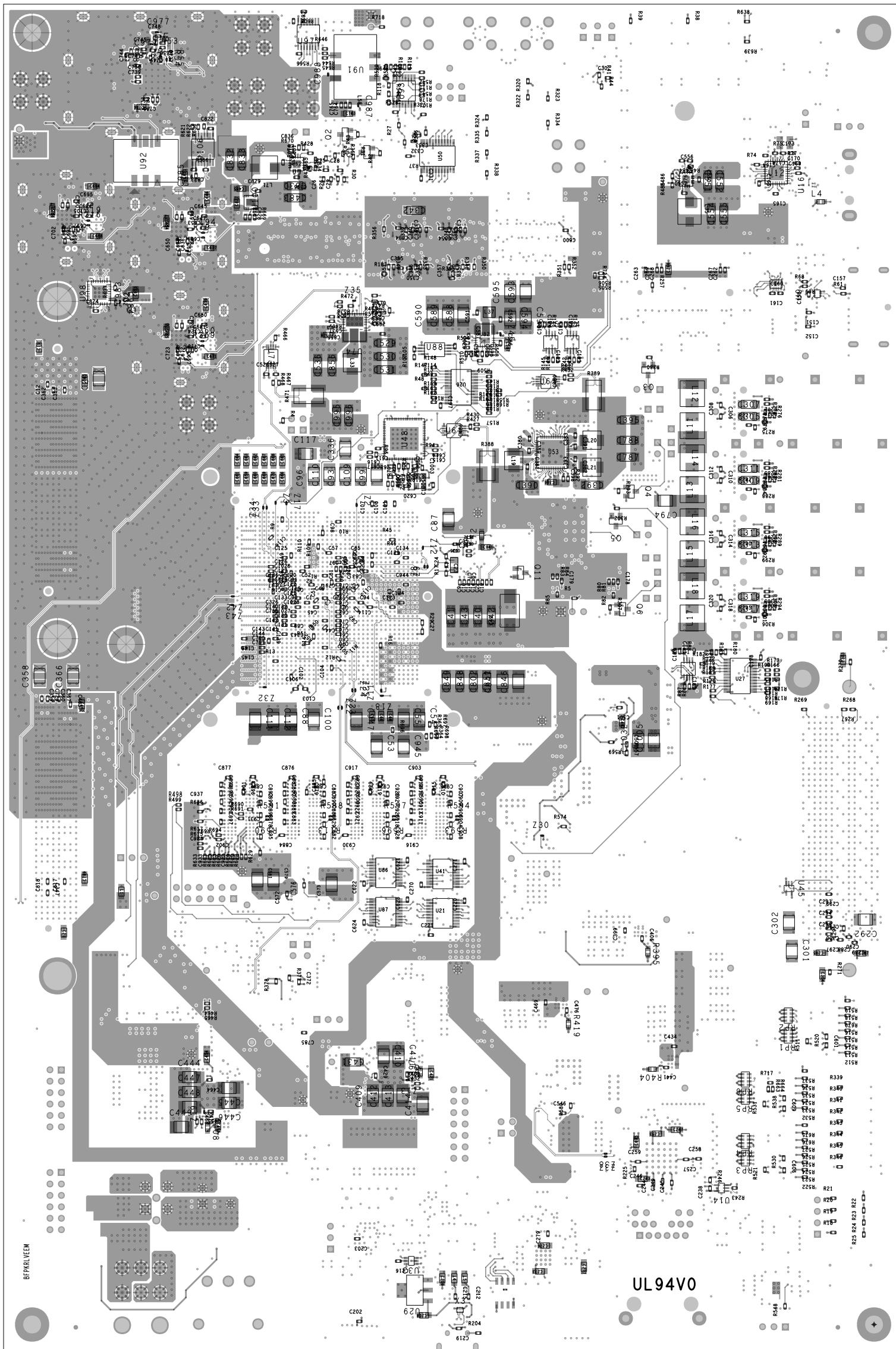
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L15_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
15	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



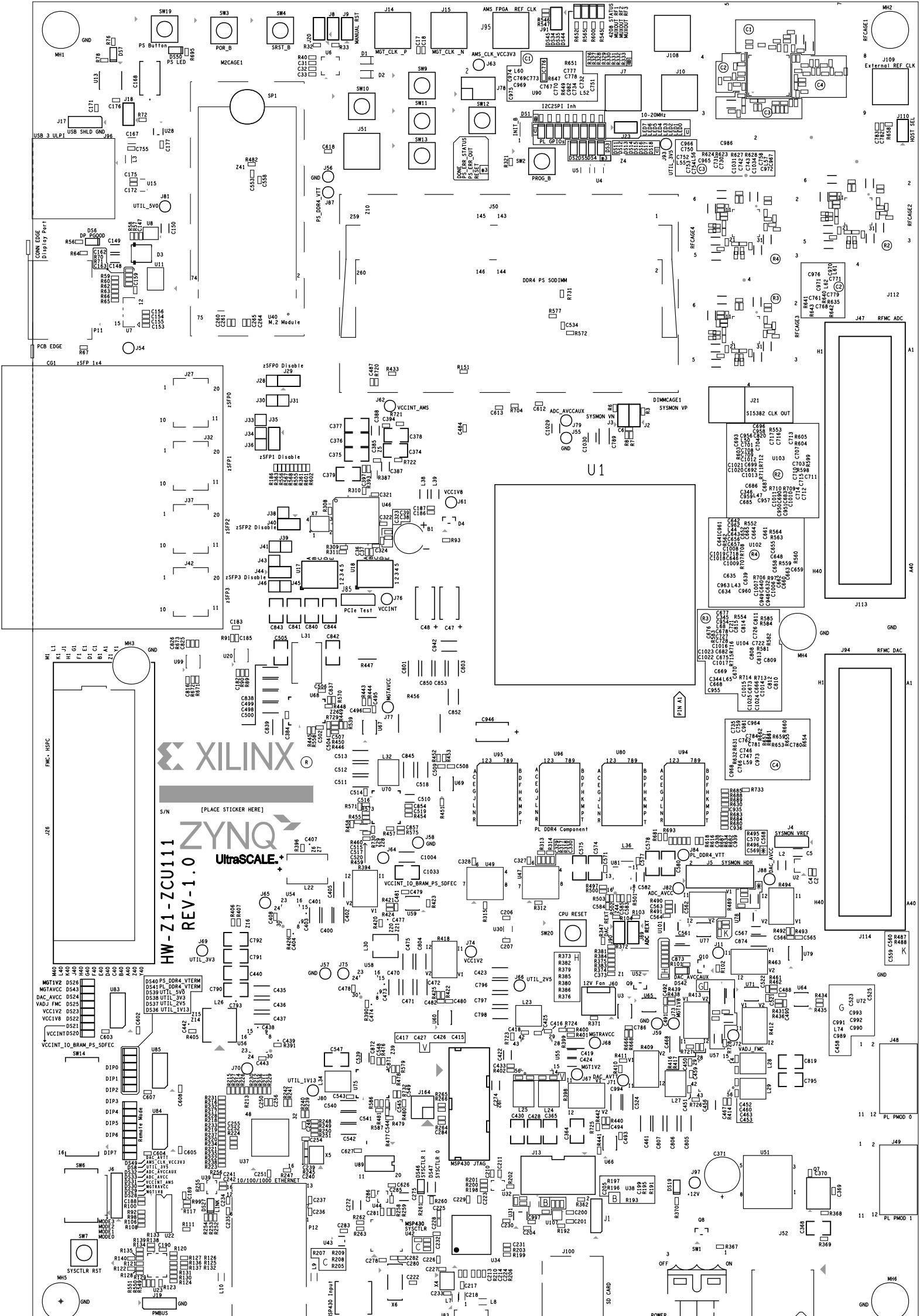
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L16_SIG
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
16	XILINX DOC USE ONLY VERSION:1.0 REVISION:01

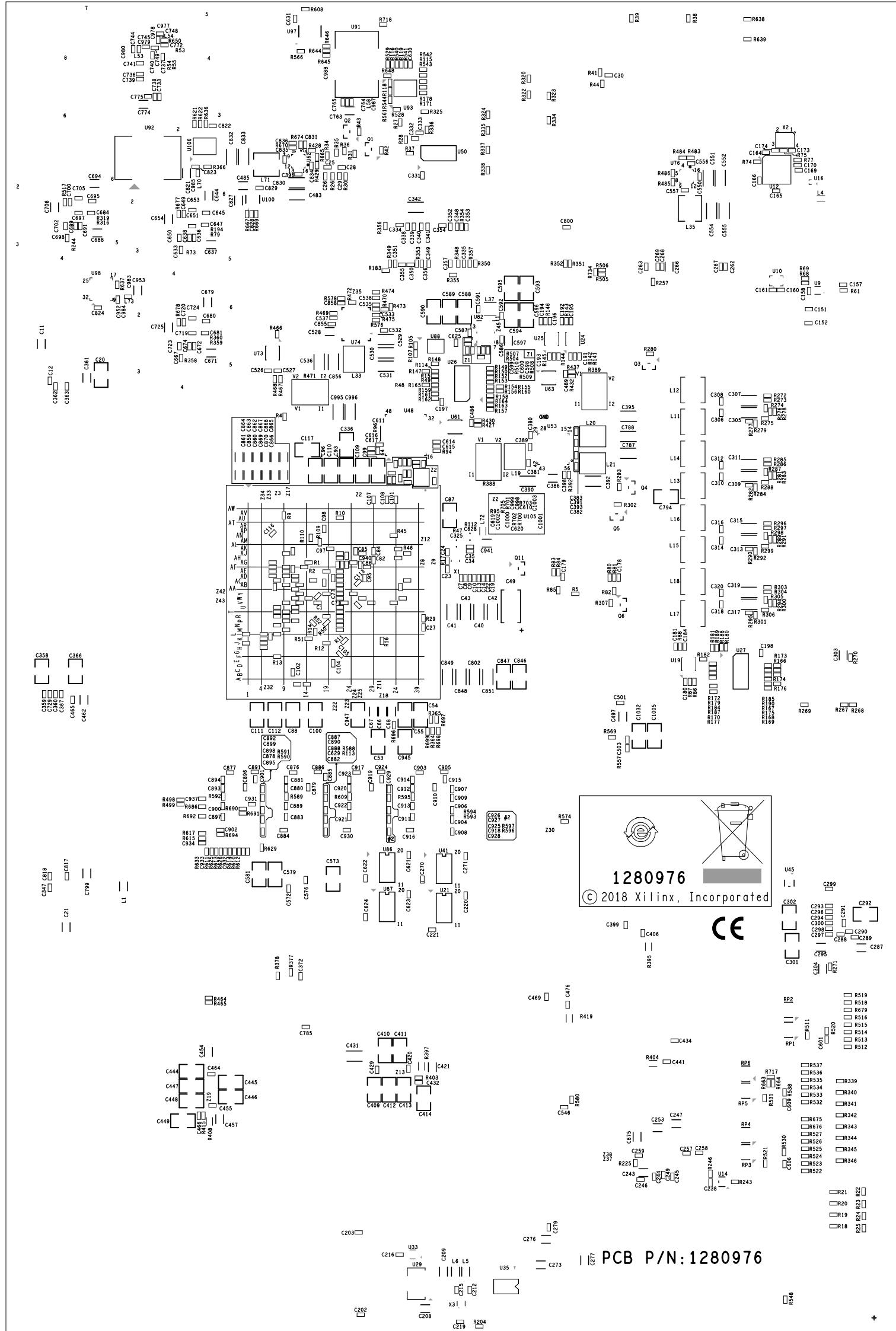


ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: L17_GND
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
17	XILINX DOC USE ONLY VERSION:1.0 REVISION:01

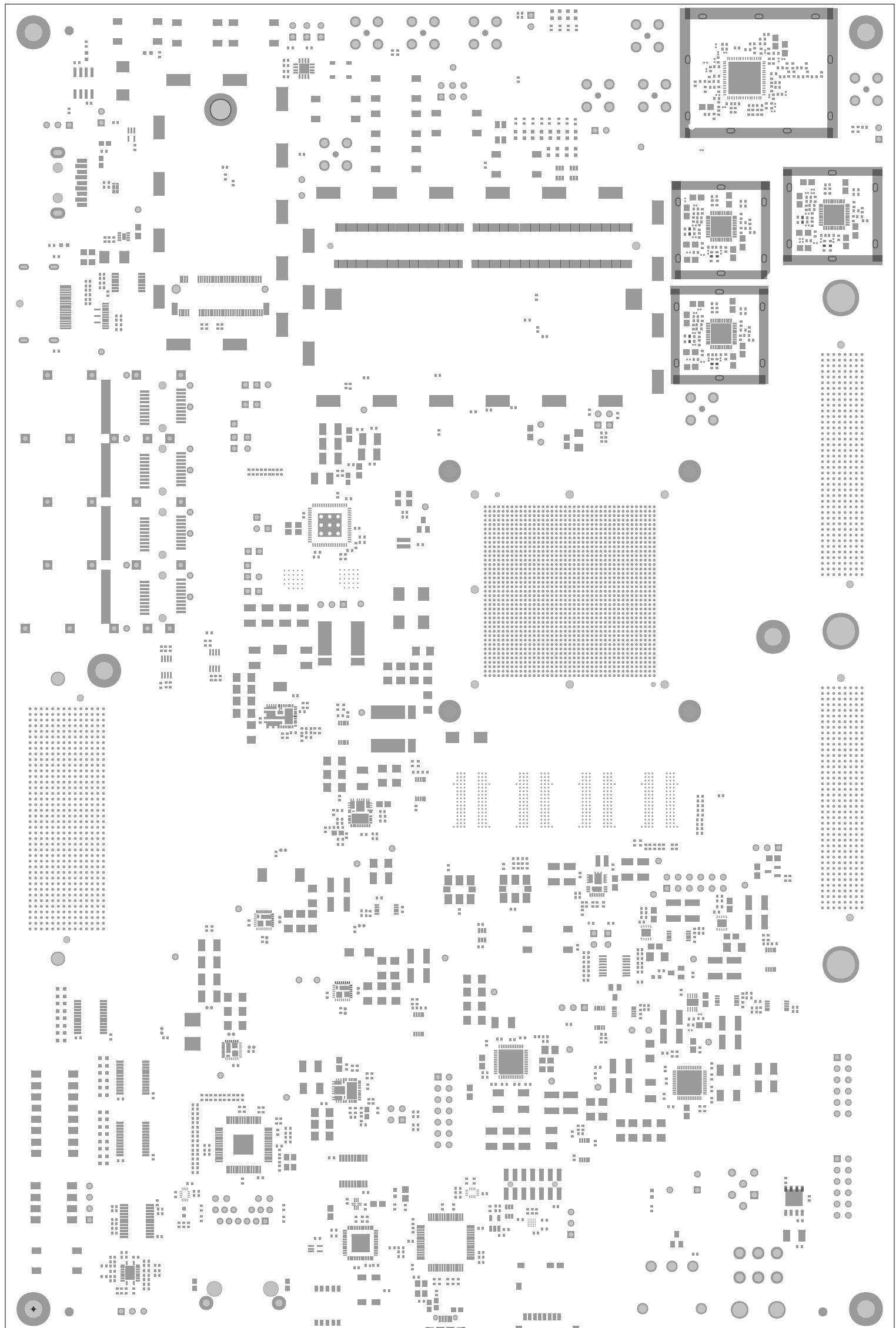


ARTWORK, ROHS COMPLIANT, HM-UT-ZCUT11	
PCB # 1580346	LAYER: BOTTOM_818
Described by Xilinx	DATE: 05\30\2018
BOARD designer: GREG I	PHONE: 408-878-0870
XILINX DOC USE ONLY VERSION:1.0 REVISION:01	18

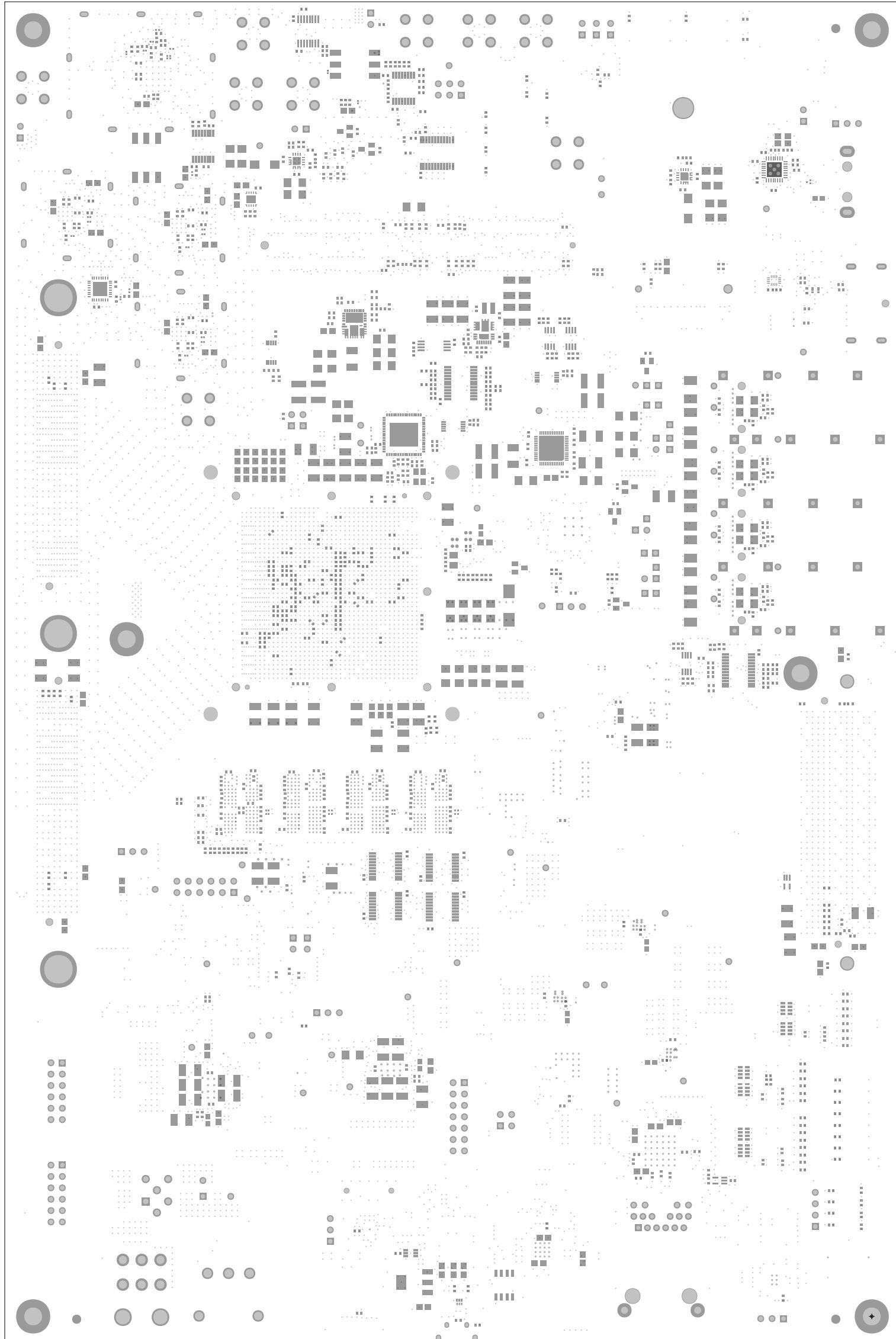




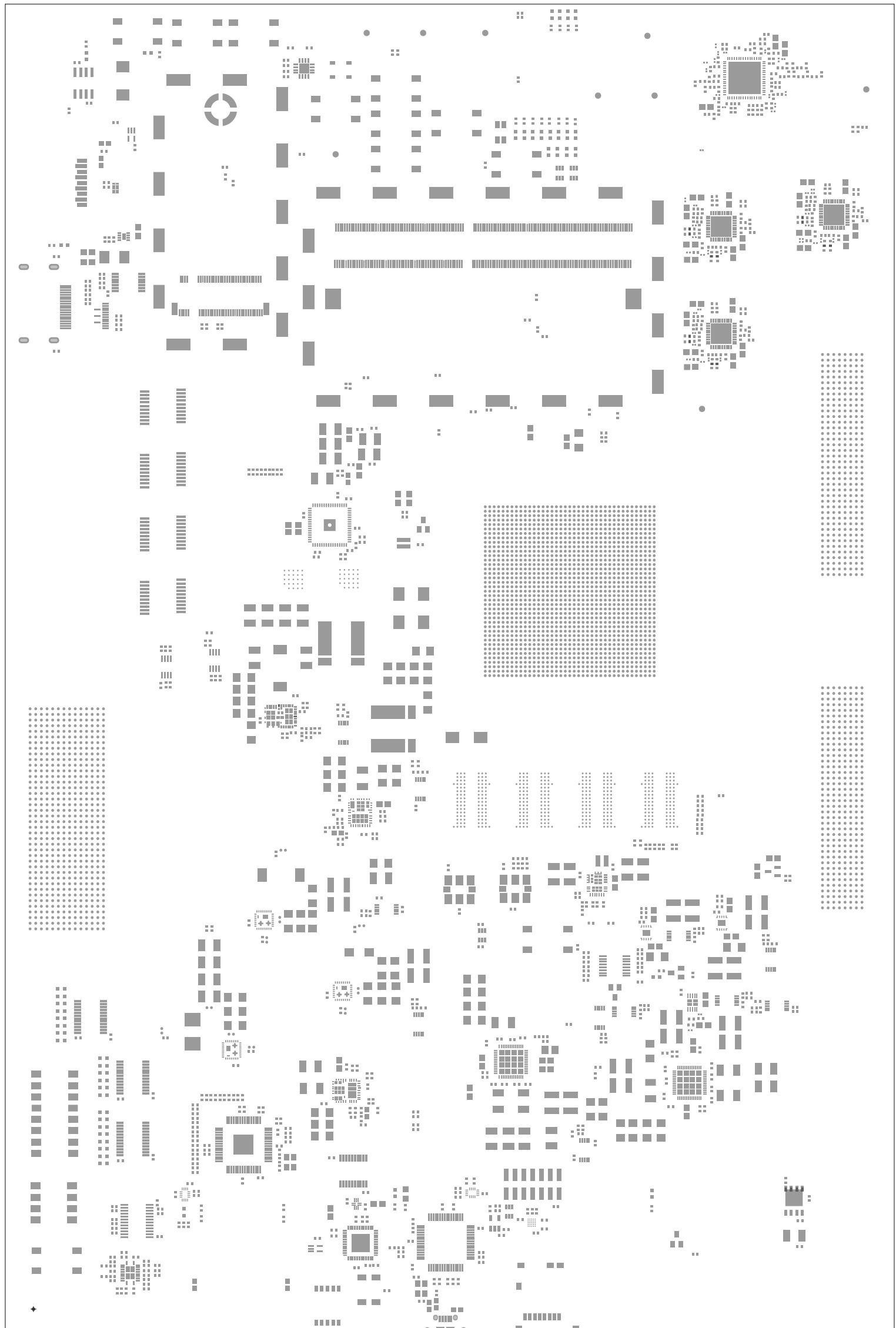
REVISION:01	VERSION:1.0	XILINX DOC USE ONLY	28
BOARD designer:	GE GE I	PHONE: 408-838-6870	
Designated PCB Xilinx	DATE: 05\30\2018	PCB # 1580a76	AYER: TOP_SILK
ARTWORK, ROHS COMPLIANT, HW-UT-2CUT11			



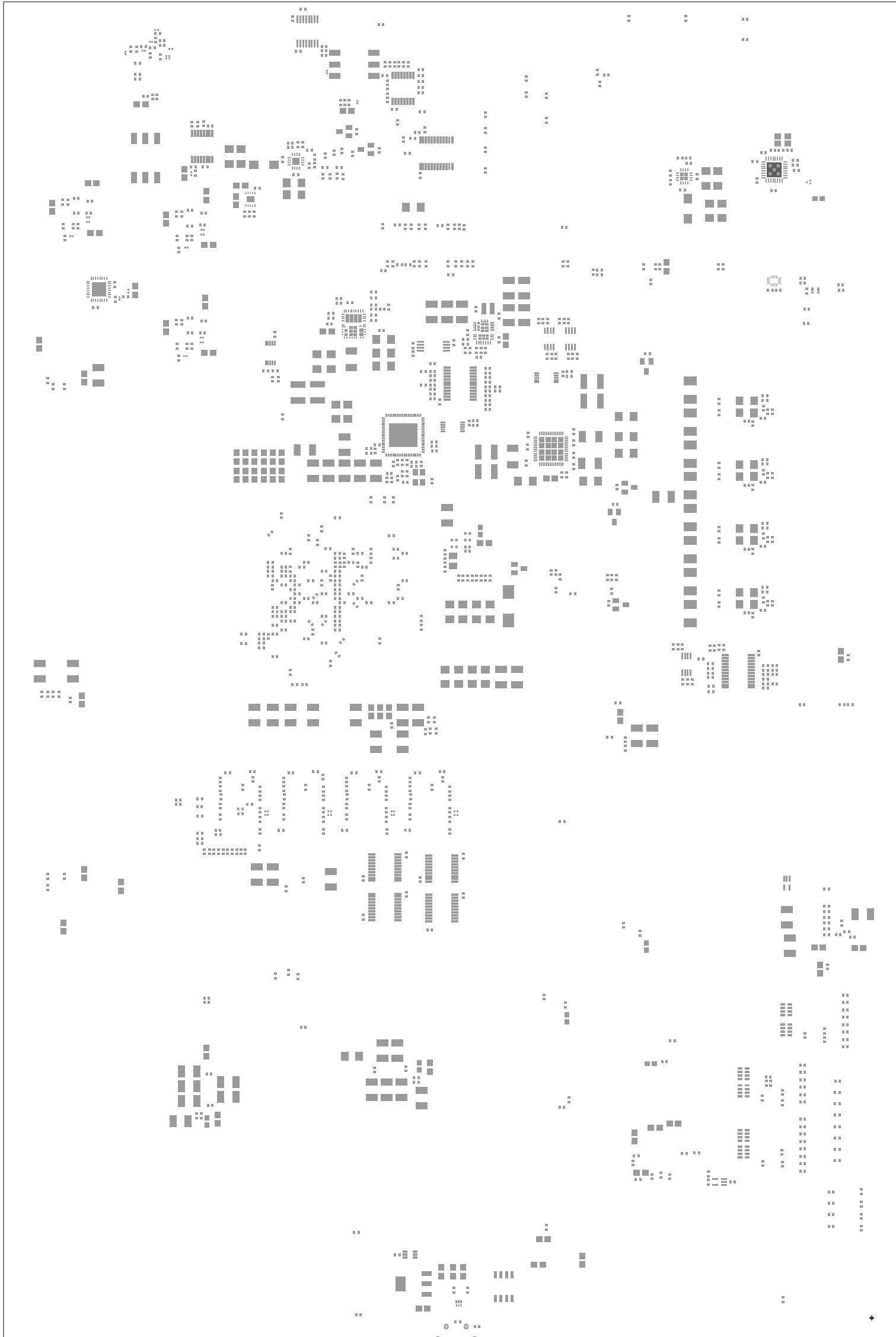
ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: MASK_TOP
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
31	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



REVISION:01	XILINX DOC USE ONLY
BOARD designer:	GREG I
PHONE:	408-870-6870
Designated PCB Manufacturer:	Xilinx
DATE:	05\30\2018
PCB #	1580376
ARTWORK, ROHS COMPLIANT, HM-UT-2CUT11	
MASK BOTTOM	LAYER:



ARTWORK, ROHS COMPLIANT, HW-U1-ZCU111	
PCB # 1280976	LAYER: PASTE_TOP
Designed by Xilinx	DATE: 05/30/2018
BOARD designer: GREG I	PHONE: 408-879-6870
29	XILINX DOC USE ONLY VERSION:1.0 REVISION:01



REVISION:01	XILINX DOC USE ONLY
BOARD designer:	GREG I
Designated PCB Xilinx	PHONE: 408-870-6870
DATE: 05\30\2018	LAYER: BOT_PASTE
PCB # 1580376	ARTWORK, ROHS COMPLIANT, HM-UT-CUT11