

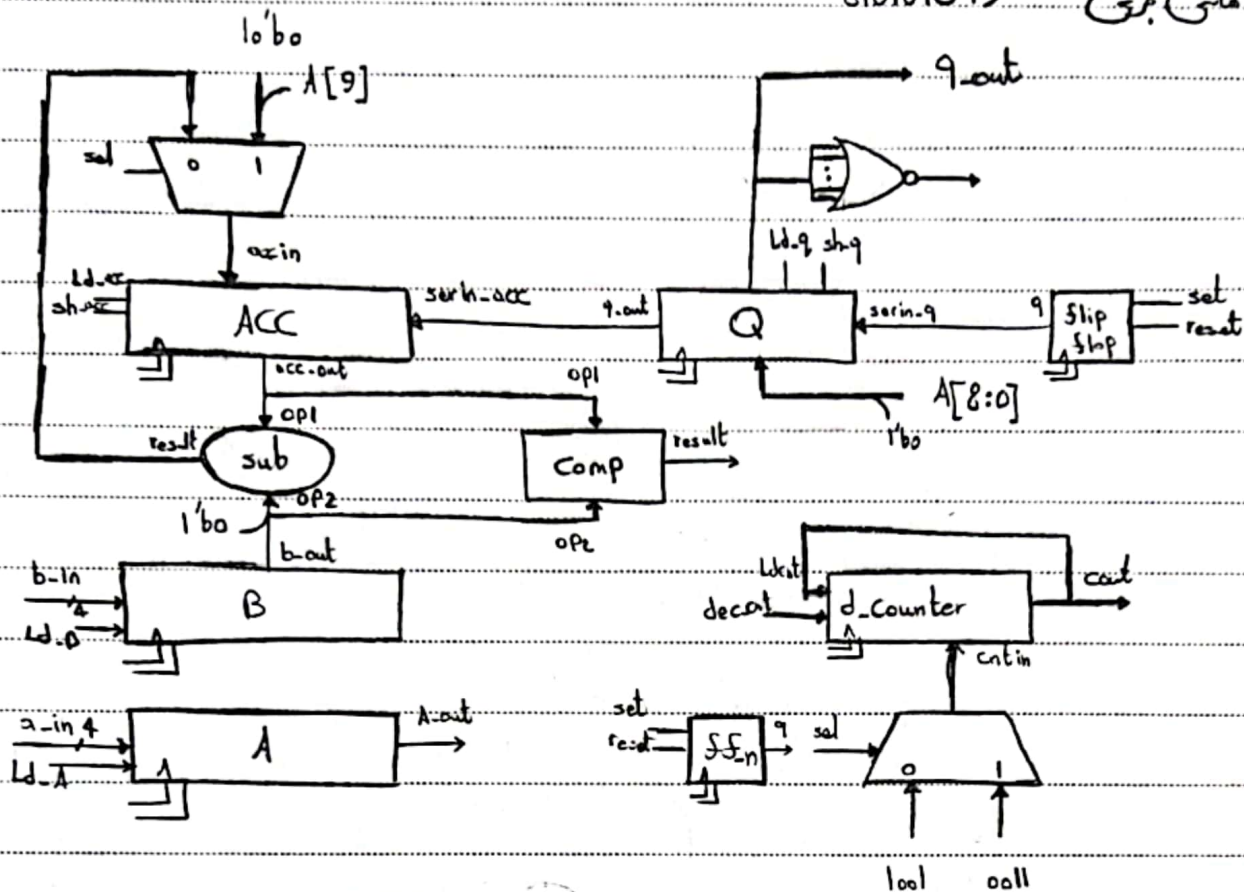
« Data path ..... مسیری داده »

810101496

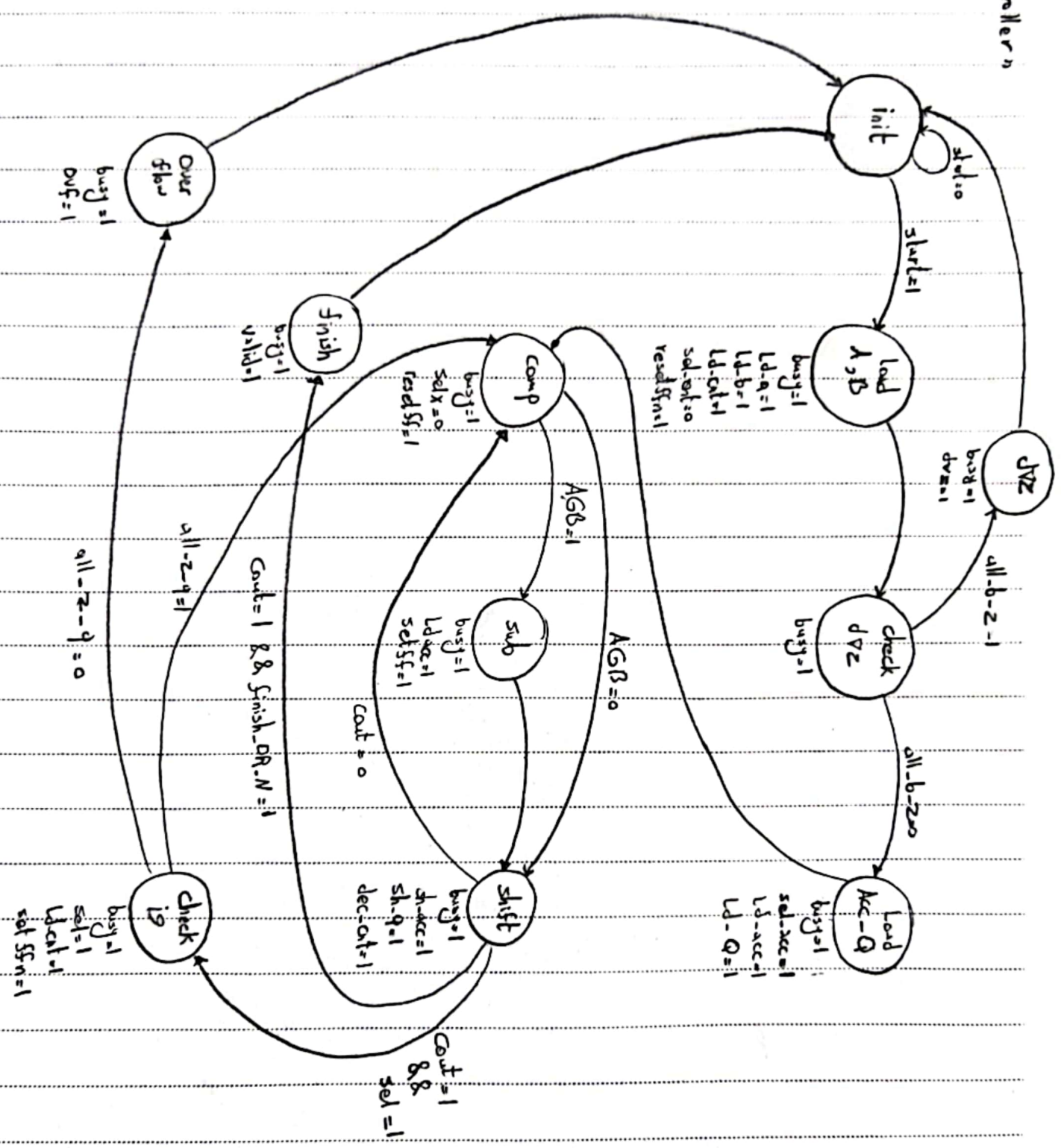
سبعان لکھ روپے

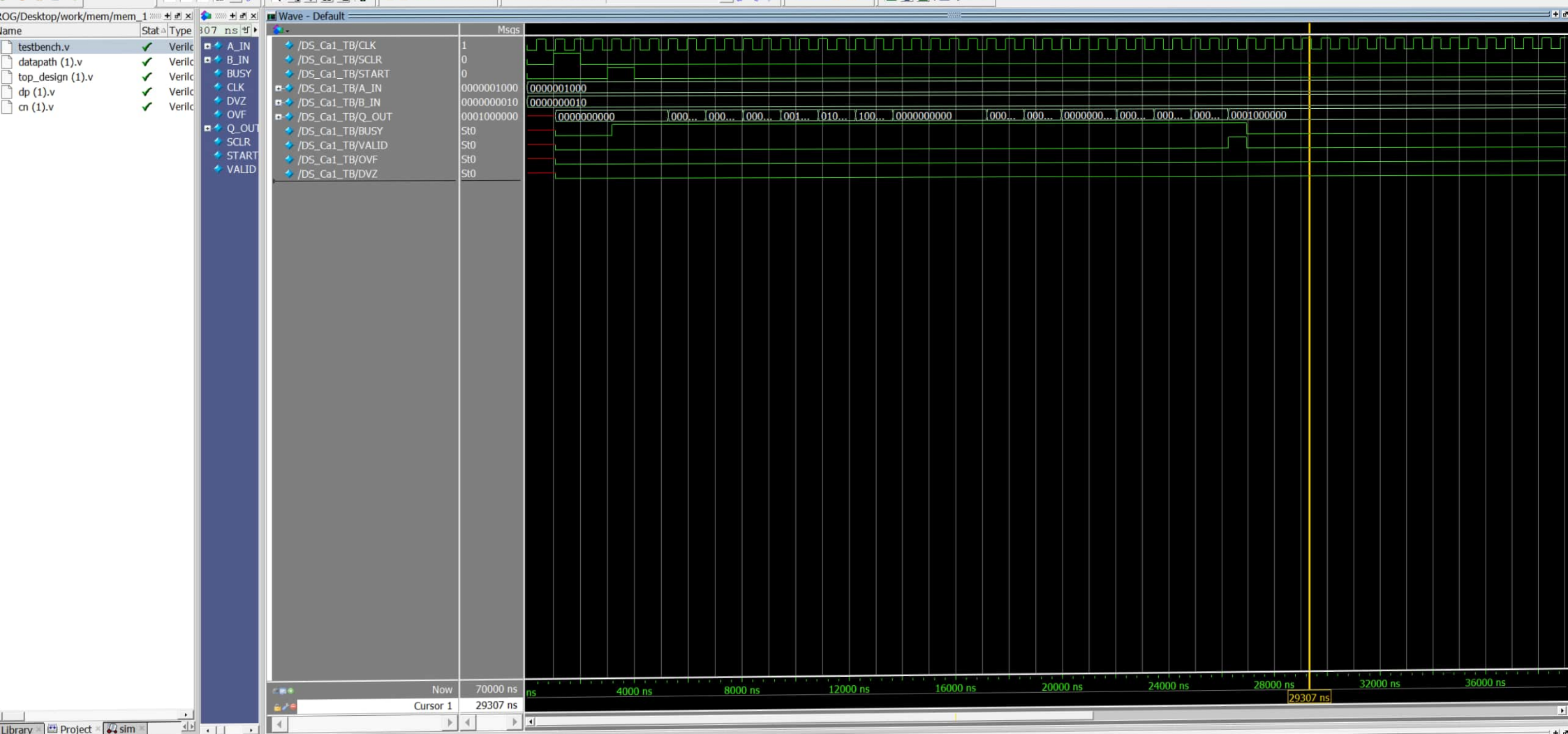
810101549

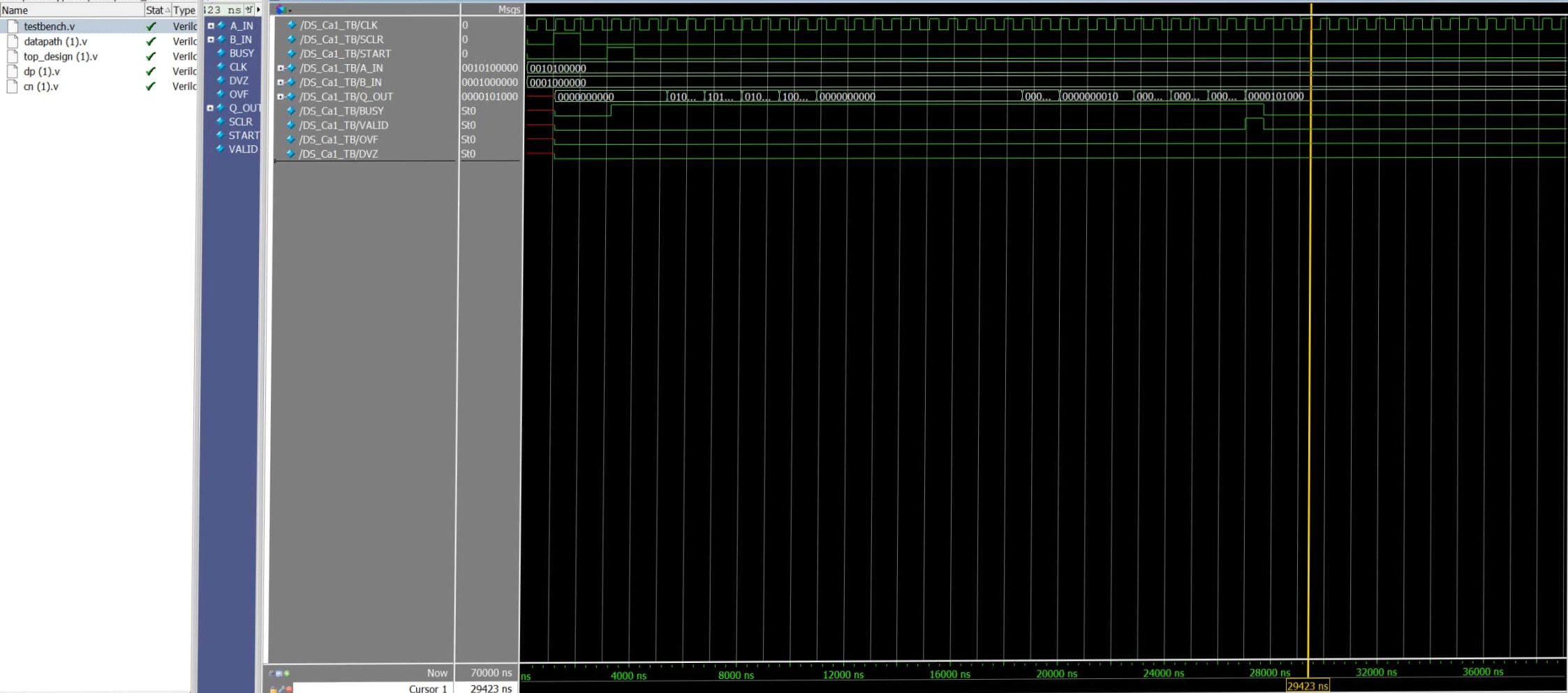
سىر ئۆيدى ھائى (بىرى)



"Calculator"





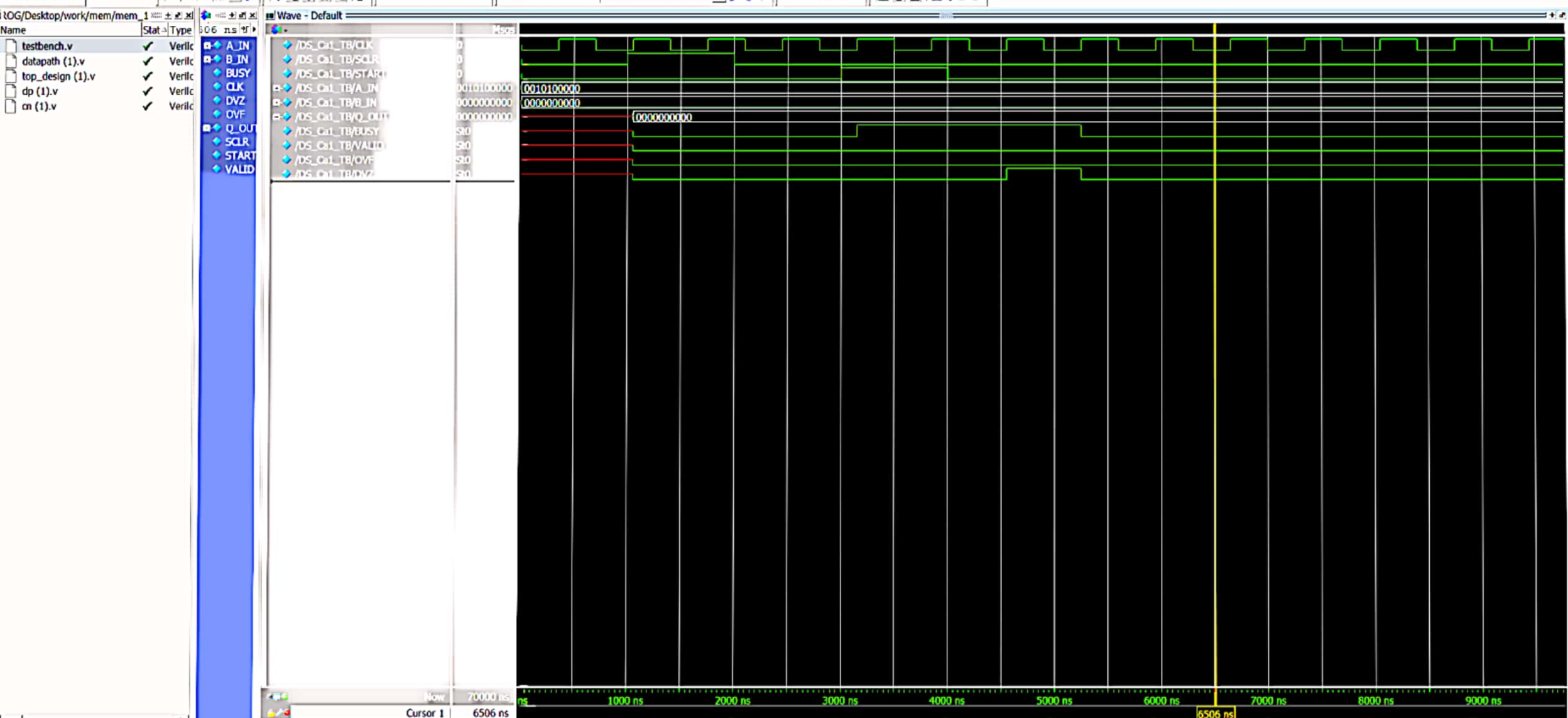
Now: 70000 ns  
Cursor 1: 29423 ns

Library Project sim

Transcript

IM 17&gt;





Name	Status	Type
testbench.v	✓	Verilog
datapath (1).v	✓	Verilog
top_design (1).v	✓	Verilog
dp (1).v	✓	Verilog
cn (1).v	✓	Verilog

The screenshot shows the 'Wave - Default' window with a list of signals and their values. The signals are listed on the left, and their corresponding message values are listed on the right. The signals are: A\_IN, B\_IN, BUSY, CLK, DVZ, OVf, Q\_OUT, SCLR, START, and VALID. The message values are: 1, 0, 0, 0, 0010100000, 0000100000, 0001010000, S0, S0, S0, and S0.

Signal	Message
A_IN	1
B_IN	0
BUSY	0
CLK	0
DVZ	0010100000
OVf	0000100000
Q_OUT	0001010000
SCLR	S0
START	S0
VALID	S0

