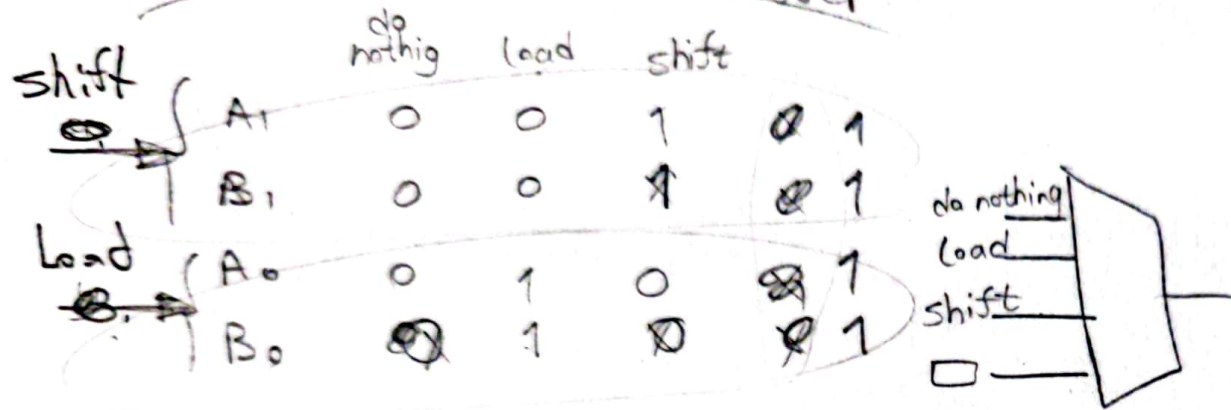
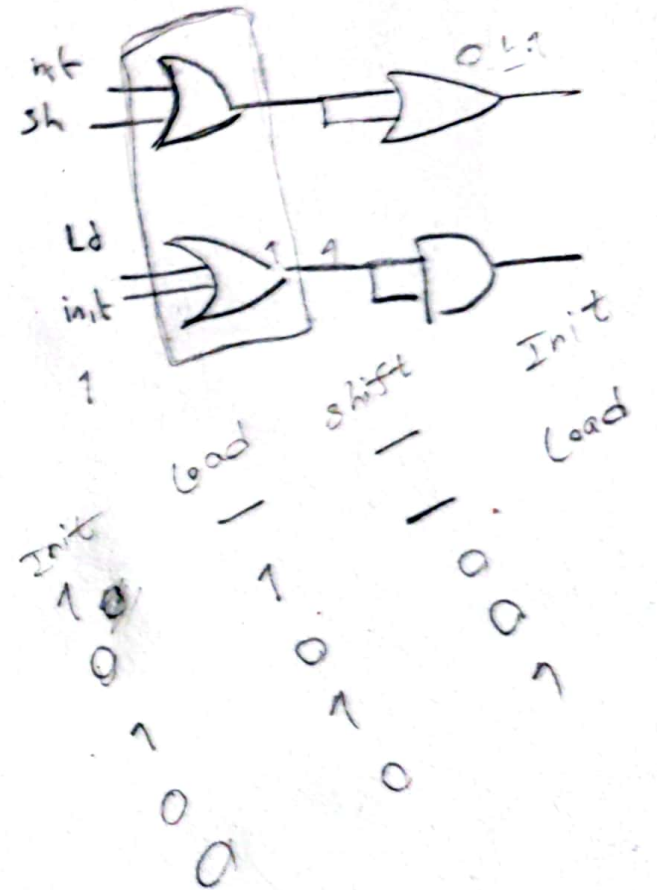


shift-register-inbuffer



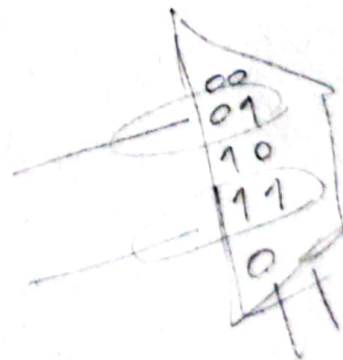
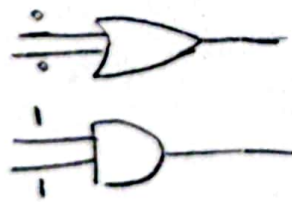
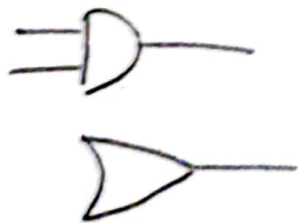
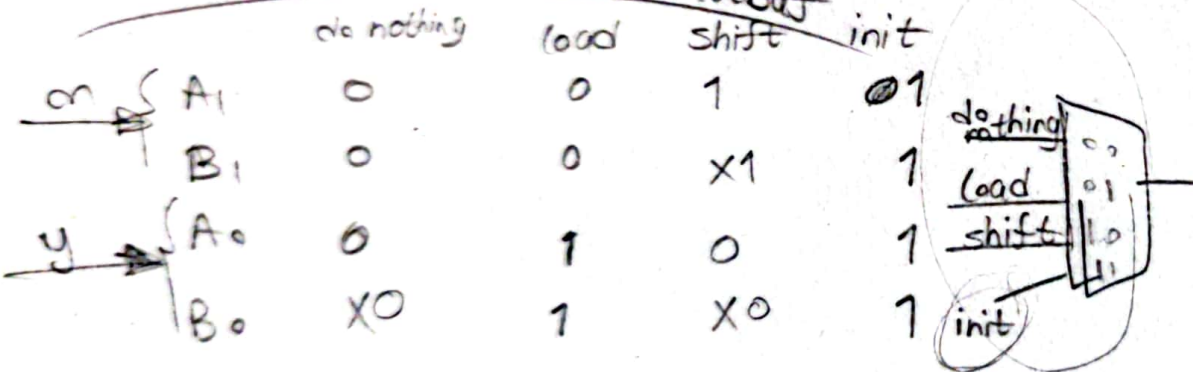
Ld 1
 init 0
 sh 0

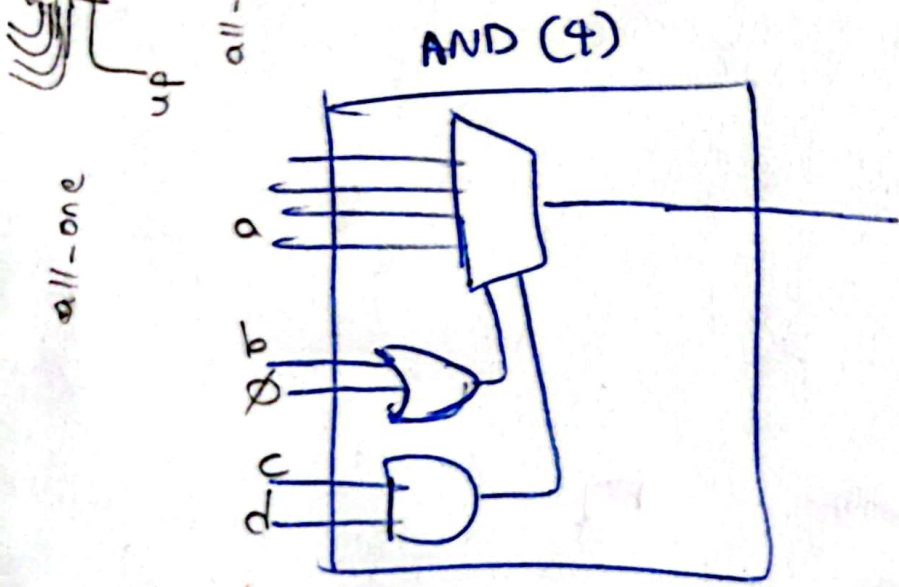
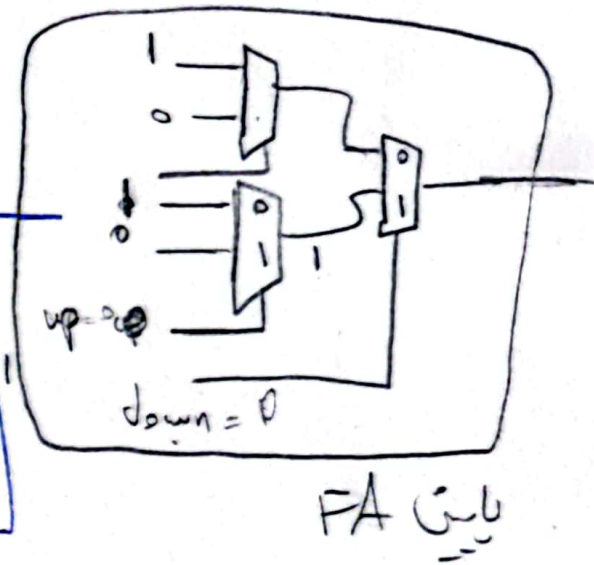
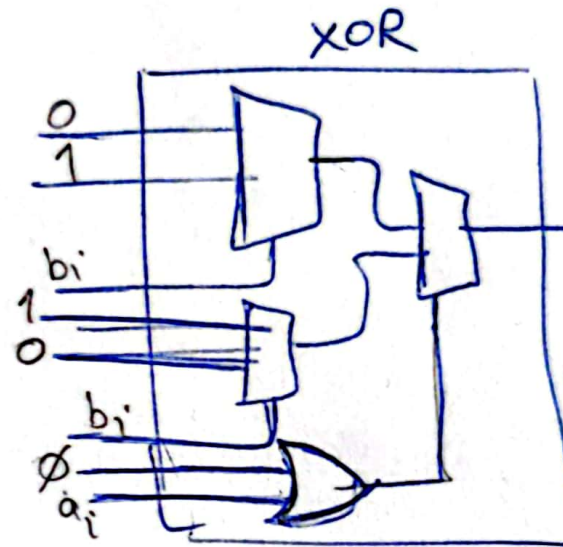
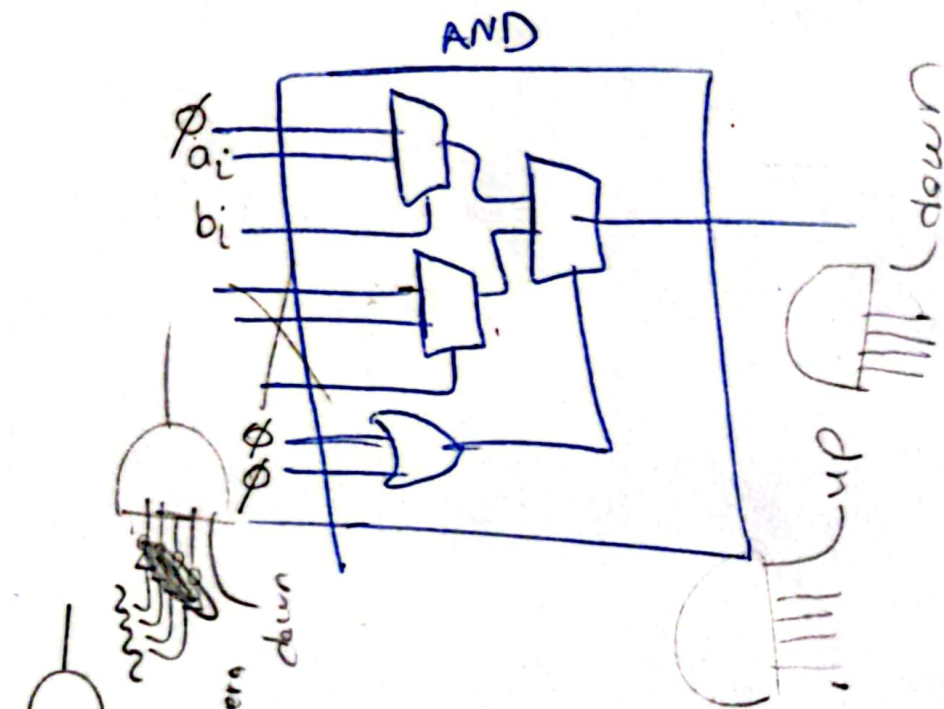
Ld 0
 init 1x
 sh 0x
 1 1 0x
 1 1 x0



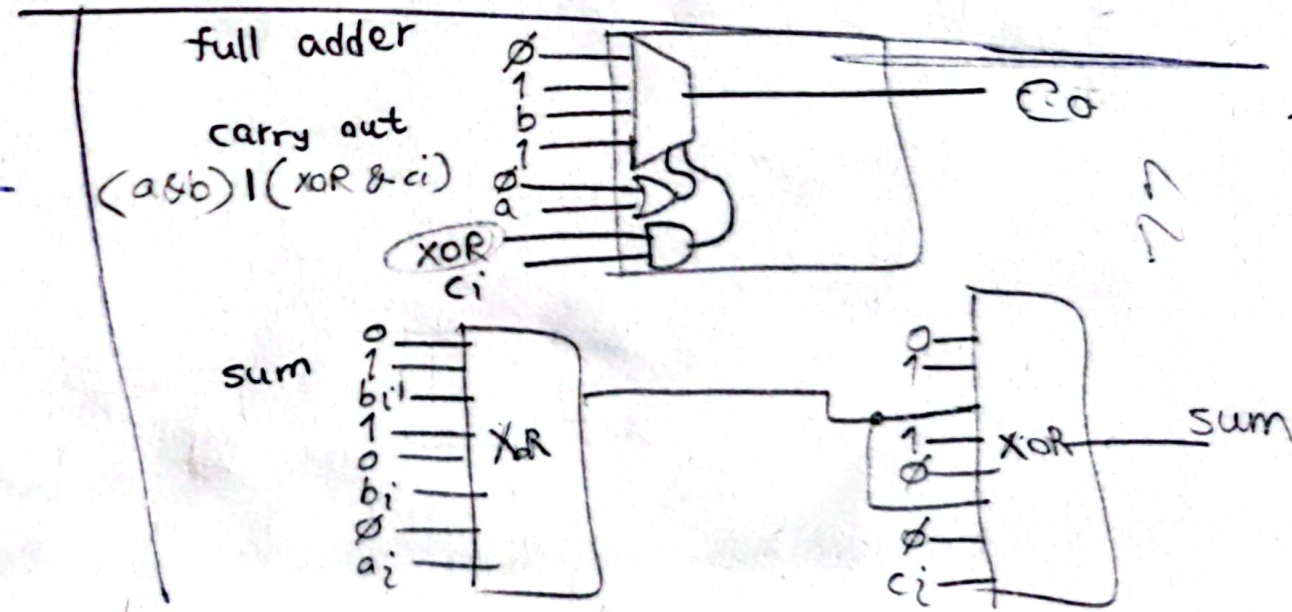
init 0

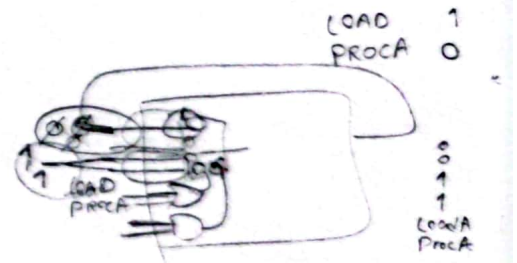
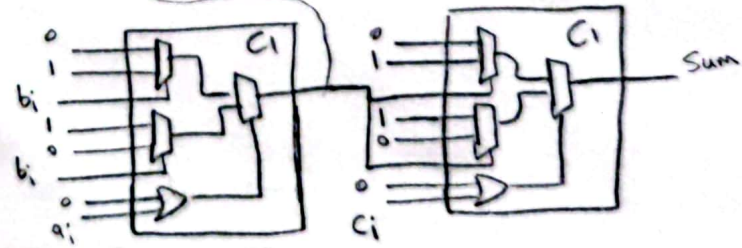
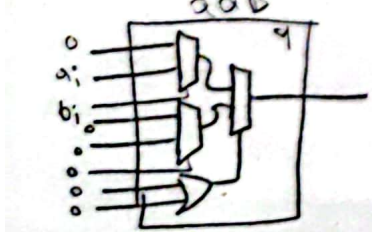
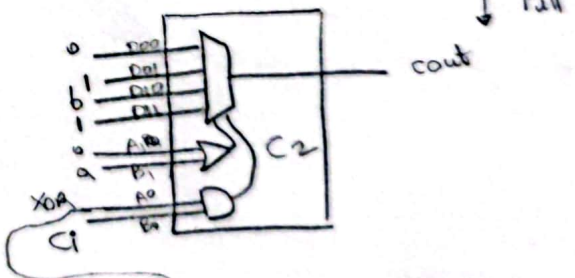
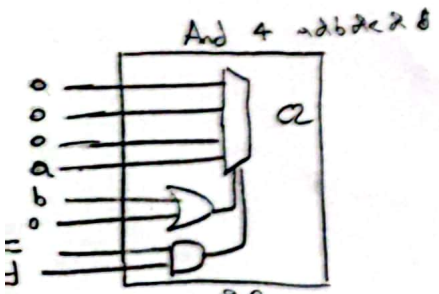
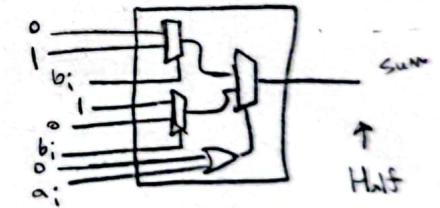
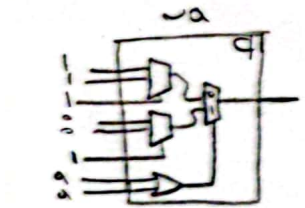
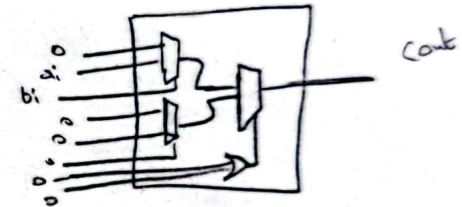
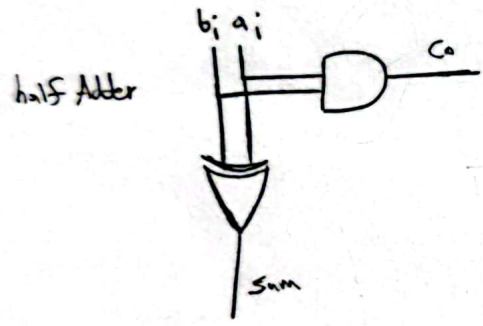
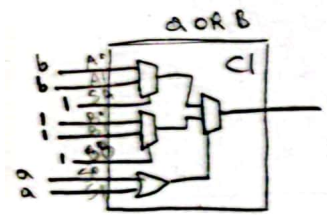
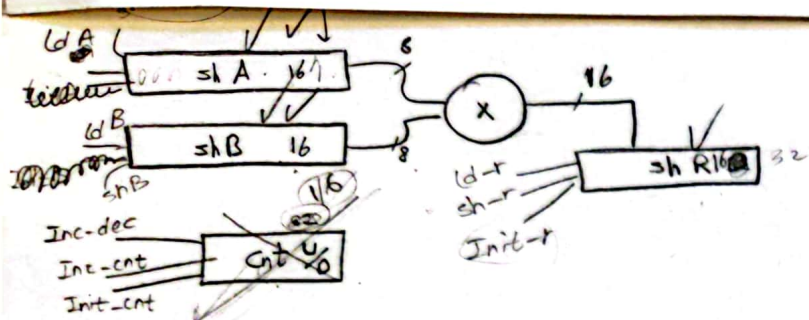
shift-register-outbuffer





$$\text{XOR} = \bar{a}b + a\bar{b}$$





0 - - -
0 - - -
0 - - -

