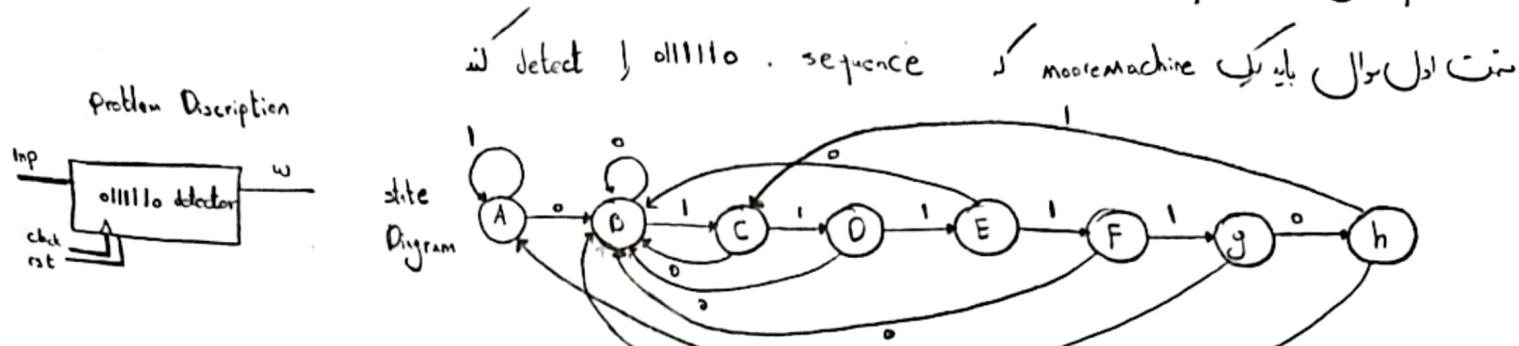


در این بخش (C45) سلسله جنین بیشتر دارد و سوال فرمایه ترتیب هر کدام از این بین سارا اجمل داده و در آخر این بیکد مارا بهم متعلق ترین خوبی گرفته.



state Table

state	0	1	w
000 A	B	A	0
001 B	C		0
010 C	D		0
011 D	E		0
100 E	F		0
101 F	G		0
110 G	H	A	0
111 H	C	B	1



$V_2 V_1 V_0$	0	1	w
000	001	000	0
001	001	010	0
010	001	011	0
011	001	100	0
100	001	101	0
101	001	110	0
110	111	000	0
111	001	010	1

Transition Table  
↓ f = Excitation Table

$V_1 V_2$

	..	01	11	b
..	0	0	1	0
01	0	0	1	0
11	0	0	0	1
10	0	1	0	0

$D_2$

$V_1 V_2$

	..	01	11	b
..	0	0	0	0
01	0	0	1	1
11	0	0	1	0
10	0	1	0	1

$D_1$

$V_1 V_2$

	..	01	11	b
..	0	0	0	0
01	1	1	0	0
11	1	1	0	0
10	1	0	0	1

$D_0$

$V_1 V_2$

	..	01	11	b
..	0	0	0	0
01	0	0	0	0
11	0	1	0	0
10	0	0	0	0

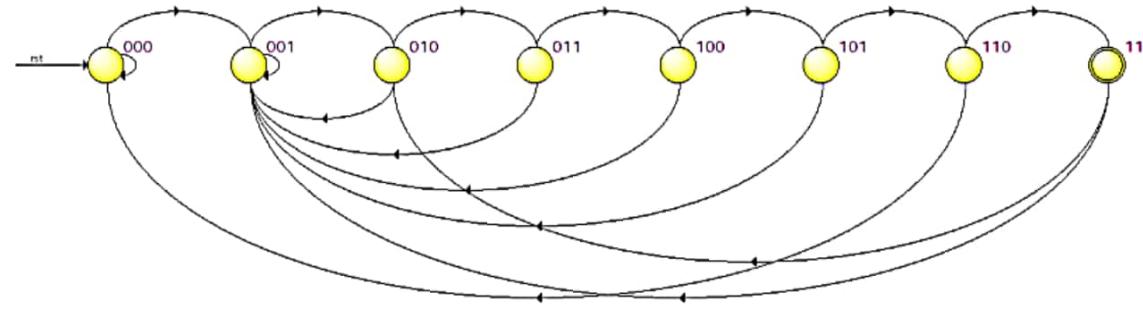
w

$$D_2 = \text{inp}V_2\bar{V}_1 + \text{inp}\bar{V}_2V_1V_0 + \bar{\text{inp}}V_2V_1\bar{V}_0$$

$$D_1 = \text{inp}\bar{V}_1V_0 + \text{inp}V_2V_0 + \bar{\text{inp}}V_2V_1\bar{V}_0 + \text{inp}\bar{V}_2V_1\bar{V}_0$$

$$D_0 = \bar{\text{inp}} + \bar{V}_2\bar{V}_1\bar{V}_0 + \bar{V}_2V_1\bar{V}_0$$

$$w = V_2V_1V_0$$



Source State	Destination State	Condition
1 000	000	(inp)
2 000	001	(linp)
3 001	001	(linp)
4 001	010	(inp)

Transitions / Encoding /

Object Navigator Files seqQ.v

Table of Contents

- Flow Summary
- <<Filter>>
- Flow Status Successful - Tue Jan 02 23:26:50 2024
- Quartus Prime Version 20.1.0 Build 711 06/05/2020 SJ Lite Edition
- Revision Name seqQ
- Top-level Entity Name seqQ
- Family Cyclone IV GX
- Device EP4CGX15BF14A7
- Timing Models Final
- Total logic elements 7 / 14,400 (< 1 %)
- Total registers 7
- Total pins 4 / 81 (5 %)
- Total virtual pins 0
- Total memory bits 0 / 552,960 (0 %)
- Embedded Multiplier 9-bit elements 0
- Total GXB Receiver Channel PCS 0 / 2 (0 %)
- Total GXB Receiver Channel PMA 0 / 2 (0 %)
- Total GXB Transmitter Channel PCS 0 / 2 (0 %)
- Total GXB Transmitter Channel PMA 0 / 2 (0 %)
- Total PLLs 0 / 3 (0 %)

Tasks Compilation

Task

- Compile Design
- Analysis & Synthesis
- Fitter (Place & Route)
- Assembler (Generate programming)
- Timing Analysis
- EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

IP Catalog

- Installed IP
- Project Directory No Selection Available
- Library
  - Basic Functions
  - DSP
  - Interface Protocols
  - Processors and Peripherals
  - University Program
- Search for Partner IP

All Find... Find Next

Time	Type	Message
204019	Generated	file seqQ_7_1200mv_-40c_slow.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ_min_1200mv_-40c_fast.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ.v in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ_7_1200mv_125c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ_7_1200mv_-40c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ_min_1200mv_-40c_v_fast.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
204019	Generated	file seqQ_v.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
>	Quartus Prime EDA Netlist Writer was successful. 0 errors, 1 warning	
>	293000 Quartus Prime Full Compilation was successful. 0 errors, 14 warnings	

Project Navigator Files seqQ.v

Table of Contents

- Flow Summary
- Flow Settings
- Flow Non-Default Global
- Flow Elapsed Time
- Flow OS Summary
- Flow Log
- Analysis & Synthesis
  - Summary
  - Settings
  - Parallel Compilation
  - Source Files Read
  - Resource Usage Summary
  - Resource Utilization by
- State Machines
  - State Machine - seqQ
- Optimization Results
  - Register Statistics
  - Post-Synthesis Netlist
  - Elapsed Time Per Partition
  - Messages
- Fitter
- Assembler
- Timing Analyzer
- EDA Netlist Writer
- Flow Messages
- Flow Suppressed Message

Encoding Type: One-Hot

All Find... Find Next

Line	Time	Message
1	204019	Generated file seqQ_7_1200mv_-40c_slow.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ_min_1200mv_-40c_fast.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ_7_1200mv_125c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ_7_1200mv_-40c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ_min_1200mv_-40c_v_fast.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
1	204019	Generated file seqQ_v.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool
>	293000	Quartus Prime EDA Netlist Writer was successful. 0 errors, 1 warning
<	293000	Quartus Prime Full Compilation was successful. 0 errors, 14 warnings

File Edit View Project Assignments Processing Tools Window Help

Project Navigator Files seqQ seqQ.v Compilation Report - seqQ IP Catalog

**Analysis & Synthesis Summary**

**Analysis & Synthesis Status:** Successful - Tue Jan 02 23:26:42 2024  
**Quartus Prime Version:** 20.1.0 Build 711 06/05/2020 SJ Lite Edition

**Revision Name:** seqQ  
**Top-level Entity Name:** seqQ  
**Family:** Cyclone IV GX

**Total logic elements:** 7  
**Total registers:** 7  
**Total pins:** 4  
**Total virtual pins:** 0  
**Total memory bits:** 0  
**Embedded Multiplier 9-bit elements:** 0  
**Total GXB Receiver Channel PCS:** 0  
**Total GXB Receiver Channel PMA:** 0  
**Total GXB Transmitter Channel PCS:** 0  
**Total GXB Transmitter Channel PMA:** 0  
**Total PLLs:** 0

**Messages:**

- Filter
- Assembler
- Timing Analyzer
- EDA Netlist Writer
- Flow Messages
- Flow Suppressed Message

Tasks Compilation

Task

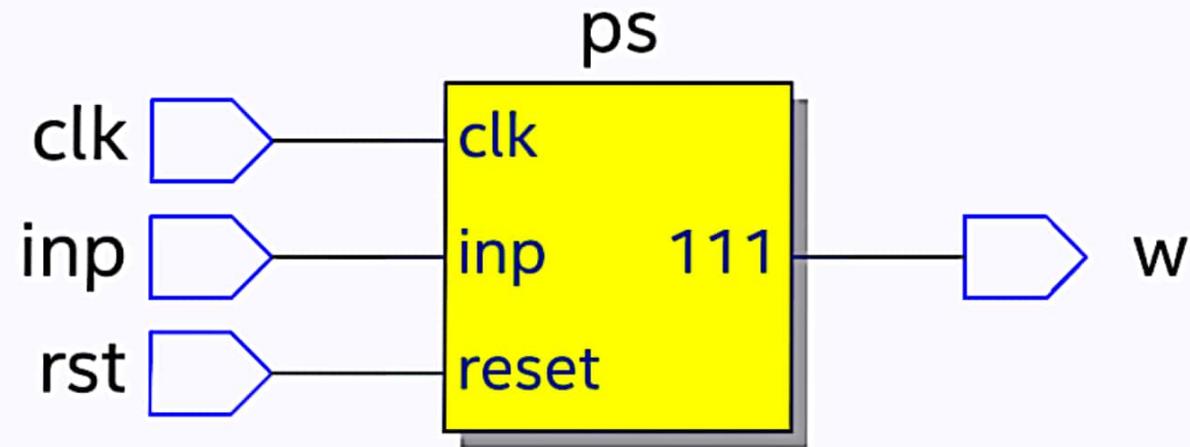
- Compile Design
- Analysis & Synthesis
- Fitter (Place & Route)
- Assembler (Generate programming)
- Timing Analysis
- EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

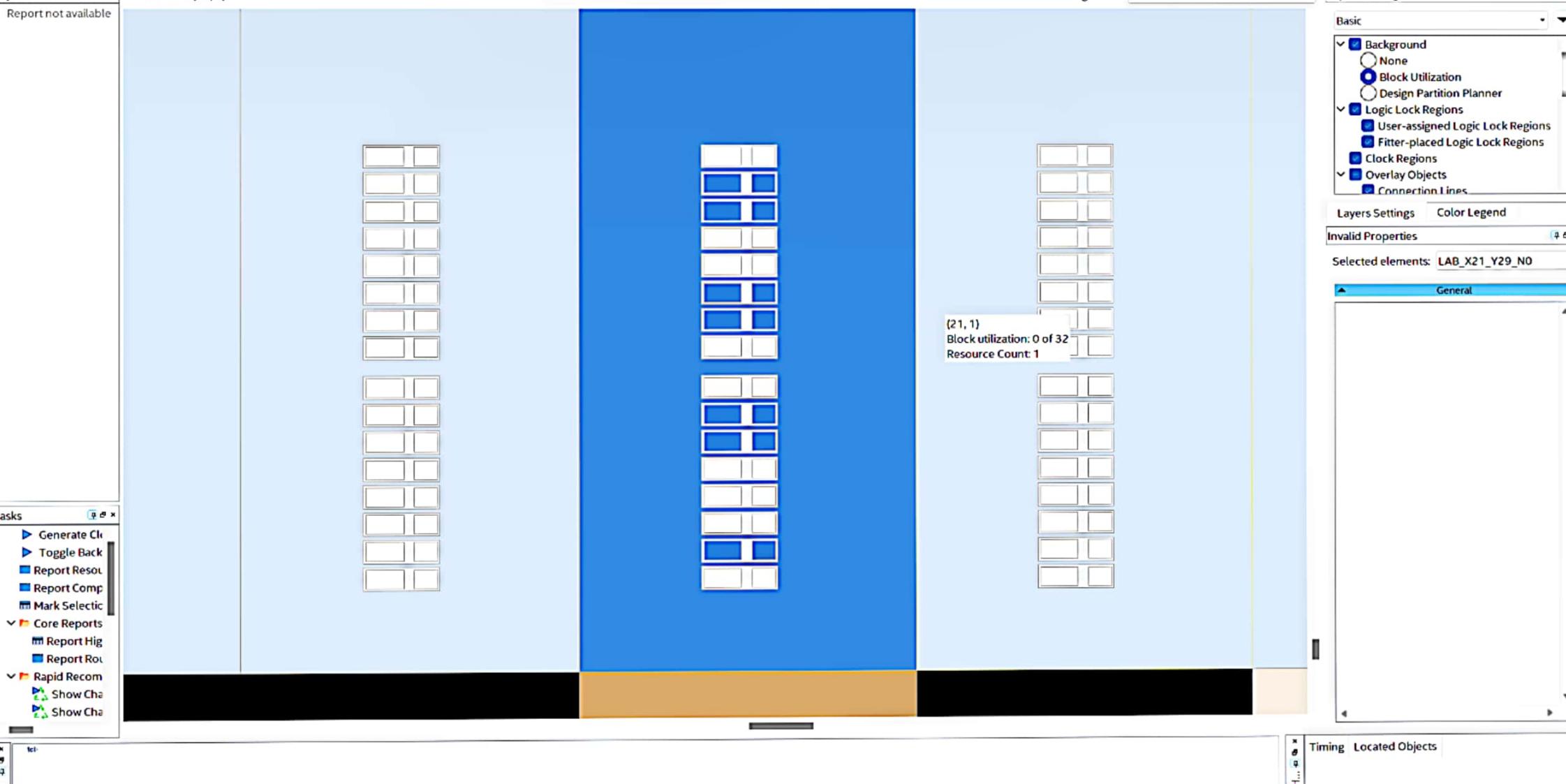


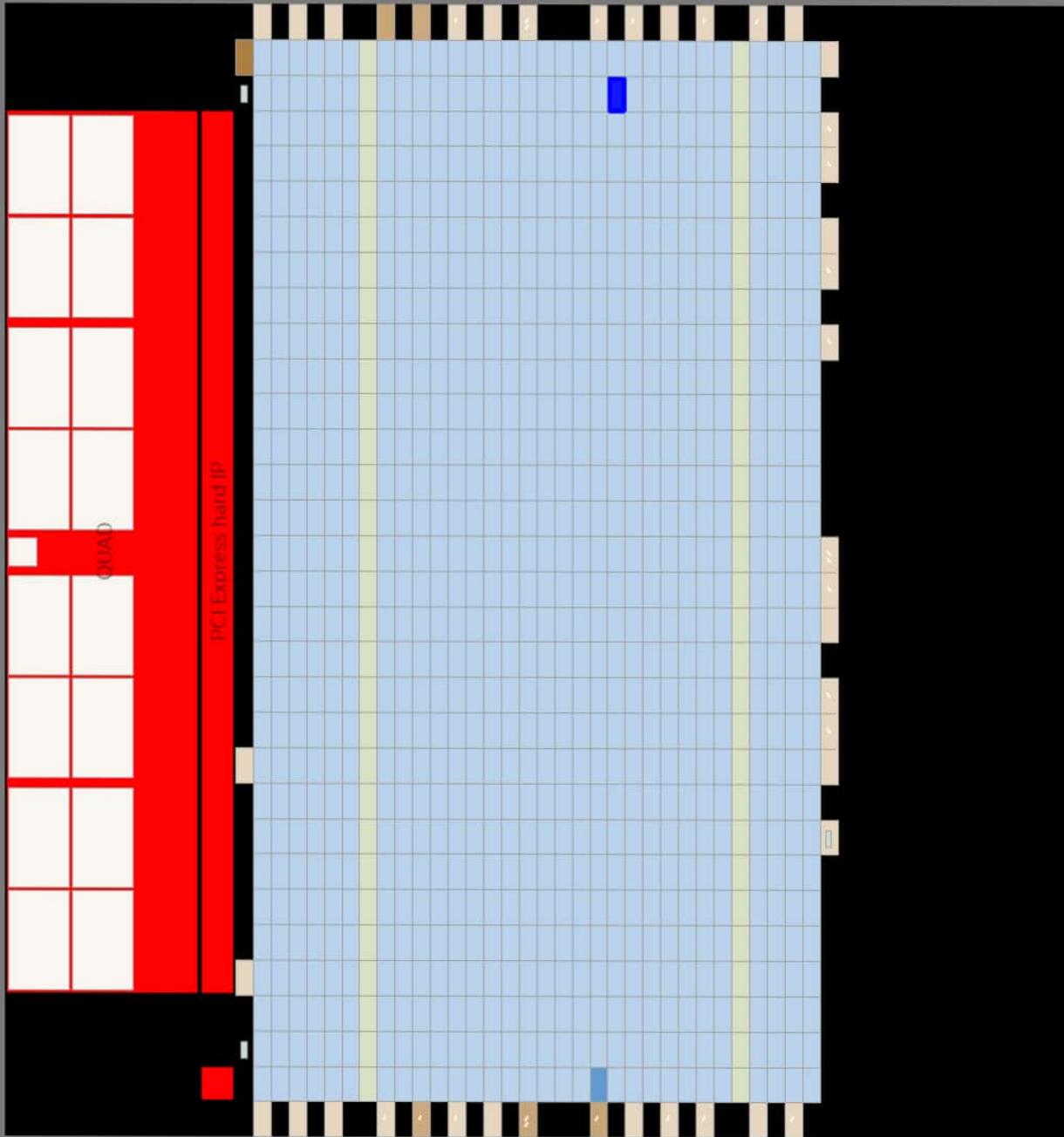
All Find... Find Next

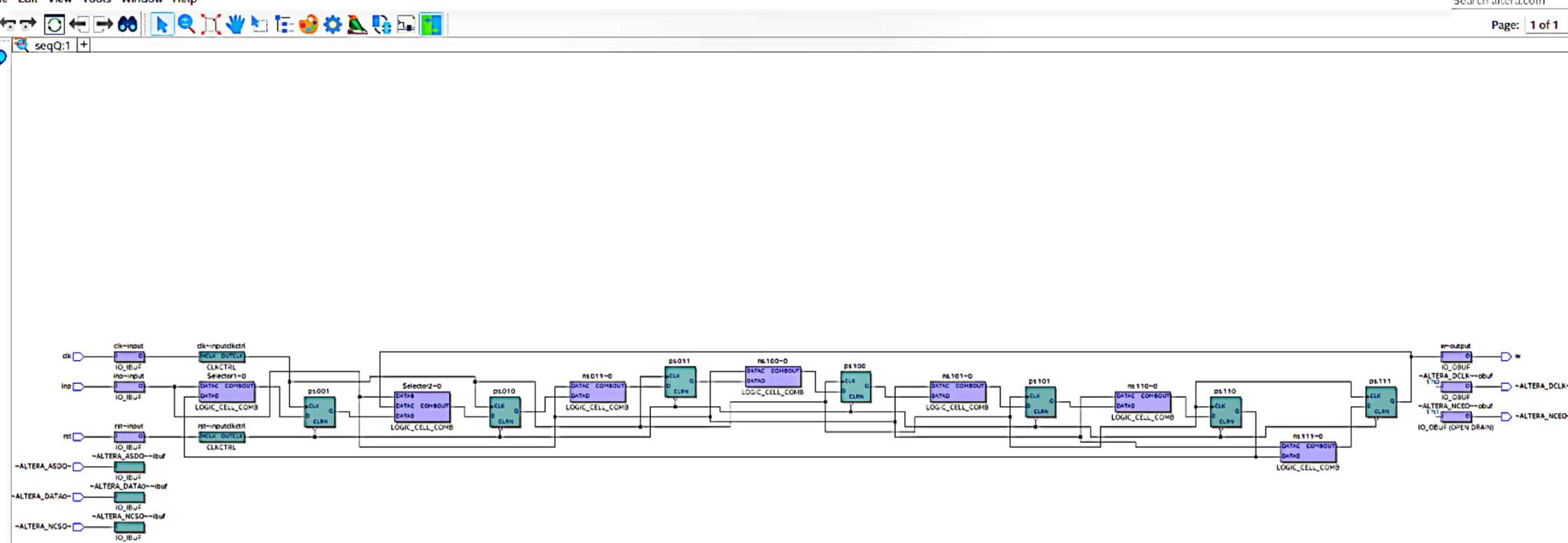
Logs TD Messages

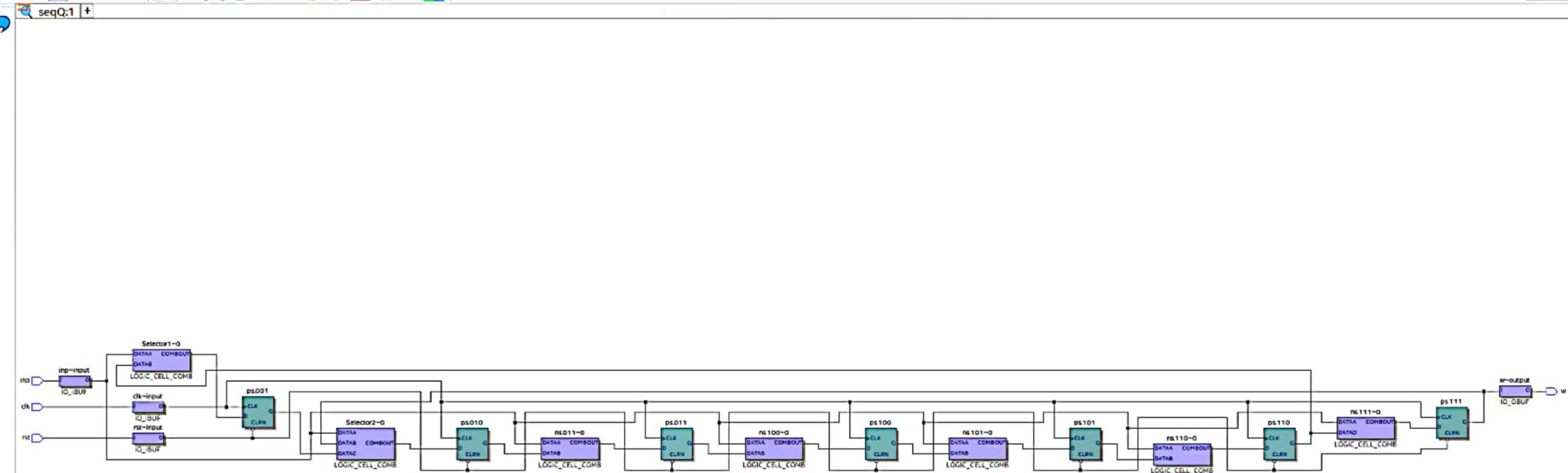
204019 Generated file seqQ\_7\_1200mv\_-40c\_slow.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ\_min\_1200mv\_-40c\_fast.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ.vo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ\_7\_1200mv\_125c\_v\_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ\_7\_1200mv\_-40c\_v\_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ\_min\_1200mv\_-40c\_v\_fast.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 204019 Generated file seqQ\_v.sdo in folder "C:/Users/ROG/Desktop/myC5/sequence/simulation/modelsim/" for EDA simulation tool  
 Quartus Prime EDA Netlist Writer was successful. 0 errors, 1 warning  
 293000 Quartus Prime Full Compilation was successful. 0 errors, 14 warnings

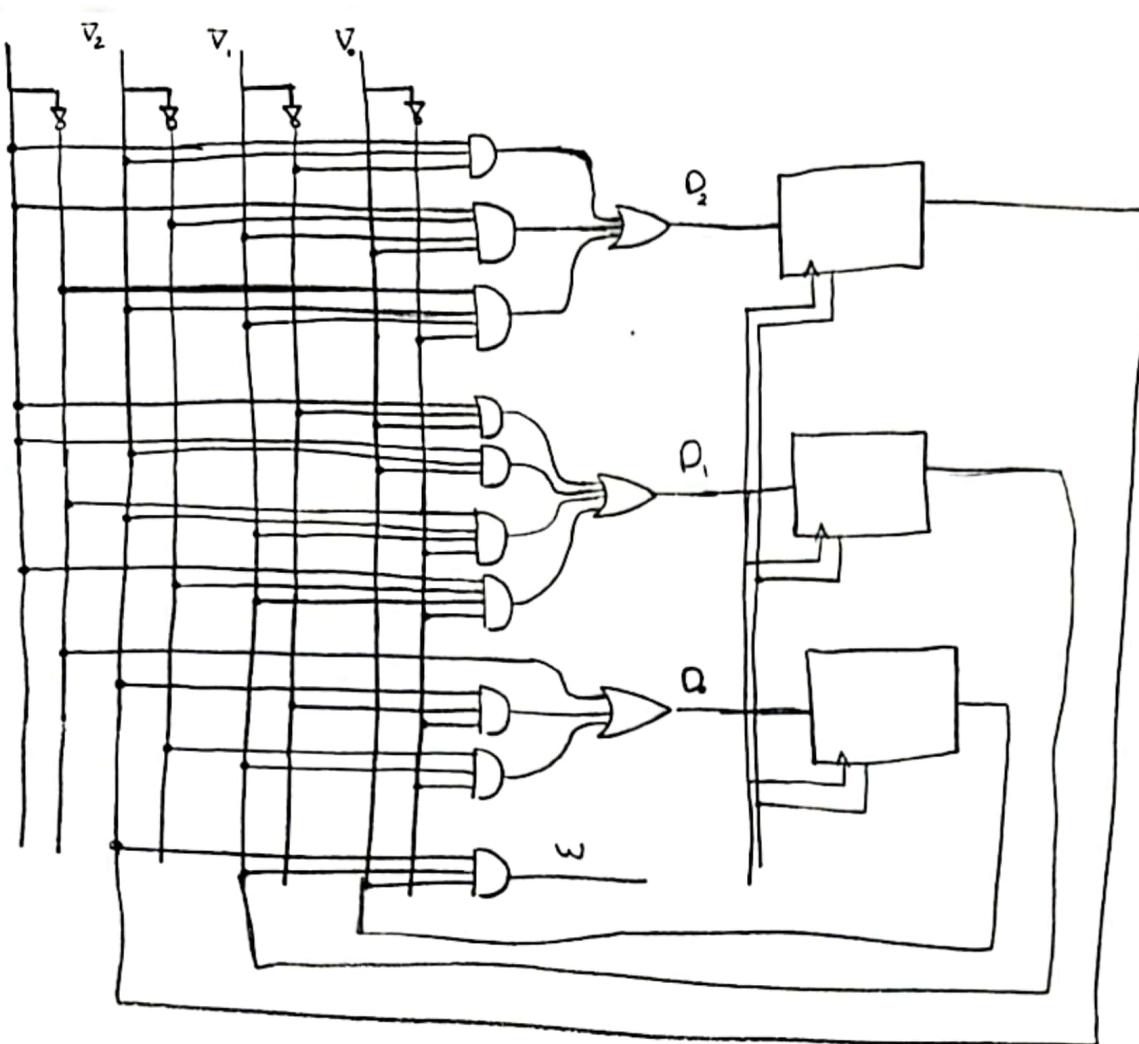








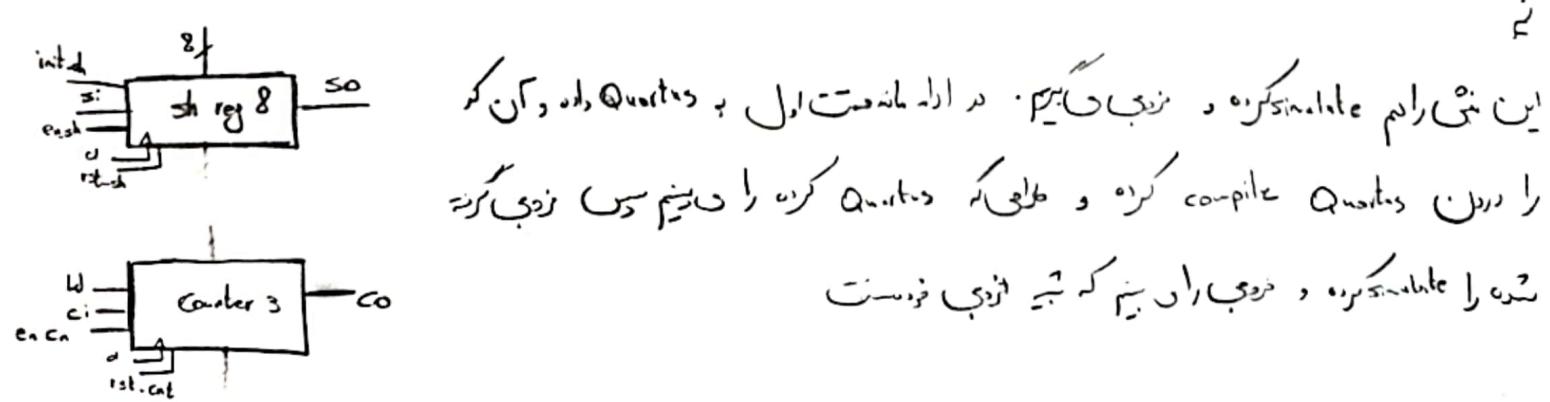




تست ریلیک ین مدار را با سل عافن فریم که مایک ہے جو comb دارم کیل بٹھ . وہ دس ایں درجہ میں دھل دیکھ .

لیڈ راب دھانڈو دلہ و آن ہل state را کئیہ و عابد گن رام پنه . دس لذخرا state دھانڈو دلک کر دھانڈو دس رام دھانڈو دلک نہیں کہ تست آن ما بیار بیار شیہ میں است

تست بسی روڑہ ما ریزاز تنسائی sequence باید 8 دوں بیورا درکیں shift reg ذیروں مڑہ دتمیں گویم 8 دوں یعنی کیک counter رس مت بدما بایہ کیں counter کے 8 دنالہ دلود ملکی انگریز دلکار آن کیں shift reg 8 bit ملیں



Project Navigator Files cntshiftQ

Table of Contents

- Flow Summary
- <<Filter>>
- Flow Status Successful - Tue Jan 02 23:33:50 2024
- Quartus Prime Version 20.1.0 Build 711 06/05/2020 SJ Lite Edition
- Revision Name cntshiftQ
- Top-level Entity Name cntshiftQ
- Family Cyclone IV GX
- Device EP4CGX15BF14A7
- Timing Models Final
- Total logic elements 14 / 14,400 (< 1 %)
- Total registers 11
- Total pins 17 / 81 (21 %)
- Total virtual pins 0
- Total memory bits 0 / 552,960 (0 %)
- Embedded Multiplier 9-bit elements 0
- Total GXB Receiver Channel PCS 0 / 2 (0 %)
- Total GXB Receiver Channel PMA 0 / 2 (0 %)
- Total GXB Transmitter Channel PCS 0 / 2 (0 %)
- Total GXB Transmitter Channel PMA 0 / 2 (0 %)
- Total PLLs 0 / 3 (0 %)

Tasks Compilation

Task

- Compile Design
- Analysis & Synthesis
- Fitter (Place & Route)
- Assembler (Generate programming)
- Timing Analysis
- EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

IP Catalog

- Installed IP
- Project Directory No Selection Available
- Library
  - Basic Functions
  - DSP
  - Interface Protocols
  - Processors and Peripherals
  - University Program
- Search for Partner IP

All Find... Find Next

Time	Type	Message
204019	Generated	file cntshiftQ_7_1200mv_-40c_slow.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ_min_1200mv_-40c_fast.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ_7_1200mv_125c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ_7_1200mv_-40c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ_min_1200mv_-40c_v_fast.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
204019	Generated	file cntshiftQ_v.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
>	Quartus Prime EDA Netlist Writer was successful. 0 errors, 1 warning	
>	293000 Quartus Prime Full Compilation was successful. 0 errors, 15 warnings	

System Processing (125)

Project Navigator Files cntshiftQ

Table of Contents Analysis & Synthesis Summary

<<Filter>>

**Analysis & Synthesis Status** Successful - Tue Jan 02 23:33:41 2024  
**Quartus Prime Version** 20.1.0 Build 711 06/05/2020 SJ Lite Edition  
**Revision Name** cntshiftQ  
**Top-level Entity Name** cntshiftQ  
**Family** Cyclone IV GX

**Total logic elements** 14  
**Total registers** 11  
**Total pins** 17  
**Total virtual pins** 0  
**Total memory bits** 0  
**Embedded Multiplier 9-bit elements** 0  
**Total GXB Receiver Channel PCS** 0  
**Total GXB Receiver Channel PMA** 0  
**Total GXB Transmitter Channel PCS** 0  
**Total GXB Transmitter Channel PMA** 0  
**Total PLLs** 0

**Analysis & Synthesis**

- Summary
- Settings
- Parallel Compilation
- Source Files Read
- Resource Usage Summary
- Resource Utilization by
- Optimization Results
- Post-Synthesis Netlist
- Elapsed Time Per Partition
- Messages

**Fitter**

**Assembler**

**Timing Analyzer**

**EDA Netlist Writer**

- Flow Messages
- Flow Suppressed Message

IP Catalog

Installed IP

Project Directory No Selection Available

Library

- Basic Functions
- DSP
- Interface Protocols
- Processors and Peripherals
- University Program

Search for Partner IP

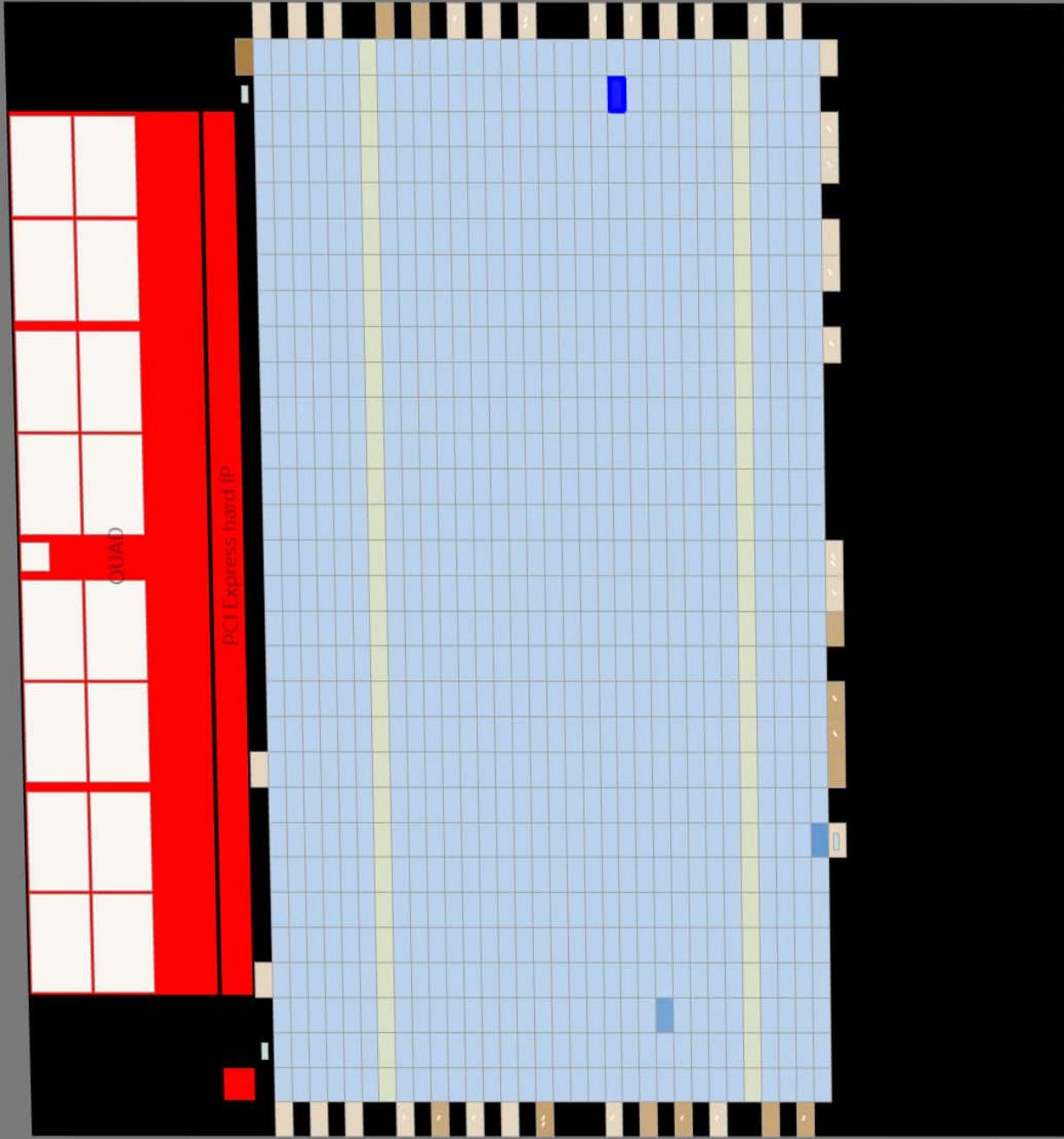
Tasks	Compilation
<b>Task</b>	
Compile Design	
>	Analysis & Synthesis
>	Fitter (Place & Route)
>	Assembler (Generate programming)
>	Timing Analysis
>	EDA Netlist Writer
■	Edit Settings
▼	Program Device (Open Programmer)

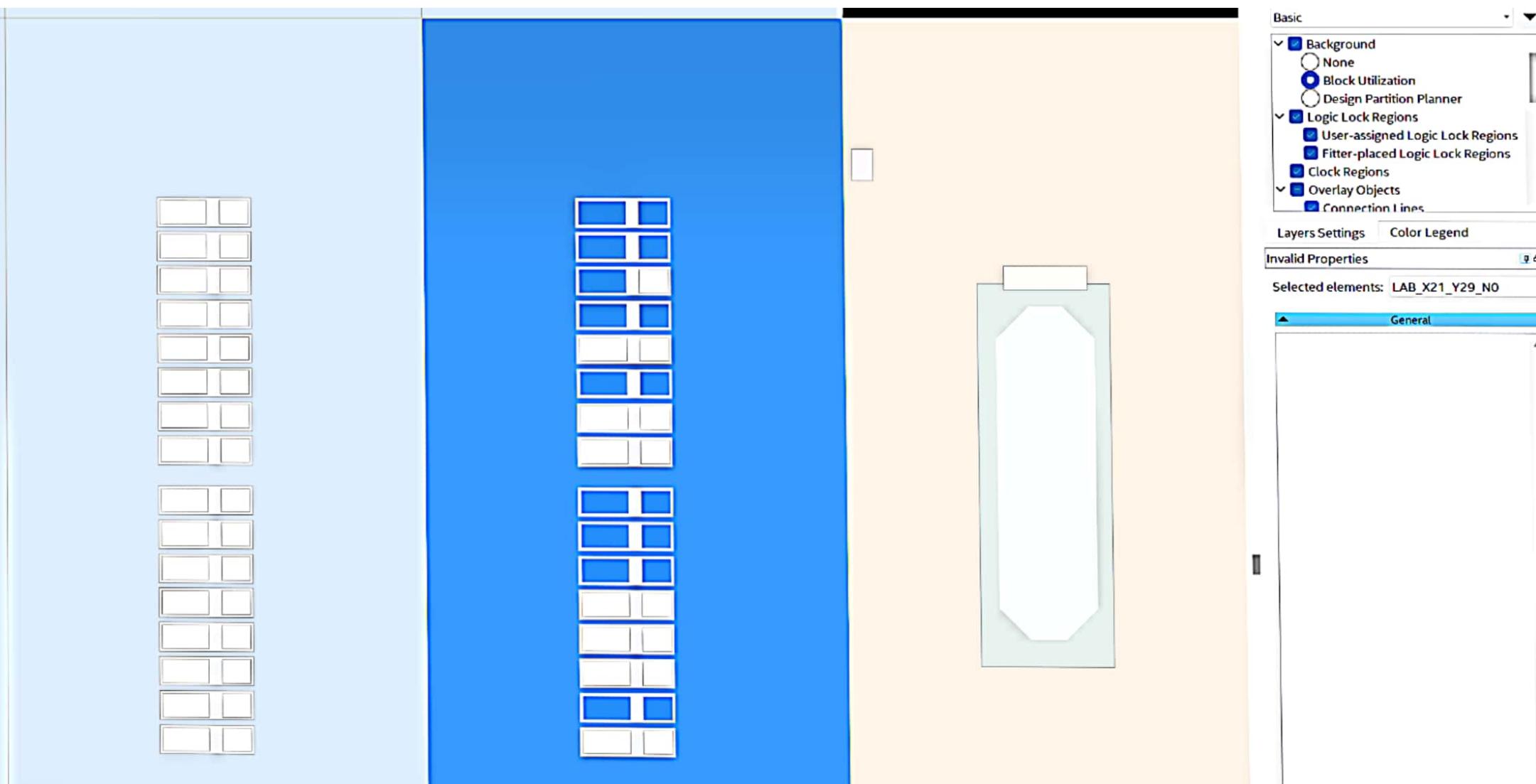


Find... Find Next

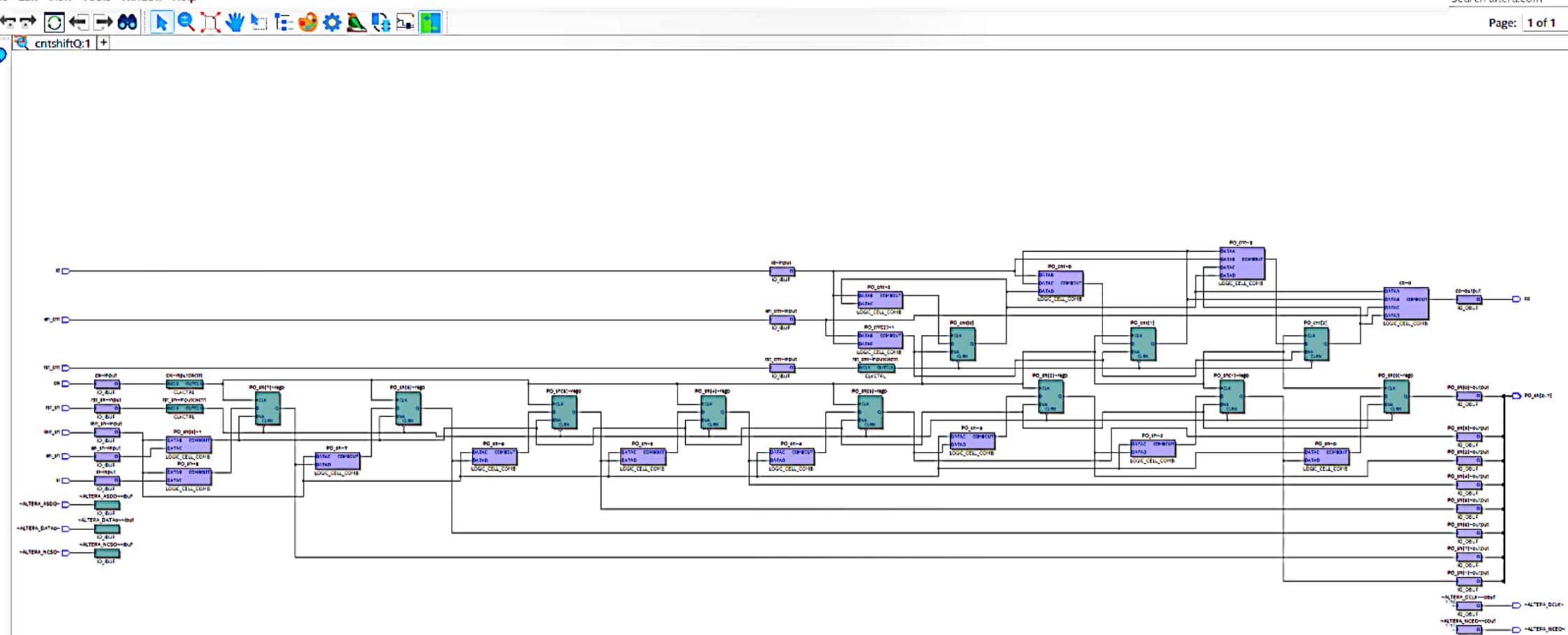
## Funs TD Message

- 204019 Generated file cntshiftQ\_7\_1200mv\_-40c\_v\_slow.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ\_min\_1200mv\_-40c\_v\_fast.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ.vo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ\_7\_1200mv\_125c\_v\_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ\_7\_1200mv\_-40c\_v\_slow.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ\_min\_1200mv\_-40c\_v\_fast.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- 204019 Generated file cntshiftQ\_v.sdo in folder "C:/Users/ROG/Desktop/myC5/cntshift/simulation/modelsim/" for EDA simulation tool
- Quartus Prime EDA Netlist writer was successful. 0 errors, 1 warning
- 293000 Quartus Prime Full Compilation was successful. 0 errors, 15 warnings



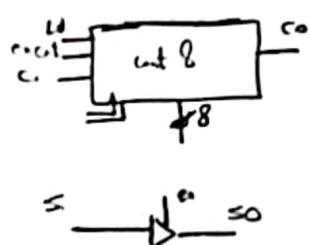






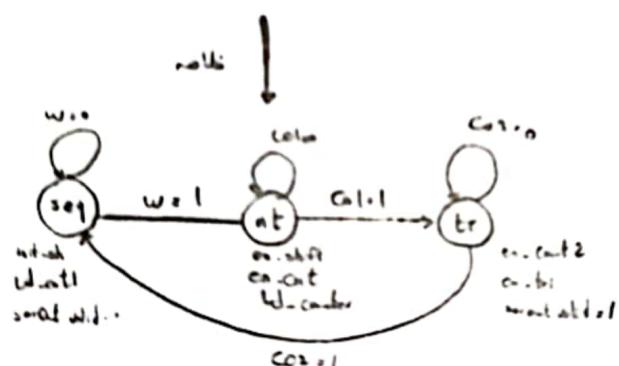
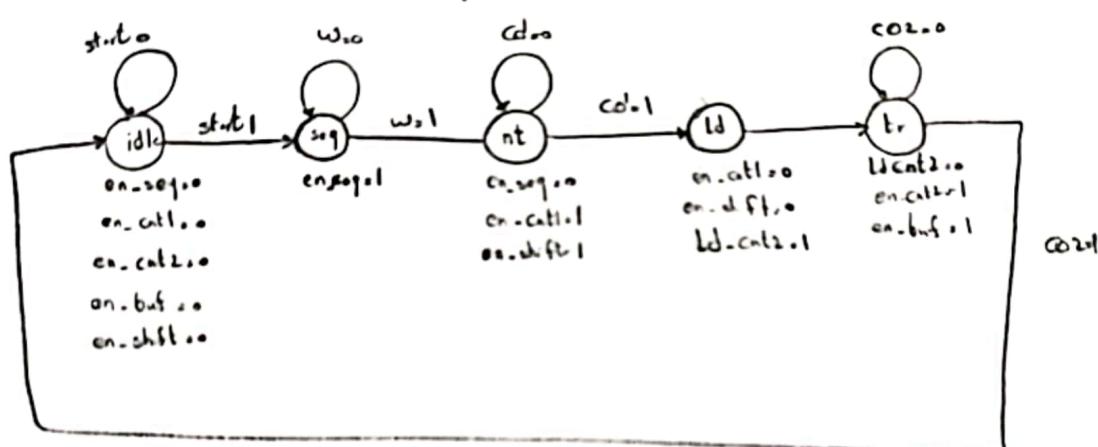
zohreh kashani Johnson 8/10/1496

نت ۳ بروز نام از اینه عی کرر right و مت تبل لذیوه بود اما از همیکه بین buffer ون ۵۰ فایل  
نهان این درجیک center 8 bit فیل رام در گیک tri-state buffer (tristate buffer)



ای این ماده است برآورده آن را افکار داشت داشت ها نهی کنیم  
در این ماده بسیاری اهل بریم.

دست بعدیکه controller کی بیت مارکم دیدی و نوی تمام حالت بیل نا ایر نظر لایه ای state on ماتریس زیر



حال بی از کل این می در داستان Q و از میان تیکیکی ماتریس دست گزه و آن سارا با استفاده از ابزاری ای شهادت Q بهم سغل کرته و مارکم را معرفی کرد، در بی مزی داستان Q بهم نویسندگ نویسندگ آن را ستون کنم.

bufcntQ

Project Navigator Files

bufcntQ.v

Table of Contents

**Flow Summary**

<<Filter>>

Flow Status Successful - Tue Jan 02 23:41:14 2024  
 Quartus Prime Version 20.1.0 Build 711 06/05/2020 SJ Lite Edition  
 Revision Name bufcntQ  
 Top-level Entity Name bufcntQ  
 Family Cyclone IV GX  
 Device EP4CGX15BF14A7  
 Timing Models Final  
 Total logic elements 22 / 14,400 (< 1 %)  
 Total registers 10  
 Total pins 16 / 81 (20 %)  
 Total virtual pins 0  
 Total memory bits 0 / 552,960 (0 %)  
 Embedded Multiplier 9-bit elements 0  
 Total GXB Receiver Channel PCS 0 / 2 (0 %)  
 Total GXB Receiver Channel PMA 0 / 2 (0 %)  
 Total GXB Transmitter Channel PCS 0 / 2 (0 %)  
 Total GXB Transmitter Channel PMA 0 / 2 (0 %)  
 Total PLLs 0 / 3 (0 %)

Compilation Task

- Compile Design
  - Analysis & Synthesis
  - Fitter (Place & Route)
  - Assembler (Generate programming)
  - Timing Analysis
  - EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

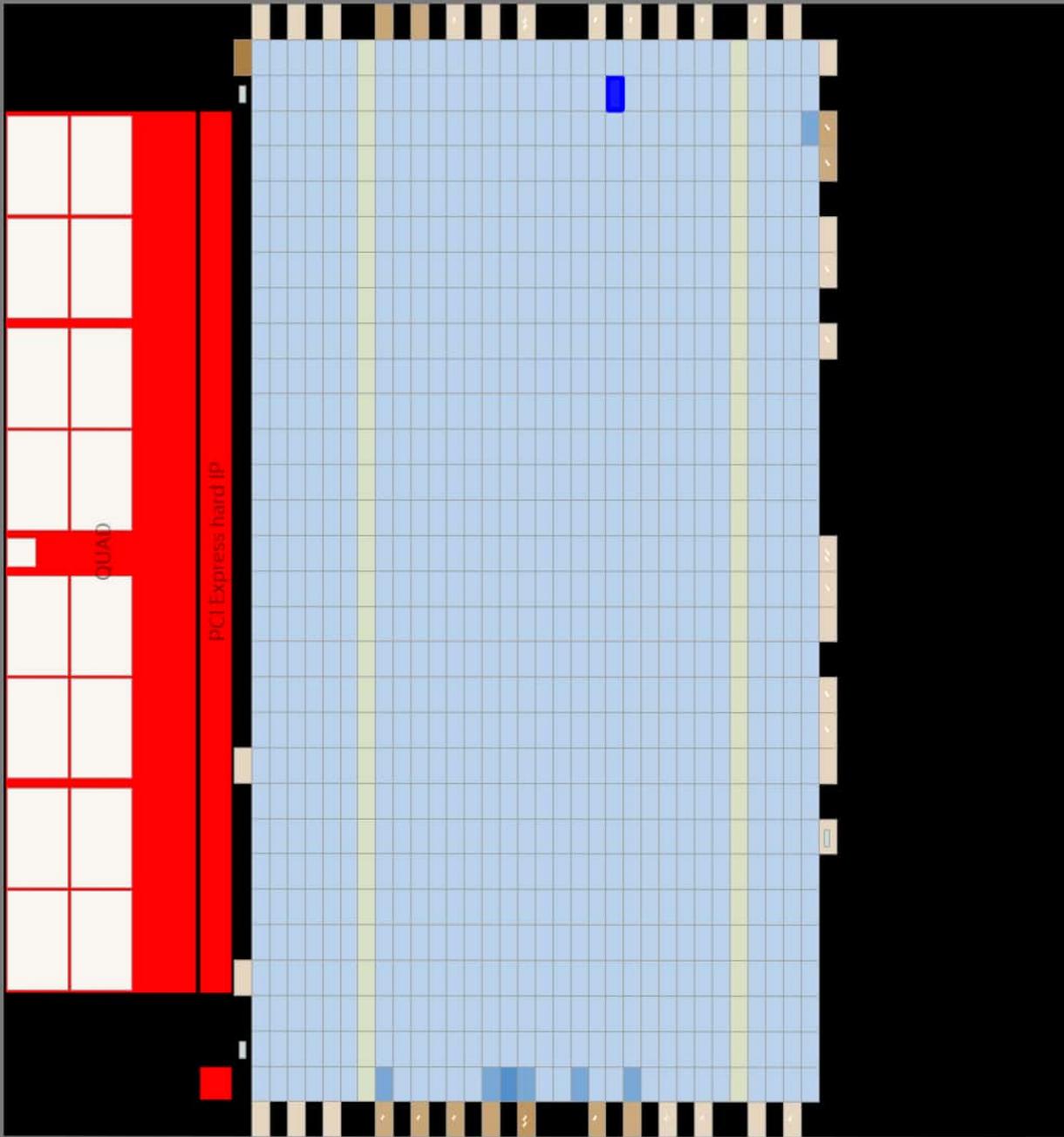
IP Catalog

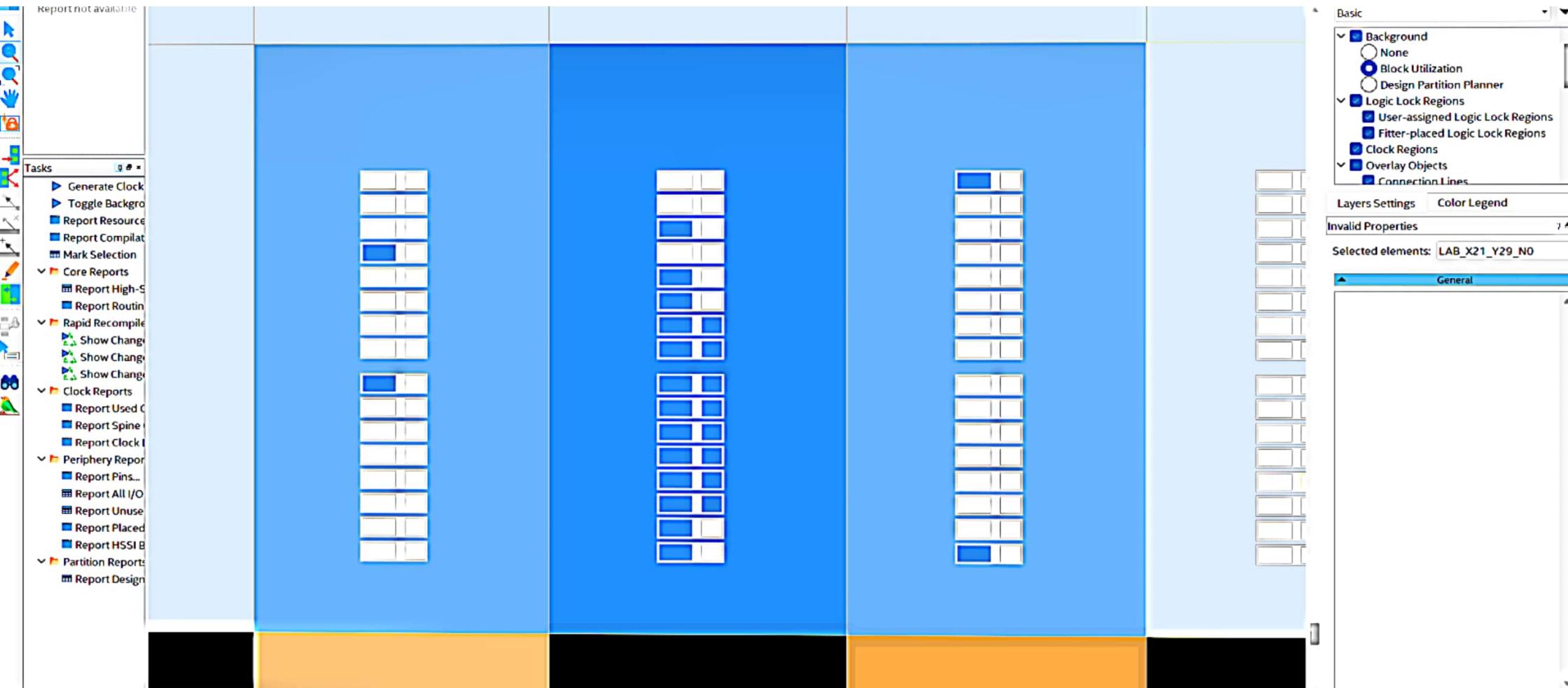
- Installed IP
- Project Directory No Selection Available
- Library
  - Basic Functions
  - DSP
  - Interface Protocols
  - Processors and Peripherals
  - University Program
- Search for Partner IP

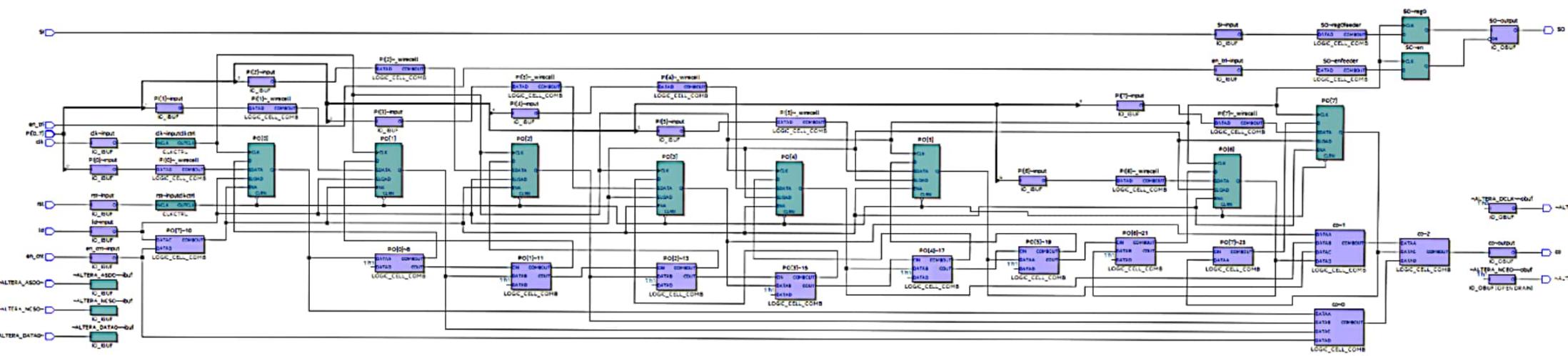
All Find... Find Next

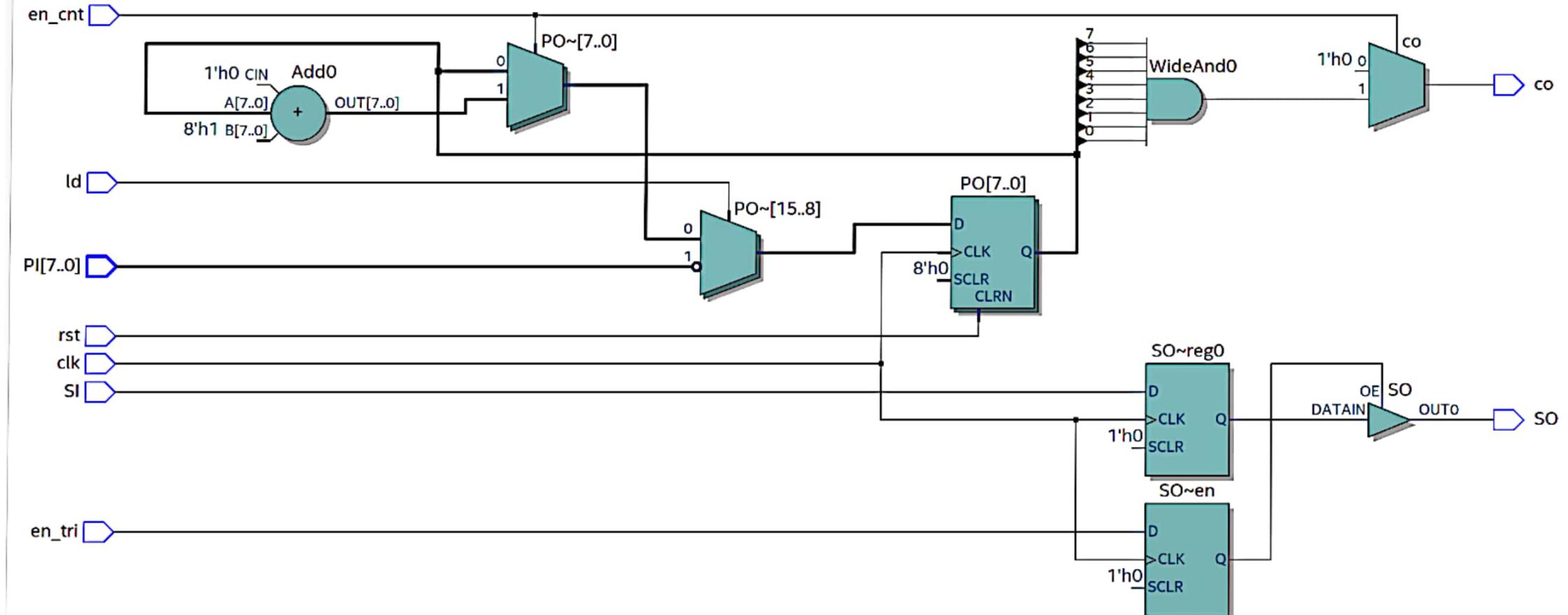
Line	Time	Message
204019	Generated file bufcntQ_7_1200mv_-40c_slow.vo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ_min_1200mv_-40c_fast.vo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ.vo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ_7_1200mv_125c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ_7_1200mv_-40c_v_slow.sdo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ_min_1200mv_-40c_v_fast.sdo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
204019	Generated file bufcntQ_v.sdo in folder "C:/Users/ROG/Desktop/myCAS/bufcnt/simulation/modelsim/" for EDA simulation tool	
>	Quartus Prime EDA Netlist Writer was successful. 0 errors, 1 warning	
293000	Quartus Prime Full Compilation was successful. 0 errors, 15 warnings	

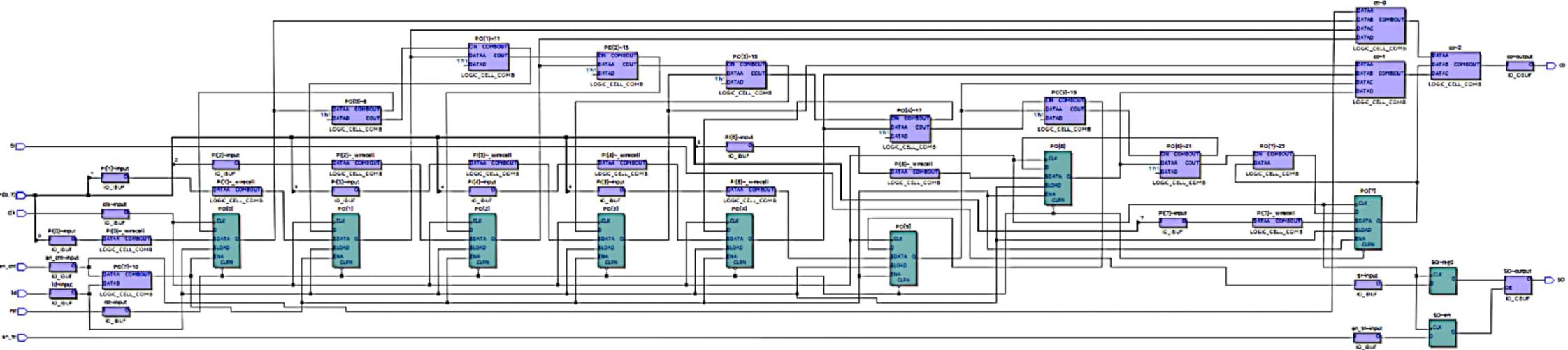
System Processing (124)

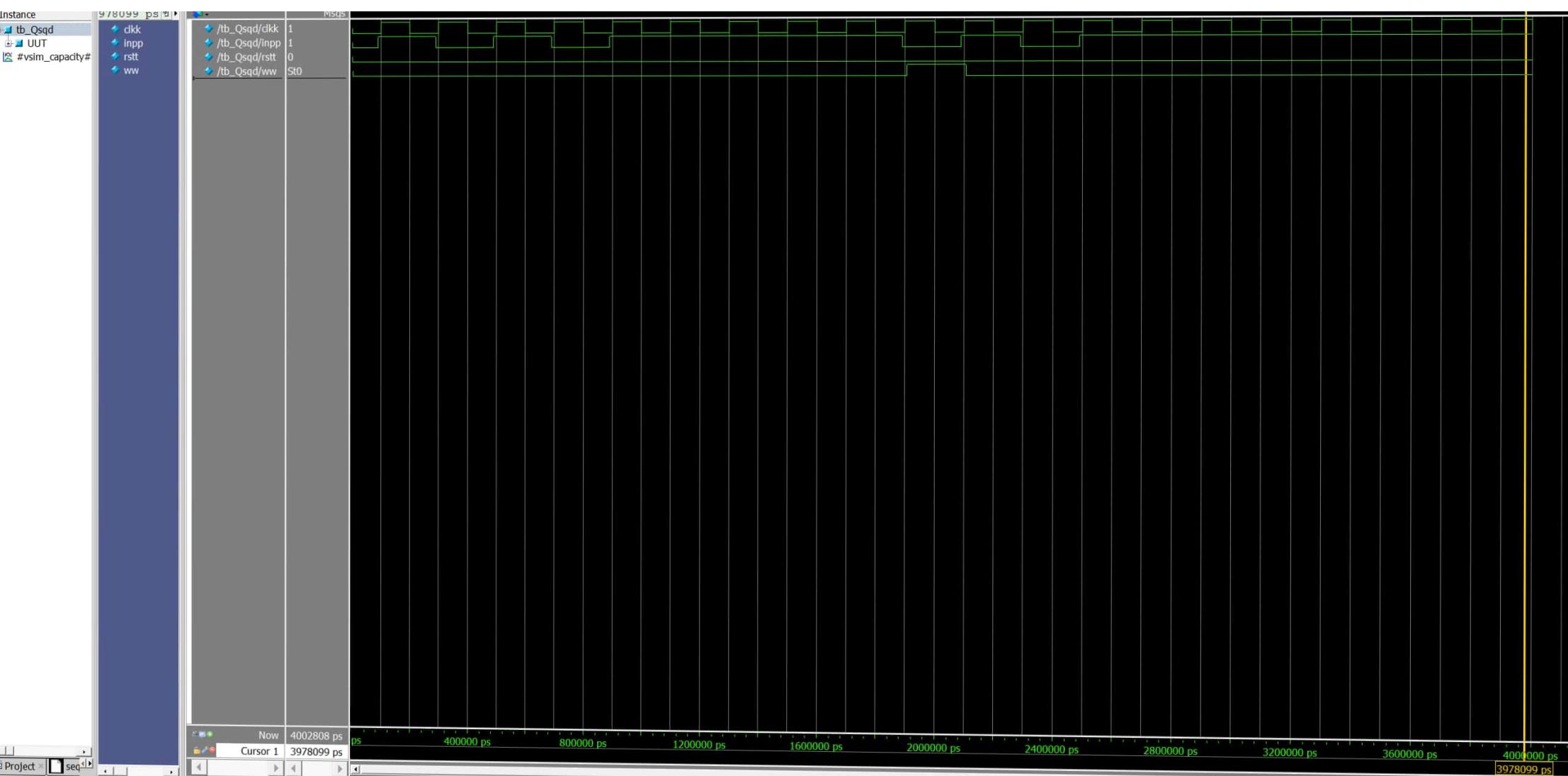


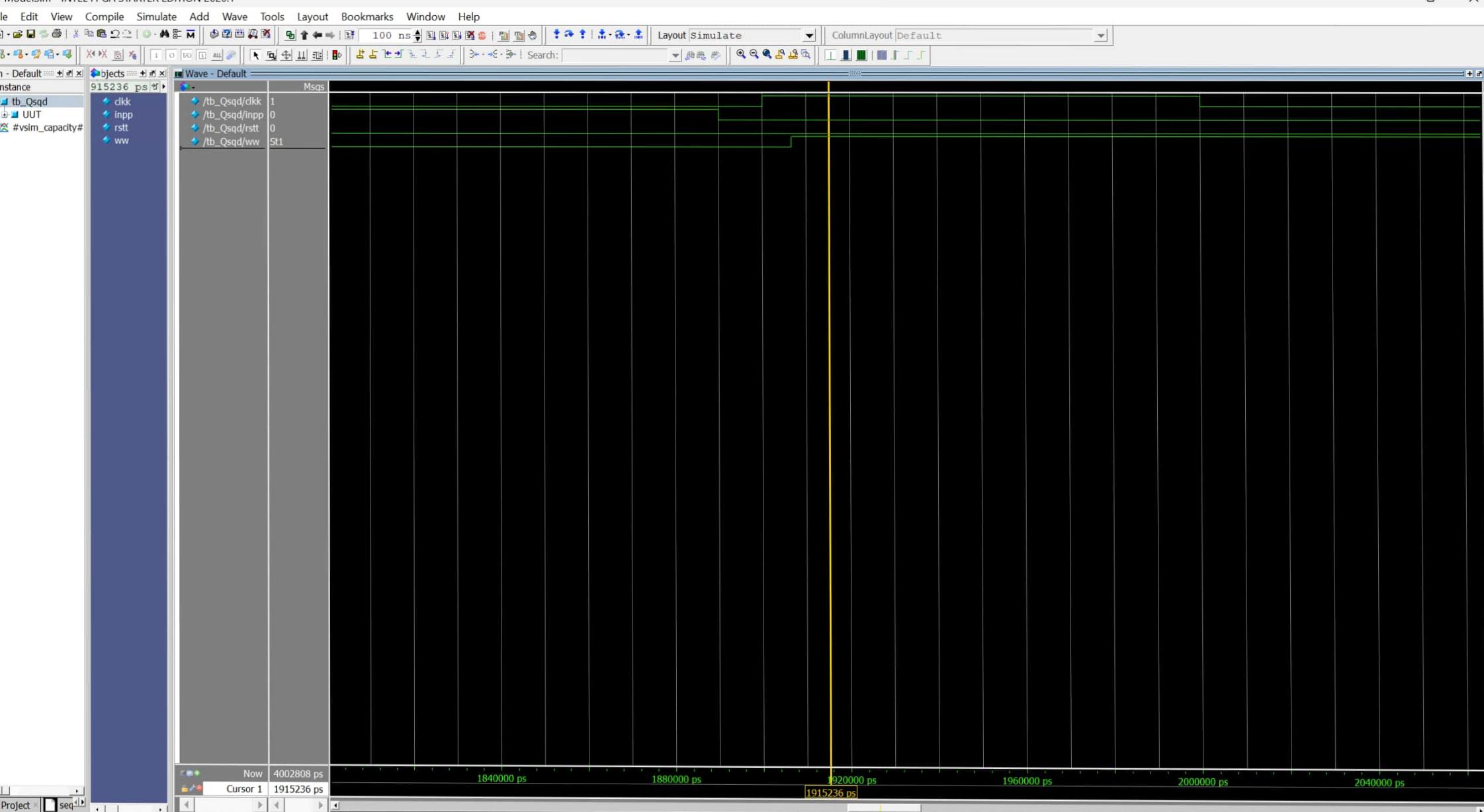


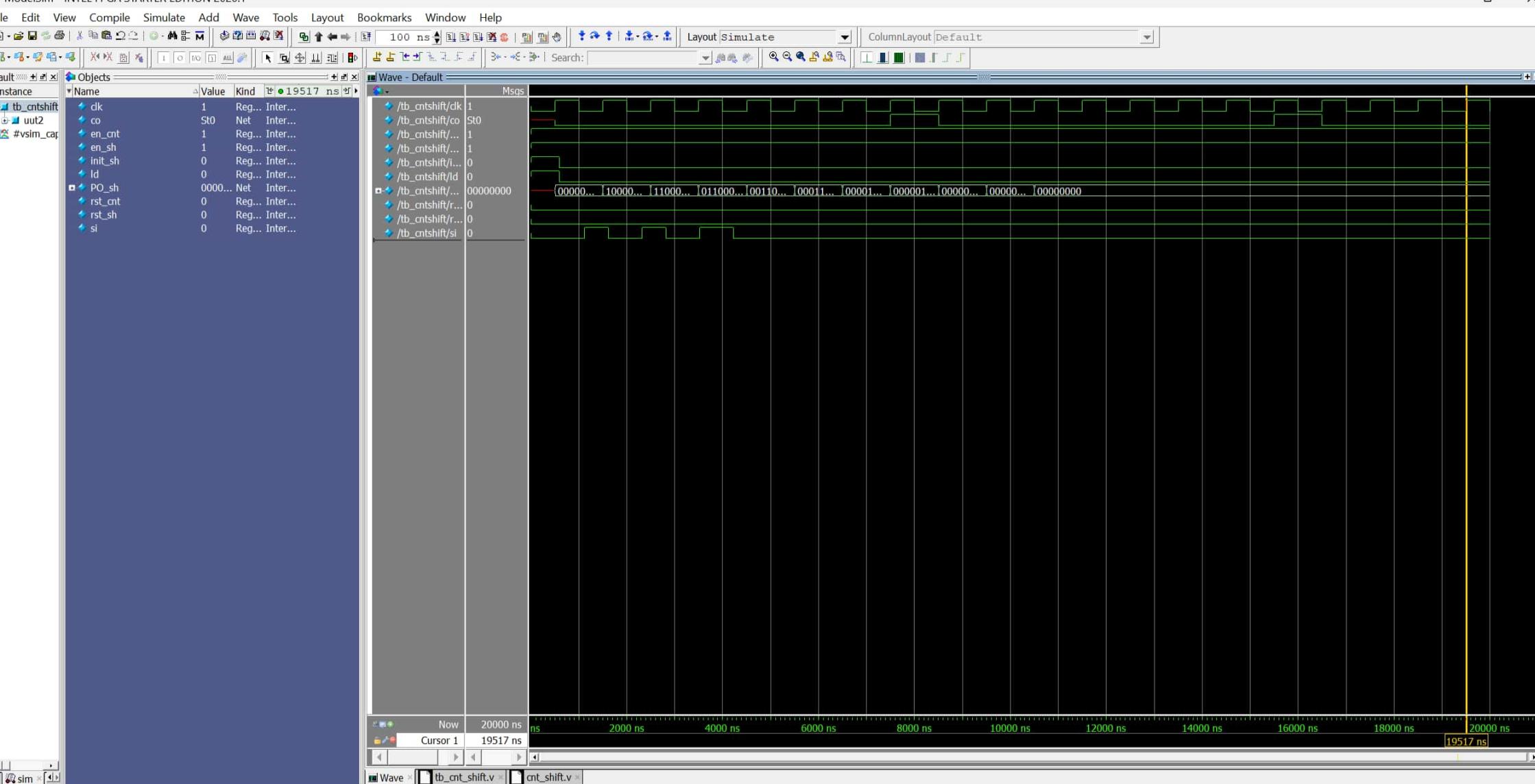








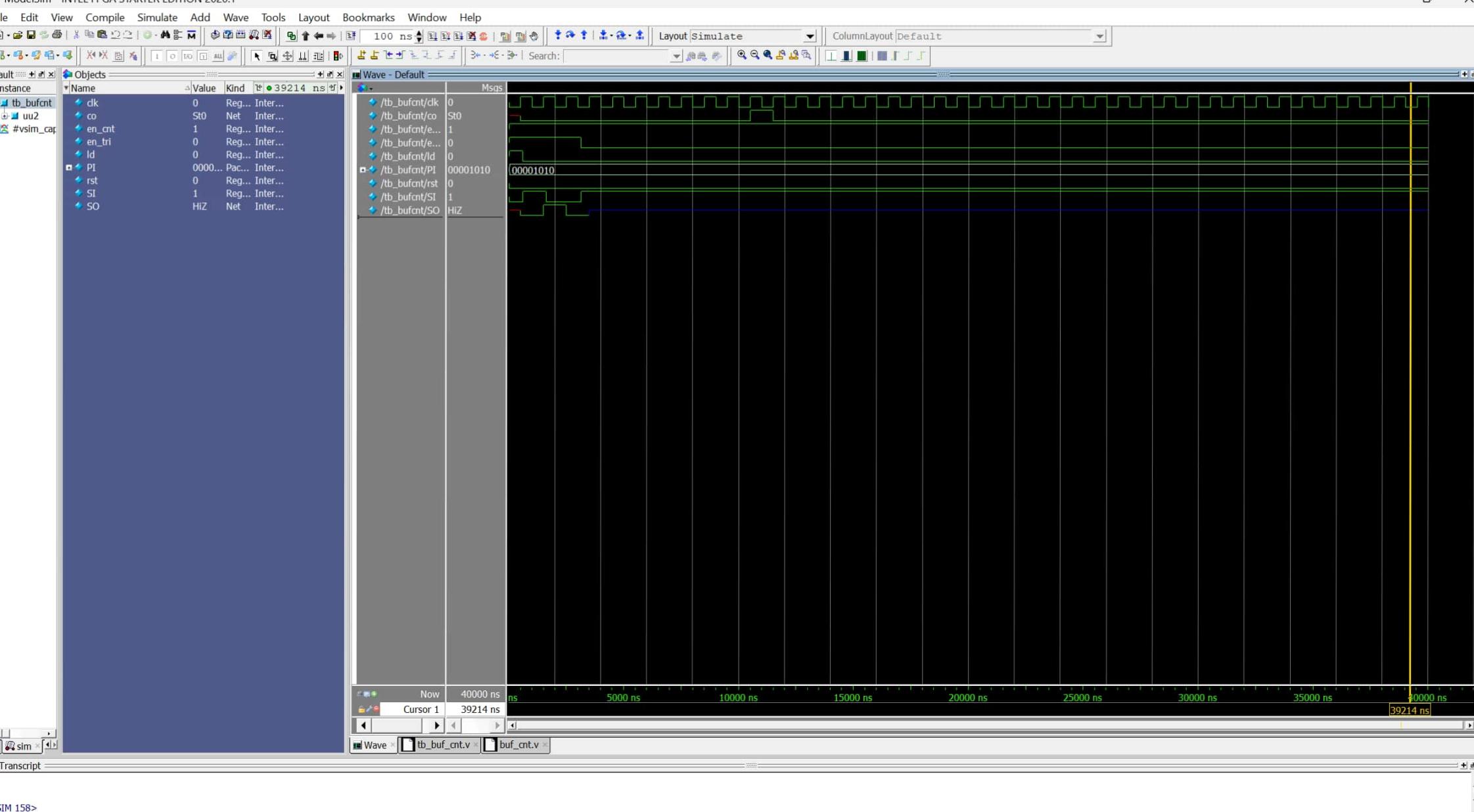


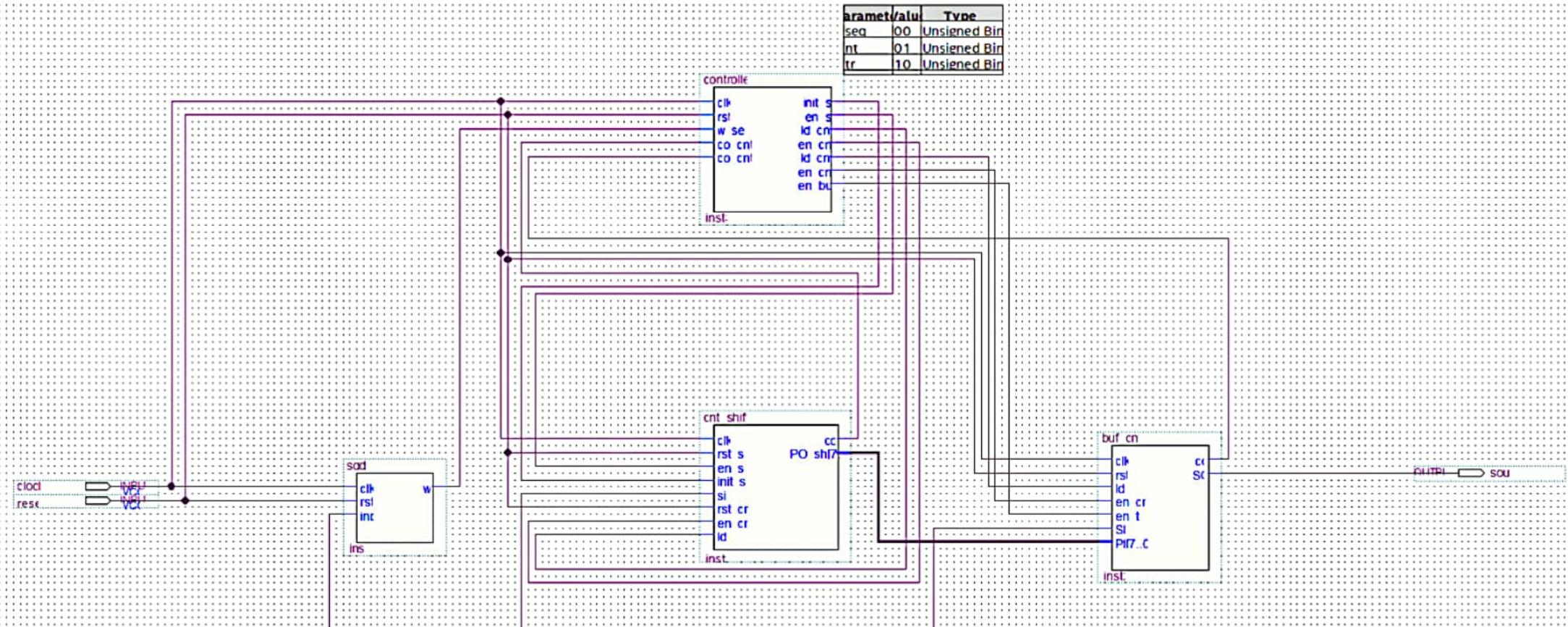


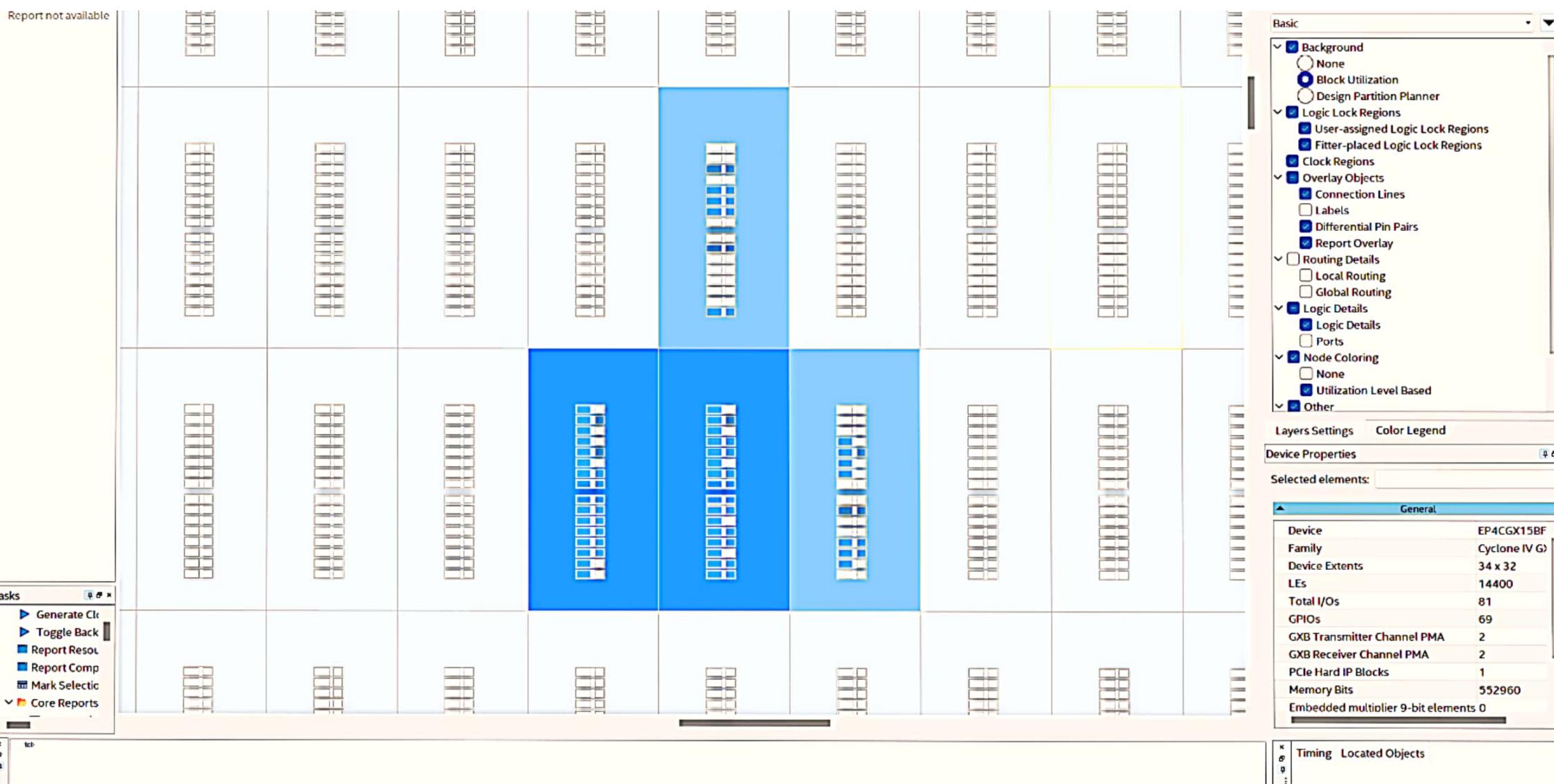
Transcript

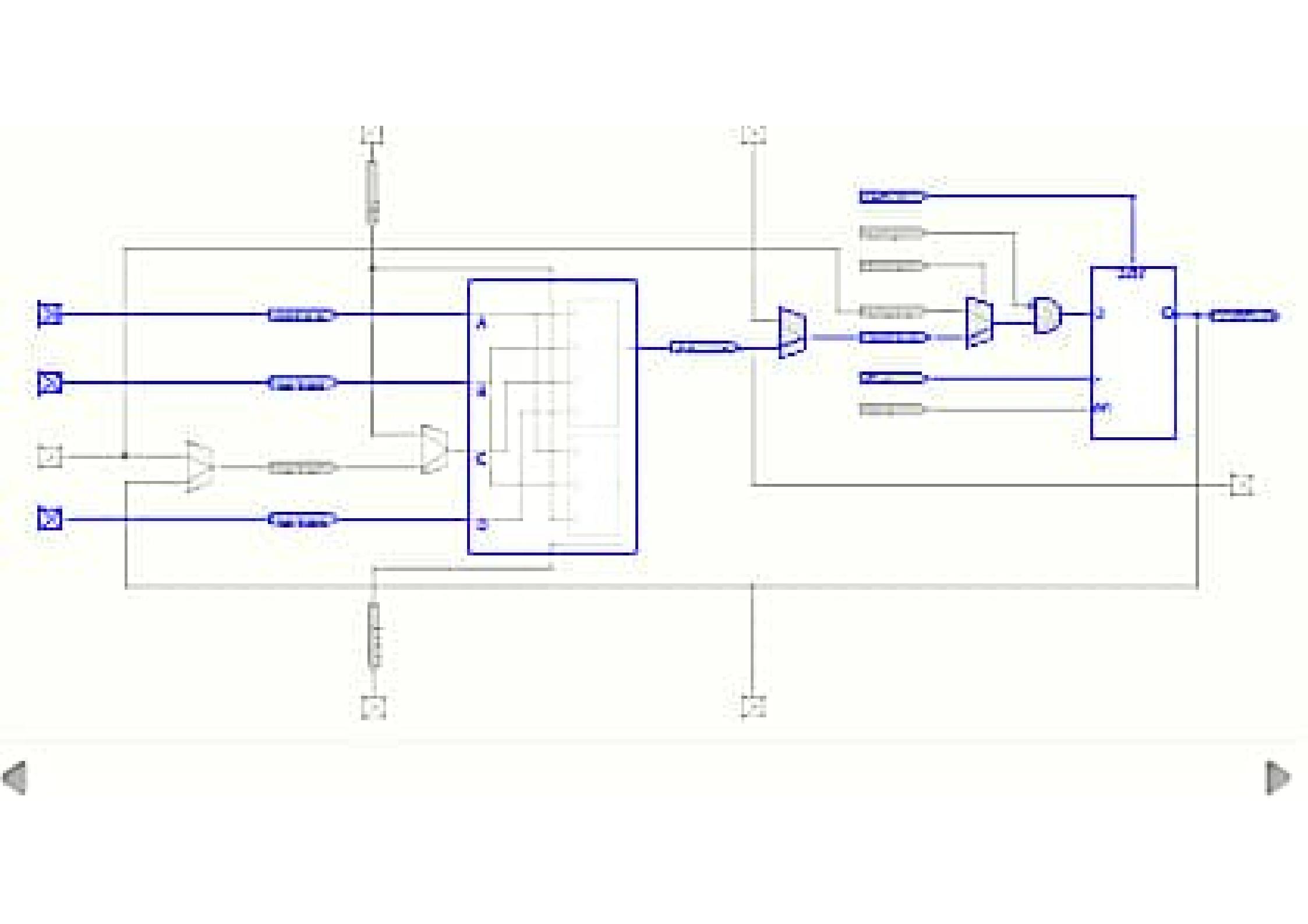
SIM 143&gt; run -all

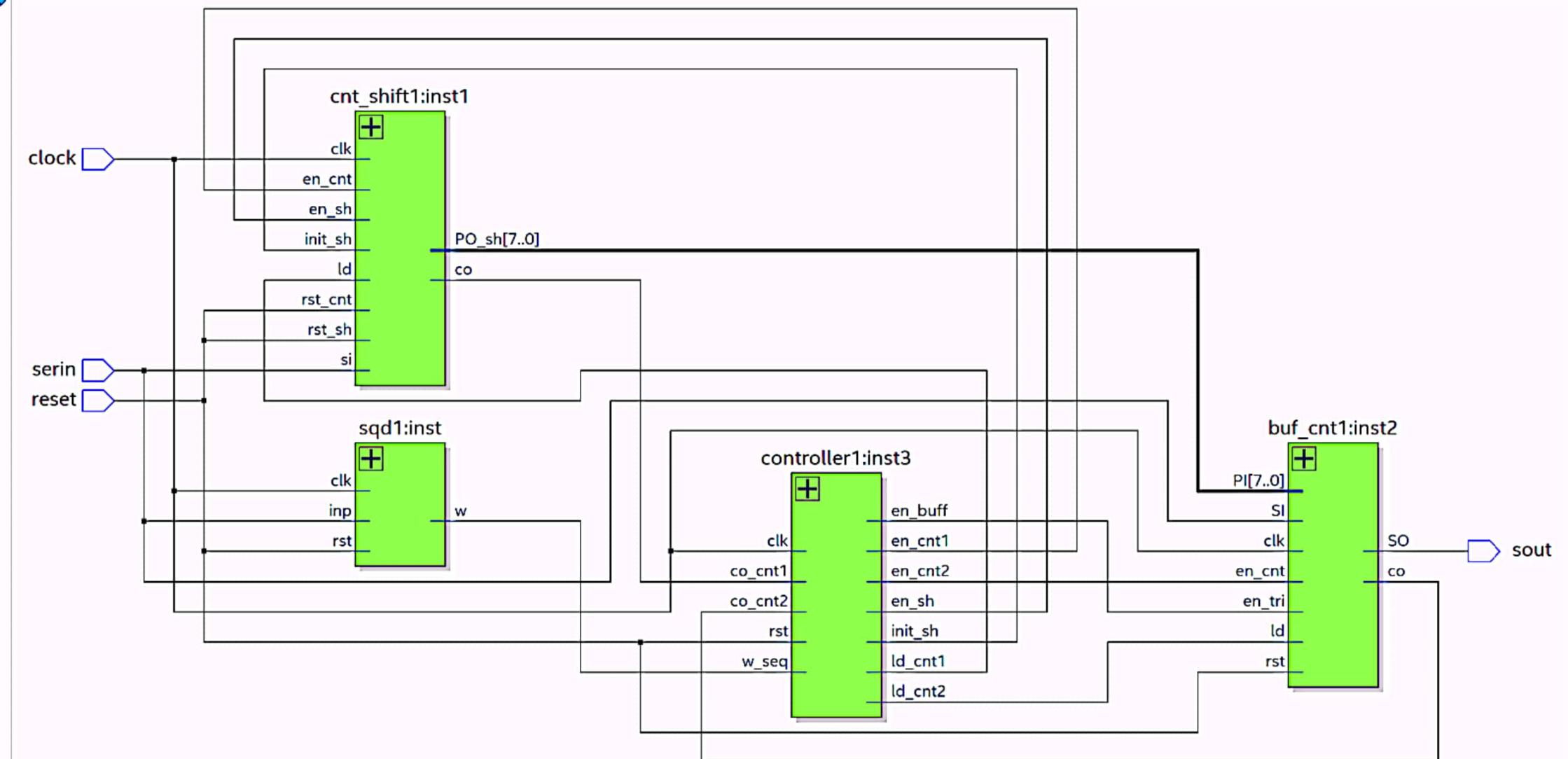
SIM 144&gt;









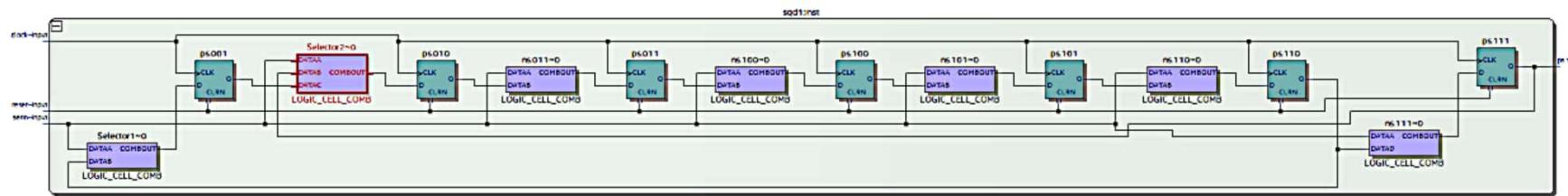


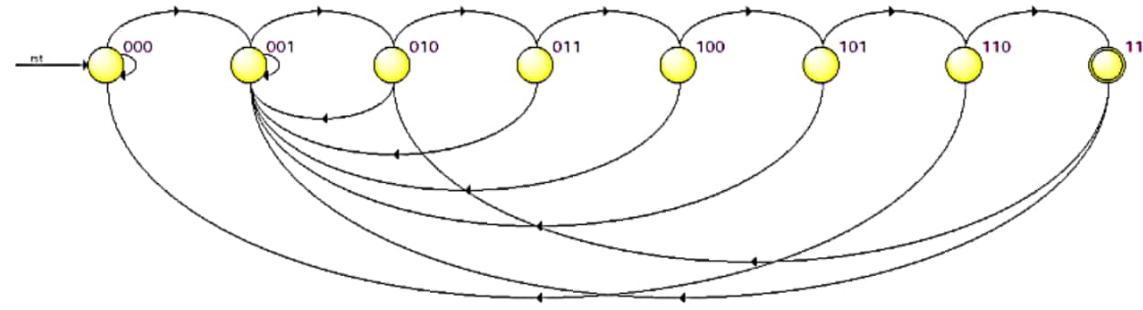
# Technology Map Viewer - Post-Mapping - C:/Users/ROG/Desktop/myCA5/ca5/ca5Q - ca5Q

File Edit View Tools Window Help

Search altera.com

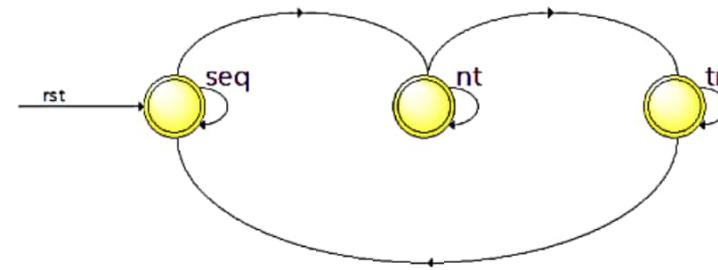
Page: 1 of 1





Source State	Destination State	Condition
1 000	000	(inp)
2 000	001	(linp)
3 001	001	(linp)
4 001	010	(inp)

Transitions / Encoding /



Source State	Destination State	Condition
1 nt	tr	(co_cnt1)
2 nt	nt	(!co_cnt1)
3 seq	seq	(!w_seq)
4 seq	nt	(w_seq)

Transitions / Encoding /

File Edit View Project Assignments Processing Tools Window Help

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ca5Q

Project Navigator Files seq.v (Read-Only) sqd.v cnt\_shift.v buf\_cnt.v controller.v blockca5.bdf ca5Q.bdf Compilation Report - ca5Q

Table of Contents

**Flow Summary**

<<Filter>>

Flow Status Successful - Tue Jan 02 22:37:42 2024  
 Quartus Prime Version 20.1.0 Build 711 06/05/2020 SJ Lite Edition  
 Revision Name ca5Q  
 Top-level Entity Name ca5Q  
 Family Cyclone IV GX  
 Device EP4CGX15BF14A7  
 Timing Models Final  
 Total logic elements 46 / 14,400 (< 1 %)  
 Total registers 31  
 Total pins 4 / 81 (5 %)  
 Total virtual pins 0  
 Total memory bits 0 / 552,960 (0 %)  
 Embedded Multiplier 9-bit elements 0  
 Total GXB Receiver Channel PCS 0 / 2 (0 %)  
 Total GXB Receiver Channel PMA 0 / 2 (0 %)  
 Total GXB Transmitter Channel PCS 0 / 2 (0 %)  
 Total GXB Transmitter Channel PMA 0 / 2 (0 %)  
 Total PLLs 0 / 3 (0 %)

Compilation Task

- Compile Design
  - Analysis & Synthesis
  - Fitter (Place & Route)
  - Assembler (Generate programming)
  - Timing Analysis
  - EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

IP Catalog

Installed IP

Project Directory No Selection Available

Library

- Basic Functions
- DSP
- Interface Protocols
- Processors and Peripherals
- University Program

Search for Partner IP

All Find... Find Next

Run ID MESSAGE  
 > Running Quartus Prime Netlist Viewers Preprocess  
 > Command: quartus\_npp ca5Q -c ca5Q --netlist\_type=atom\_map  
 ▲ 18236 Number of processors has not been specified which may cause overloading on shared machines. Set the global assignment NUM\_PARALLEL\_PROCESSORS in your QSF to an appropriate value for best performance  
 > Quartus Prime Netlist Viewers Preprocess was successful. 0 errors, 1 warning  
 \*\*\*\*\*  
 > Running Quartus Prime Netlist Viewers Preprocess  
 > Command: quartus\_npp ca5Q -c ca5Q --netlist\_type=sm\_process  
 ▲ 18236 Number of processors has not been specified which may cause overloading on shared machines. Set the global assignment NUM\_PARALLEL\_PROCESSORS in your QSF to an appropriate value for best performance  
 > Quartus Prime Netlist Viewers Preprocess was successful. 0 errors, 1 warning

System (1) Processing (150)

File Edit View Project Assignments Processing Tools Window Help

Search altera.com

ca5Q

Project Navigator Files seq.v (Read-Only) sqd.v cnt\_shift.v buf\_cnt.v controller.v blockca5.bdf ca5Q.bdf Compilation Report - ca5Q

Table of Contents Analysis & Synthesis Summary

<<Filter>>

Analysis & Synthesis Status Successful - Tue Jan 02 22:37:33 2024  
 Quartus Prime Version 20.1.0 Build 711 06/05/2020 SJ Lite Edition  
 Revision Name ca5Q  
 Top-level Entity Name ca5Q  
 Family Cyclone IV GX

**Analysis & Synthesis**

- Summary
- Settings
- Parallel Compilation
- Source Files Read
- Resource Usage Summary
- Resource Utilization by
- State Machines
- Optimization Results
- Parameter Settings by
- Post-Synthesis Netlist
- Elapsed Time Per Partition
- Messages
- Fitter
- Assembler
- Timing Analyzer
- EDA Netlist Writer
- Flow Messages
- Flow Suppressed Messages

Total logic elements 46  
 Total registers 31  
 Total pins 4  
 Total virtual pins 0  
 Total memory bits 0  
 Embedded Multiplier 9-bit elements 0  
 Total GXB Receiver Channel PCS 0  
 Total GXB Receiver Channel PMA 0  
 Total GXB Transmitter Channel PCS 0  
 Total GXB Transmitter Channel PMA 0  
 Total PLLs 0

IP Catalog

Installed IP

Project Directory No Selection Available

Library

- Basic Functions
- DSP
- Interface Protocols
- Processors and Peripherals
- University Program

Search for Partner IP

Compilation Task

Compile Design

- Analysis & Synthesis
- Place & Route
- Assembler (Generate programming)
- Timing Analysis
- EDA Netlist Writer
- Edit Settings
- Program Device (Open Programmer)

All Find... Find Next

```

Tn  MESSAGE
> Running Quartus Prime Netlist Viewers Preprocess
> Command: quartus_npp ca5Q -c ca5Q --netlist_type=atom_map
> 18236 Number of processors has not been specified which may cause overloading on shared machines. Set the global assignment NUM_PARALLEL_PROCESSORS in your QSF to an appropriate value for best performance
> Quartus Prime Netlist Viewers Preprocess was successful. 0 errors, 1 warning
*****  

> Running Quartus Prime Netlist Viewers Preprocess
> Command: quartus_npp ca5Q -c ca5Q --netlist_type=sm_process
> 18236 Number of processors has not been specified which may cause overloading on shared machines. Set the global assignment NUM_PARALLEL_PROCESSORS in your QSF to an appropriate value for best performance
> Quartus Prime Netlist Viewers Preprocess was successful. 0 errors, 1 warning

```

System (1) Processing (150)

