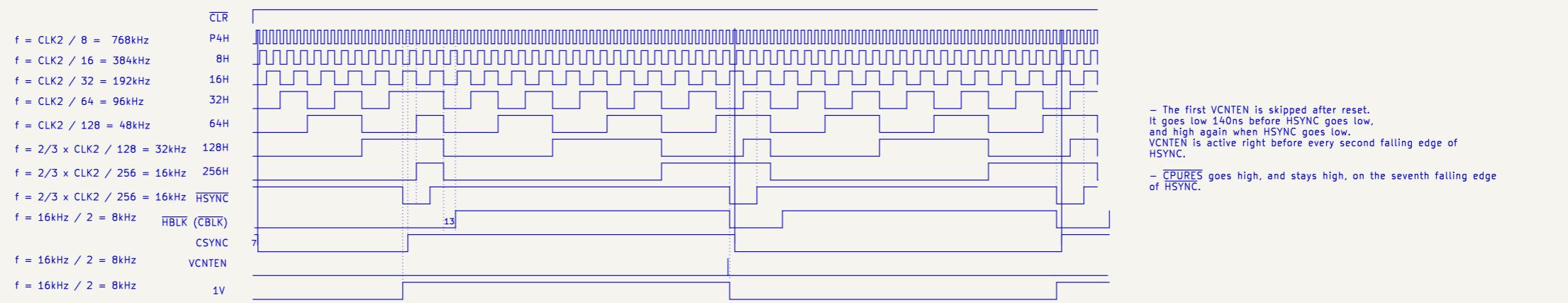


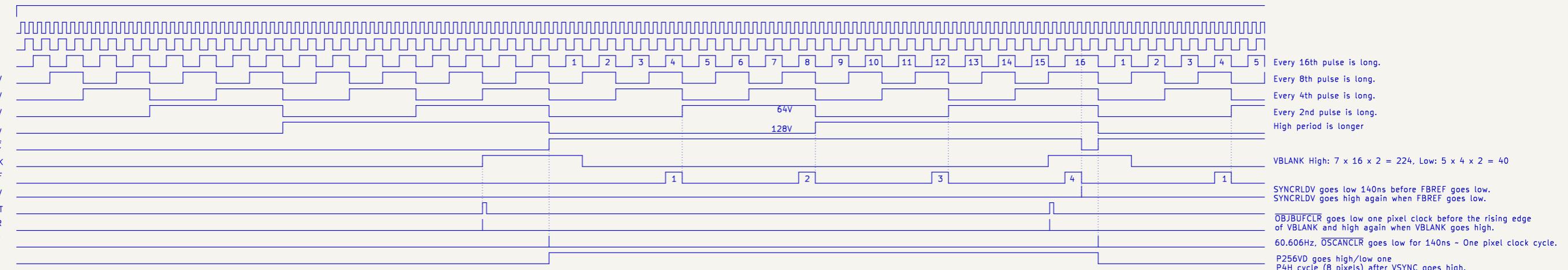
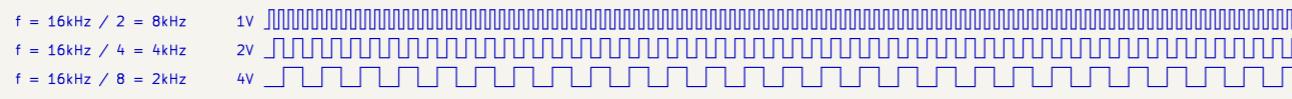
Horizontal signals



- The first VCNTEN is skipped after reset.
It goes low 140ns before HSYNC goes low,
and high again when HSYNC goes low.
VCNTEN is active right before every second falling edge of
HSYNC.

- CPURES goes high, and stays high, on the seventh falling edge
of HSYNC.

Vertical signals



Konami GX870
Sheet: /Timing diagrams/
File: timing_diagrams.kicad_sch

Title: The Final Round

Size: A3	Date: 2023-05-11
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1	Rev:

Id: 11/12	8
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A

B

C

D

E

F

A

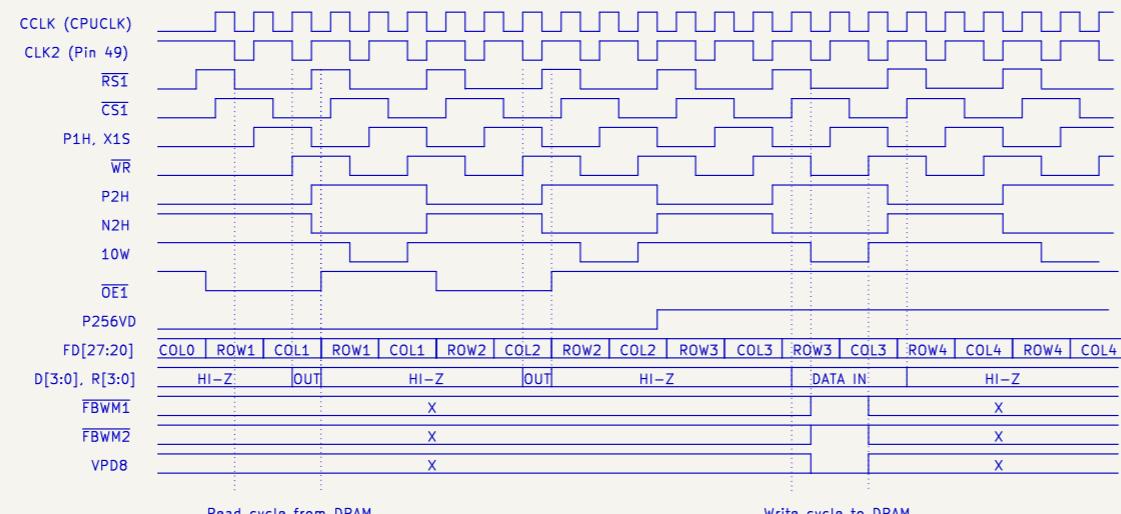
B

C

D

E

F

**Konami GX870**

Sheet: /Sprite timing diagrams/
File: sprite_timing_diagrams.kicad_sch

Title: The Final Round

Size: A3	Date: 2023-05-11
KiCad E.D.A. kicad-cli 7.0.10-7.0.10-ubuntu20.04.1	Rev: 12/12