

Pixel clock PXCK6M: f = 6MHz

Horizontal sync and blanking signals have a period of 64us and frequency 15.625kHz.
Pixels / horizontal line = 64us x 6MHz = 384 pixels.

Vertical sync and blanking signals have a period of 16.896ms and a frequency of 59.1856Hz.
Vertical lines = 16.896ms x 15.625kHz = 264 lines.

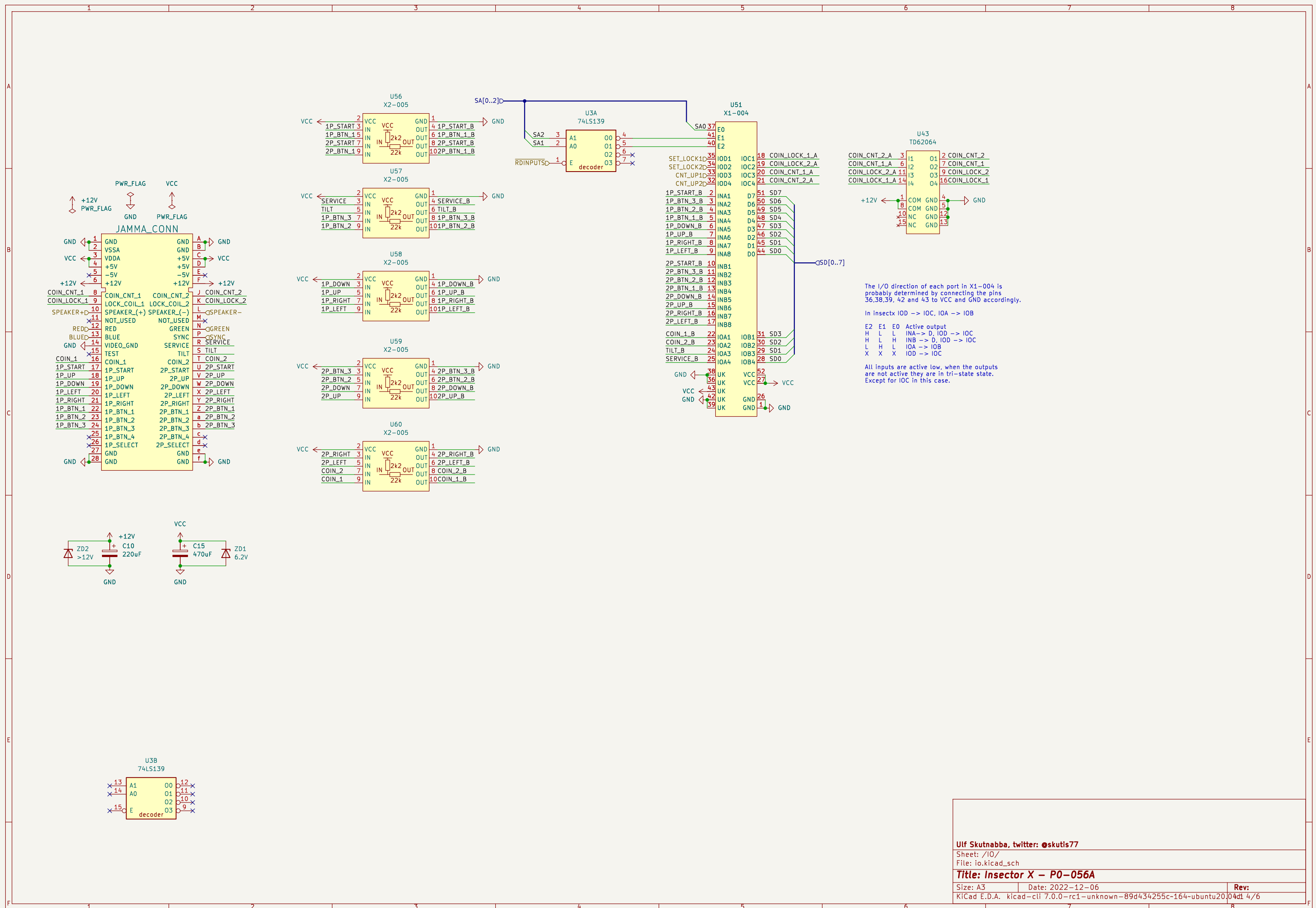
The composite SYNC signal is a wired and of HSYNC and VSYNC

	HSYNC	HSYNC	HSYNC
Active pixels	320	256	352
Blank pixels	64	128	32

	VSYNC	VSYNC	VSYNC
Active lines	224	256	
Blank lines	40	8	

	HSYNC	HSYNC	HSYNC
Front porch	32	4	16
Back porch	32	28	24

When VSYNC goes low there is a delay of 60 pixels (10us x 6MHz) from HSYNC going high. It is the same delay when VSYNC goes high.



Rev:

