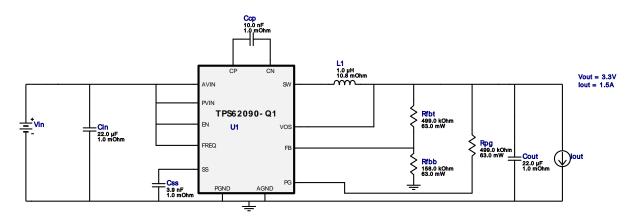


WEBENCH® Design Report

VinMin = 3.4V VinMax = 6.0V Vout = 3.3V Iout = 1.5A Device = TPS62090QRGTRQ1 Topology = Buck Created = 2020-02-12 16:18:44.038 BOM Cost = \$1.65 BOM Count = 9 Total Pd = 0.28W

Design: 83 TPS62090QRGTRQ1 TPS62090QRGTRQ1 3.4V-6V to 3.30V @ 1.5A



Design Alerts

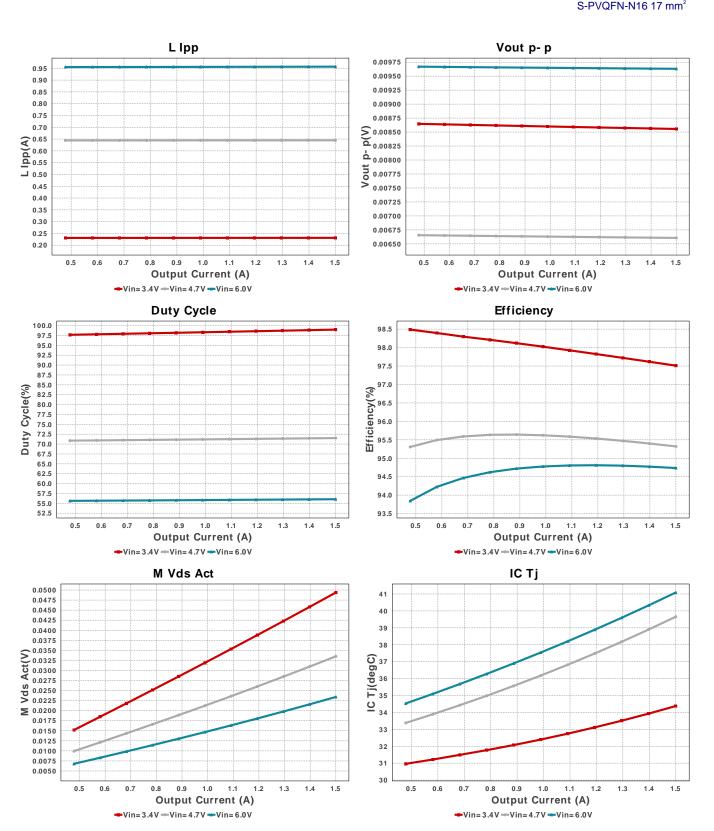
Comonent Selection Information

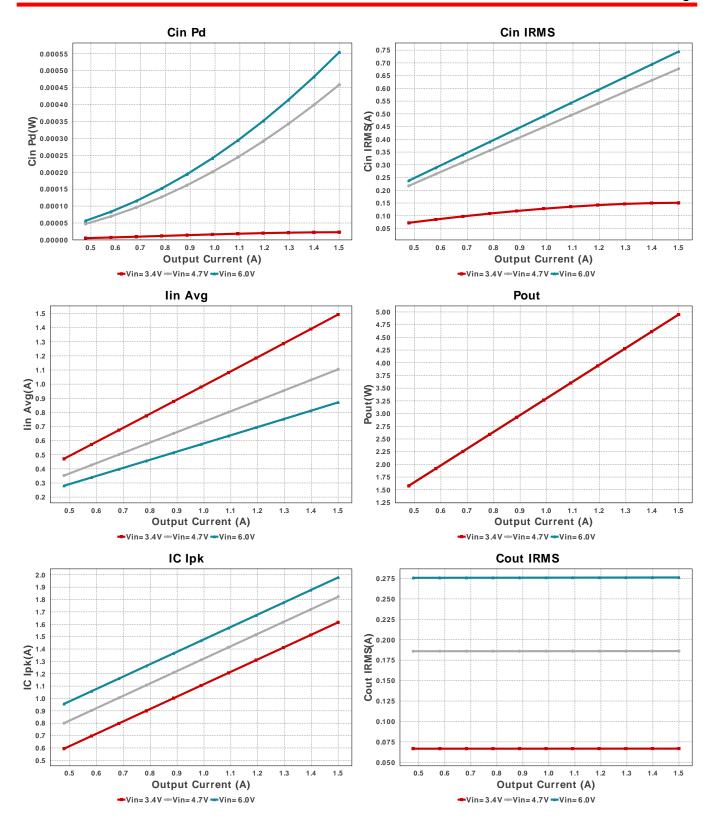
The TPS62090-Q1 is qualified for Automotive applications. All passive and other componets selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application.

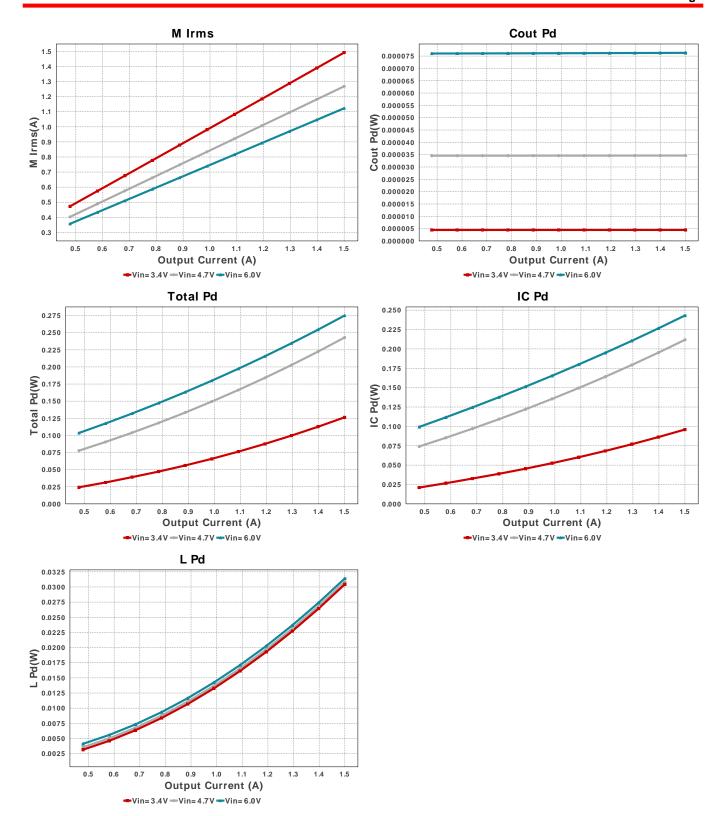
Electrical BOM

Name	Manufacturer	rer Part Number Properties Q		Qty	Price	Footprint	
Сср	MuRata	GRM155R71E103KA01D Series= X7R	Cap= 10.0 nF ESR= 1.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²	
Cin	MuRata	GRM188R60J226MEA0D Series= X5R	Cap= 22.0 uF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 6.0 A	1	\$0.05	0603 5 mm ²	
Cout	MuRata	GRM188R60J226MEA0D Series= X5R	Cap= 22.0 uF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 6.0 A	1	\$0.05	0603 5 mm ²	
Css	MuRata	GRM033R71A392KA01D Series= X7R	Cap= 3.9 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0201 2 mm ²	
L1	Coilcraft	XFL4020-102MEB	L= 1.0 μH 10.8 mOhm	1	\$0.61	XFL4020 25 mm ²	
Rfbb	Vishay-Dale	CRCW0402158KFKED Series= CRCWe3	Res= 158.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²	
Rfbt	Vishay-Dale	CRCW0402499KFKED Series= CRCWe3	Res= 499.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²	
Rpg	Vishay-Dale	CRCW0402499KFKED Series= CRCWe3	Res= 499.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²	

Name Manufacturer Part Number Properties	Qty	Price	Footprint
U1 Texas Instruments TPS62090QRGTRQ1 Switcher	1	\$0.89	







Operating Values

	#	Name	Value	Category	Description
-	1.	Cin IRMS	744.501 mA	Capacitor	Input capacitor RMS ripple current
	2.	Cin Pd	554.28 μW	Capacitor	Input capacitor power dissipation
	3.	Cout IRMS	276.254 mA	Capacitor	Output capacitor RMS ripple current
	4.	Cout Pd	76.316 µW	Capacitor	Output capacitor power dissipation
	5.	IC lpk	1.978 A	IC	Peak switch current in IC
	6.	IC Pd	243.07 mW	IC	IC power dissipation
	7.	IC Tj	41.084 degC	IC	IC junction temperature
	8.	IC Tolerance	16.0 mV	IC	IC Feedback Tolerance
	9.	ICThetaJA	45.6 degC/W	IC	IC junction-to-ambient thermal resistance
	10.	lin Avg	870.85 mA	IC	Average input current
	11.	L lpp	956.97 mA	Inductor	Peak-to-peak inductor ripple current

#	Name	Value	Category	Description
12.	L Pd	31.405 mW	Inductor	Inductor power dissipation
13.	M1 Irms	1.123 A	Mosfet	Q lavg
14.	M Vds Act	23.403 mV	Mosfet	Voltage drop across the MosFET
15.	Cin Pd	554.28 μW	Power	Input capacitor power dissipation
16.	Cout Pd	76.316 μW	Power	Output capacitor power dissipation
17.	IC Pd	243.07 mW	Power	IC power dissipation
	L Pd	31.405 mW	Power	Inductor power dissipation
19.	Total Pd	275.102 mW	Power	Total Power Dissipation
20.	BOM Count	9	System	Total Design BOM count
			Information	
21.	Duty Cycle	56.044 %	System	Duty cycle
			Information	
22.	Efficiency	94.735 %	System	Steady state efficiency
			Information	
23.	FootPrint	65.0 mm ²	System	Total Foot Print Area of BOM components
			Information	
24.	Frequency	1.581 MHz	System	Switching frequency
			Information	
25.	lout	1.5 A	System	lout operating point
			Information	
26.	Mode	CCM	System	Conduction Mode
			Information	
27.	Pout	4.95 W	System	Total output power
			Information	
28.	Total BOM	\$1.65	System	Total BOM Cost
			Information	
29.	Vin	6.0 V	System	Vin operating point
			Information	
30.	Vout	3.3 V	System	Operational Output Voltage
			Information	
31.	Vout Actual	3.327 V	System	Vout Actual calculated based on selected voltage divider resistors
			Information	
32.	Vout Tolerance	3.565 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider
			Information	resistors if applicable
33.	Vout p-p	9.632 mV	System	Peak-to-peak output ripple voltage
			Information	

Design Inputs

Name	Value	Description
lout	1.5	Maximum Output Current
VinMax	6.0	Maximum input voltage
VinMin	3.4	Minimum input voltage
Vout	3.3	Output Voltage
base_pn	TPS62090-Q1	Base Product Number
source	DC	Input Source Type
Та	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.4V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

- 1. Feature Highlights: Automotive Qualified 2.5V to 6V Vin, DCS-Control
- 2. TheTPS62090-Q1 is qualified for Automotive applications. All passive componentsselected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application
- 3. Master key: 9697144EE3E54A83[v1]
- 4. TPS62090-Q1 Product Folder: http://www.ti.com/product/TPS62090%2DQ1: contains the data sheet and other resources.

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