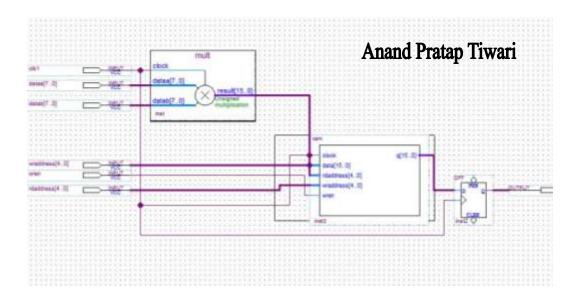
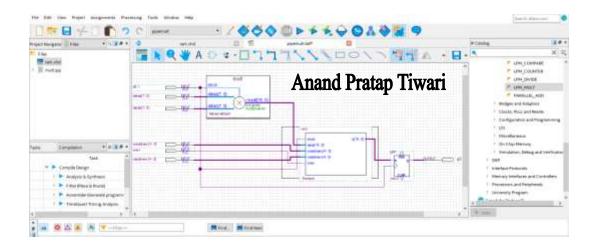
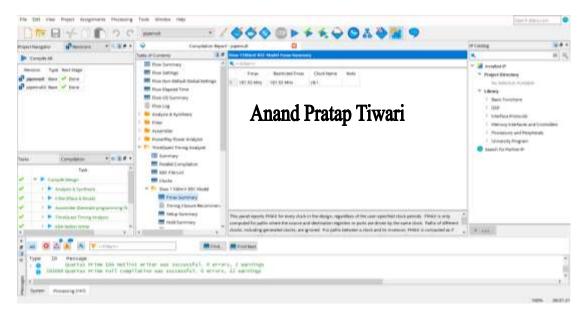
Screen Shot Submission Of Coursera (Introduction To FPGA Design For Embeded System)-Anand Pratap Tiwari

Video-6(FPGA Schematic Diagram)

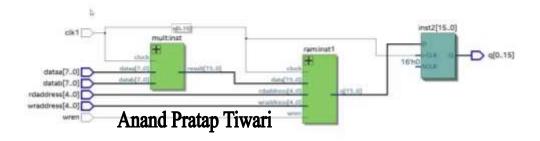




Video7(F-Max Summary)



Video 8(RTL View Of The FPGA Circuit)



Video 10(F-Max Summary)

The State Property Assignments Property State Windows Falls

Anand Pratap Tiwari

The Respond to the State State

Video 11(Graphical Analysis of FPGA)

