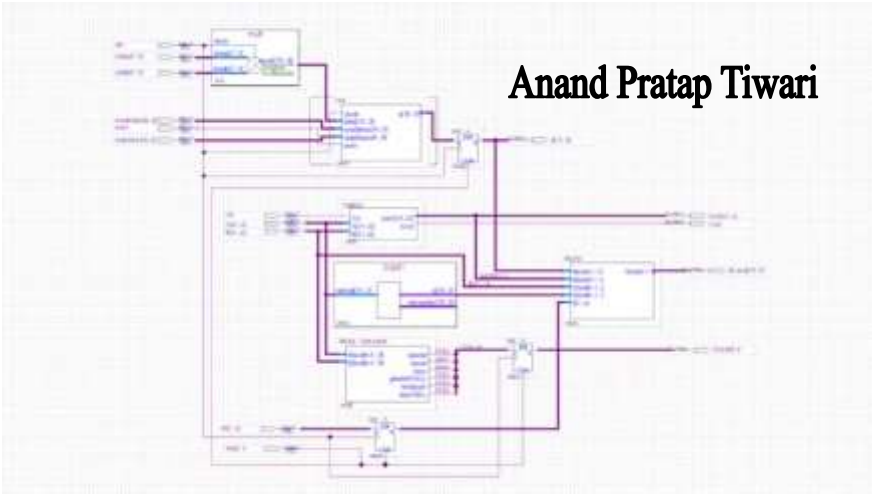
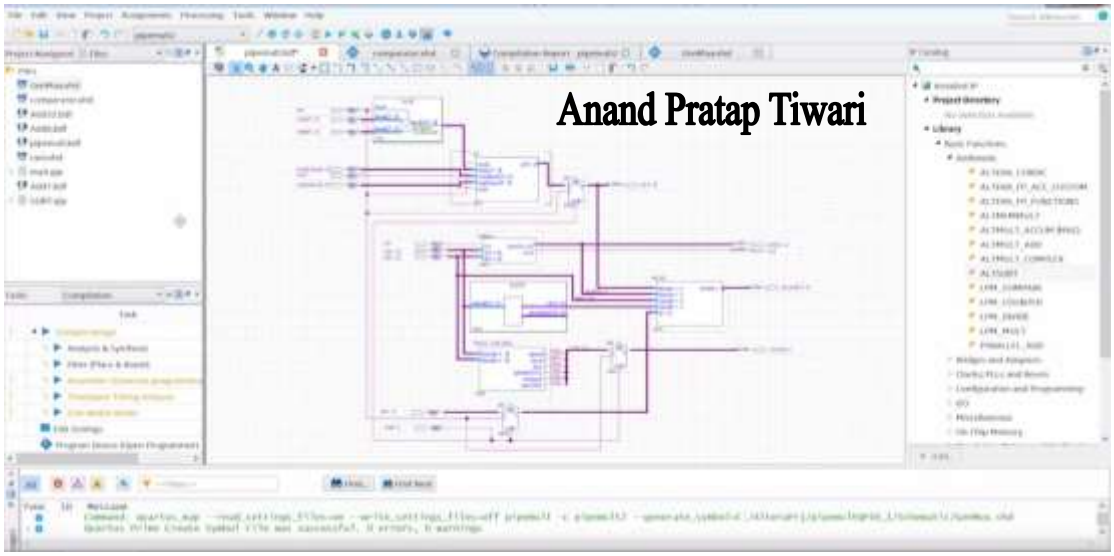
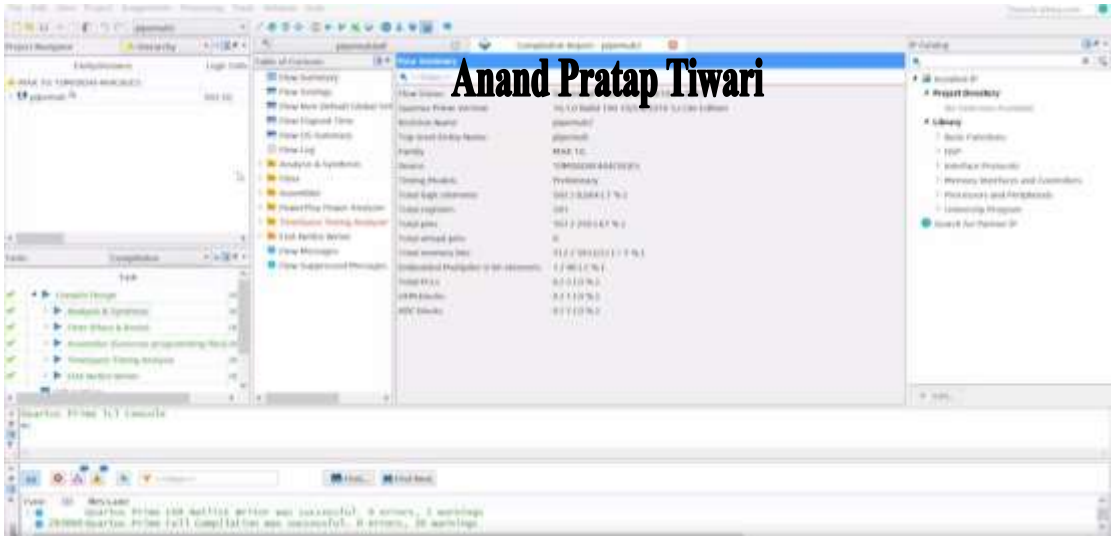
Screen Shot Submission Of Coursera (Introduction To FPGA Design For Embeded System)- Anand Pratap Tiwari

Video-3(FPGA Schematic Diagram)

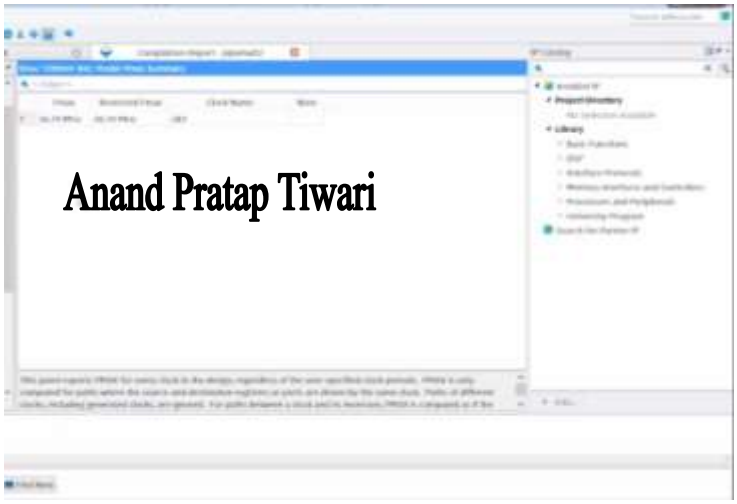




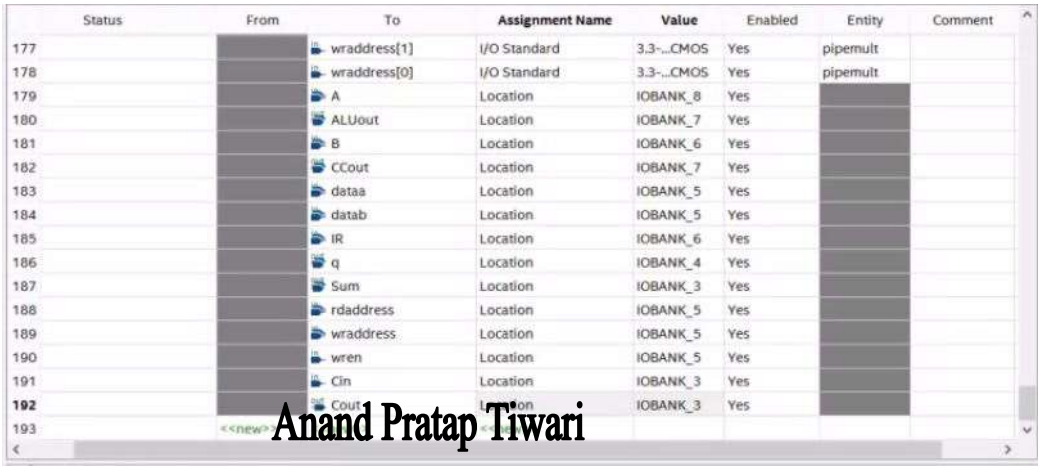
Video-4(Compilation Report)

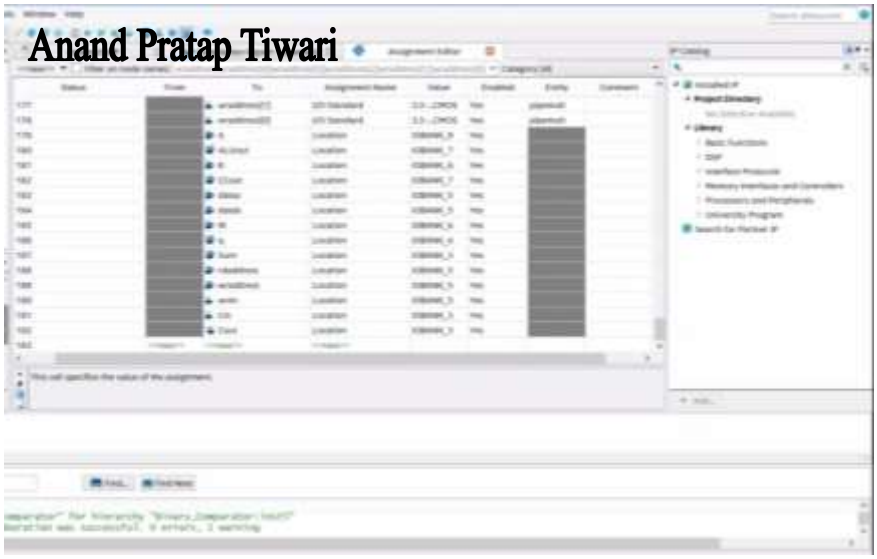


Video-4(F- Max % Utilization)



Video-6(The Assignment Editor Showing “Signal Wren”)





Video-9(Compilation Report)