

P0.19 => I/O, DSR1, SDA1
P0.20 => I/O, DTR1, SCL1
P0.21 => I/O, R1, CAN1-RD1

P0.10 => I/O, TXD2, SDA2
P0.01 => I/O, CAN1-TD1, RXD3, SCL1
P0.00 => I/O, CAN1-RD1, TXD3, SDA1

P2.00 => I/O, PWM1[1], TXD1
P2.01 => I/O, PWM1[2], RXD1
P2.02 => I/O, PWM1[3], CTS

P2.12 => I/O, EINT2
P0.11 => I/O, RXD2, SCL2
P2.13 => I/O, EINT3

P4.28 => I/O, RX_MCLK, TXD3
P0.04 => I/O,
P0.05 => I/O

P1.29 => I/O
P1.28 => I/O,
P1.27 => I/O
P1.26 => I/O
P1.25 => I/O
P1.24 => I/O, PWM1[5], MOSIO

P2.03 => I/O, PWM1[4]
P2.04 => I/O, PWM1[5]
P2.07 => I/O
P2.05 => I/O, PWM1[6]

P0.16 => I/O, RXD1, SSEL
P2.11 => I/O, I2STX_CLK, EINT4

P0.15 => I/O, TXD1, SCK
P0.18 => I/O, CTS1, MOSI
P1.30 => I/O, AD0[4]

P0.18 => I/O, MOSI
P1.23 => I/O, PWM1[4]
P0.15 => I/O, SCK

P1.31 => I/O, SCK1, AD0[5]
P3.25 => I/O, PWM1[2]
P3.26 => I/O, PWM1[3]
P0.17 => I/O, MISO

P0.15 => I/O, TXD1, SCK
P0.16 => I/O, RXD1, SSEL
P1.23 => I/O, PWM1[4]
P2.11 => I/O, I2STX_CLK, EINT4
P1.31 => I/O, SCK1, AD0[5]
P0.18 => I/O, MOSI
P2.06 => I/O
P0.17 => I/O, MISO
P3.25 => I/O, PWM1[2]
P3.26 => I/O, PWM1[3]

P0.25 => I/O, AD0[2], I2SRX_SDA, TXD3
P0.24 => I/O, AD0[1], I2SRX_WS
P0.23 => I/O, AD0[0], I2SRX_CLK

