VLSI Lab

	Task Name	Duration	Start	Finish	Contributors	Status	Comments
1	Exploring the Website	7d	01/23/16	02/01/16	Jigar, Shefali, Prabhsimran	Complete	A lot of defects.
2	Write Test Cases	7d	01/24/16	02/01/16	Jigar, Shefali, Prabhsimran	Complete	
3	Quality Metrics	4d	01/27/16	02/01/16	Jigar, Shefali	Complete	
4	Tools to look up into the code	6d	01/25/16	02/01/16	Jigar, Shefali, Prabhsimran	Complete	
5	Code Metrics	4d	01/27/16	02/01/16	Prabhsimran, Shefali	Complete	
6	Read all the documentation	7d	01/24/16	02/01/16	Shefali, Jigar	Complete	
7	Draw various UML Diagrams	5d	01/26/16	02/01/16	Jigar, Prabhsimran	Complete	
8	Discuss various pros and cons regarding the website	7d	01/24/16	02/01/16	Jigar, Shefali, Prabhsimran	Complete	
9	Coding	5d	01/26/16	02/01/16	Jigar, Shefali, Prabhsimran	Complete	
10	Review the Code	3d	01/28/16	02/01/16	Jigar, Prabhsimran	Complete	
11	Run Metrics for the new Code	3d	01/28/16	02/01/16	Shefali, Jigar	Complete	
12	Testing	2d	01/30/16	02/01/16	Jigar, Prabhsimran	Complete	
13	Fix Bugs	2d	01/30/16	02/01/16	Shefali, Prabhsimran	Complete	