Lab Name Experiment i	id Feature	Requirements		est Case	Test	Test Steps	Expected Status	Test Case	Pre/Post		Dev Owner Reviewed History	Additional		
- роллоне	Usability	User should be able to view	Ty	ositive	To view the Introduction	1.Click on the "start the	Result  1.Introduction page should  Passed	Owner Shefali	Conditions  Refer to the test step id 1	Environment Required OS: Linux Browsers:		Comments		
		the Introduction of VLSI Lab Experiment 2.			page of the VLSI Lab Experiment 2	experiment" link below "2. Schematic Design Of Transistor	be opened 2.Content should be present with no distortions and			Firefox,Chrom e Bandwidth: 100Mbps Hardware				
						Level NAND & NOR Gate." on "List of Experiments"	Junk			Hardware Configuration: 4GBRAM , Processor:i3				
						page of VLSI Lab. 2.It should re- direct to the Introduction								
VLSI Lab	2 Usability	User should be able to view	2 P	ositive	To view the Objective page	page of experiment.  1.Click on the "Objective" tab		Shefali	Refer to the test step id 1	OS: Linux Browsers:				
		the Objective of VLSI Lab Experiment 2.			of the VLSI Lab Experiment 2	of the page.	be opened 2.Content should be present with no distortions and			Firefox,Chrom e Bandwidth: 100Mbps Hardware				
VLSI Lab	2 Usability	User should be	3 P	Positive	To view the	1.Click on the	Junk characters  1.User should Passed	Shefali	Refer to the test step id 2	Configuration: 4GBRAM , Processor:i3				
		able to view the Manual page on clicking the "Manual" link			Experiment page	2. Click on the "Click Here For Experiment Manual" link in	page and Video should be played		test step id 2	Browsers: Firefox,Chrom e Bandwidth: 100Mbps				
		on Virtual Experiment page.				"Manual" Tab	showing the manual. 2.Content should be present with no			Hardware Configuration: 4GBRAM , Processor:i3				
VLSI Lab	2 Usability	User should be able to view	4 P	Positive	To view the	1.Click on the	distortions and Junk characters  1.User should Failed	Shefali	Refer to the test step id 2	OS: Linux Browsers:	1.In Firefox browser a			
		the Experiment page on clicking the "Experiment"			Experiment page	Experiment " Tab. 2. Click on the "Experiment"	be re-directed to the Experiment page and UI should be		test step id 2	Browsers: Firefox,Chrom e Bandwidth: 100Mbps Hardware	browser a message is displayed saying "Java Appelt option is off in your			
		link on Virtual Experiment page.				link in "Virtual Experiment " Tab	displayed to perform test. 2.Content should be present with no			Hardware Configuration: 4GBRAM , Processor:i3	is off in your Brouser :(". 2.No such message is displayed in			
VLSI Lab	2	Heer :	5	)oetti	To vite or	1.0%	distortions and Junk characters	Ot- 5 "	Dof	Oc. I.	Chrome browser and the page is empty.			
	Usability	User should be able to view the Procedure of VLSI Lab Experiment 2.	P	Positive	To view the Procedure page of the VLSI Lab Experiment 2	"Proceure" tab of the page.	be opened 2.Content should be	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e Bandwidth:				
VLSI Lab	2						present with no distortions and Junk characters			100Mbps Hardware Configuration: 4GBRAM, Processor:i3				
	Usability	User should be able to view the Theory of VLSI Lab	6 P	Positive	To view the Theory page of the VLSI Lab Experiment 2	1.Click on the "Theory" tab of the page.	2.Content	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e				
		Experiment 2.					should be present with no distortions and Junk characters			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM,				
VLSI Lab	2 Usability	User should be able to view the Quiz of	7 P	ositive	To view the Quiz page of the VLSI Lab	1.Click on the "Quiz" tab of the page.	1.Quiz page Failed should be opened	Shefali	Refer to the test step id 1	Processor:i3 OS: Linux Browsers: Firefox,Chrom	1. Some of the pictures are not displayed on the page in			
		VLSI Lab Experiment 2.			Experiment 2		2.Content should be present with no distortions and Junk			e Bandwidth: 100Mbps Hardware Configuration:	on the page in Firefox as well as Chrome.			
VLSI Lab	2 Usability	User should be able to view the References		Positive	To view the References page of the	1.Click on the "References" tab of the	characters  1.References Passed page should	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers: Firefox,Chrom				
		the References of VLSI Lab Experiment 2.			page of the VLSI Lab Experiment 2	tab of the page.	be opened 2.Content should be present with no distortions and			e Bandwidth : 100Mbps Hardware				
VLSI Lab	2 Usability	User should be able to view	9 P	ositive	To view the Feedback	1.Click on the "Feedback" tab	Junk characters  1.Feedback Passed	Shefali	Refer to the test step id 1	Configuration: 4GBRAM , Processor:i3 OS: Linux Browsers:				
		the Feedback of VLSI Lab Experiment 2.			page of the VLSI Lab Experiment 2	of the page.	be opened 2.Content should be present with no		, Joe IU I	Firefox,Chrom e Bandwidth : 100Mbps				
VLSI Lab	2 Usability	User should be	10	ositive	To submit the	1.Fill the	distortions and Junk characters  1.Feedback Passed	Shefali	Refer to the	Hardware Configuration: 4GBRAM , Processor:i3				
		able to submit the Feedback form of VLSI Lab Experiment 2.			Feedback form of the VLSI Lab	"Feedback" form and click	form should submitted successfully. 2.Successfull submission		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps				
VLSI Lab	2				-		message should be displayed.			Hardware Configuration: 4GBRAM , Processor:i3				
	Functionality	User should be able to select any component from the	11   P	ositive		1.Fill the "Feedback" form and click on submit button.	1.Feedback form should submitted successfully. 2.Successfull	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e Bandwidth:				
\/  Q    ab	2	available icons on experiment page form of VLSI Lab			pointiettl Z		2.Successfull submission message should be displayed.			100Mbps Hardware Configuration: 4GBRAM,				
VLSI Lab	Functionality	User should be able to rotate any component	12 P	Positive	components	1.select a component. 2.Click on the component	1.The Passed component should be rotated	Shefali	Refer to the test step id 1	Processor:i3 OS: Linux Browsers: Firefox,Chrom e				
		from the available icons on experiment page form of VLSI Lab			from experiment page of the	and select "Rotate" in the dialogue box to rotate it.	successfully.			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM,				
VLSI Lab	2 Functionality	User should be able to relocate any	13 P	Positive	To relocate the available circuit	1.select a component. 2.Click on the	1.The Passed component should be	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers: Firefox,Chrom				
		component from the available icons on experiment page form of			components from experiment page of the VLSI Lab	component and drag it to reocate it.	rlocated successfully.			e Bandwidth : 100Mbps Hardware Configuration:				
VLSI Lab	2 Functionality	VLSI Lab Experiment 2. User should not be able to	14 N	legative	Experiment 2  Any component	1.select a component.	1.The Passed component should not be	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers:				
		relocate a component on any other comonent on experiment			could not be put over another	2.Click on the component and drag it to put it over another	should not be relocated.			Firefox,Chrom e Bandwidth : 100Mbps Hardware				
VLSI Lab	2 Functionality	page form of VLSI Lab Experiment 2. User should be		ositive	To join the	component.  1.Select wire	1.The Passed	Shefali	Refer to the	Configuration: 4GBRAM , Processor:i3 OS: Linux				
		able to join the components with wire on experiment page form of			circuit components using wire from experiment	component to join that end	components should be connected successfully.		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps				
VLSI Lab	2 Functionality	VLSI Lab Experiment 2.	16	Negative	page of the	point to other component's end point.	1.User should Passed	Shefali	Refer to the	Hardware Configuration: 4GBRAM , Processor:i3				
	. Griodoriality	not be able to end wire on without connecting a		Junive	from experiment page of the VLSI Lab	component. 2.Click on the red squares of a component	not be able to end the wire. 2.The wire should be bent	SHOIAII	test step id 1	Browsers: Firefox,Chrom e Bandwidth:				
		component's red square on its end point experiment page form of			Experiment 2		from that place on which user clicked.			100Mbps Hardware Configuration: 4GBRAM , Processor:i3				
VLSI Lab	2 Functionality	VLSI Lab Experiment 2. User should able to	17 P	Positive		connect the	1.User should Failed able to see the NAND gate	Shefali	Refer to the test step id 1	OS: Linux Browsers:	1. "The circuit is not complete.			
		simulate working of NAND gate on experiment page form of			NAND Gate on experiment page of the VLSI Lab Experiment 2	described on Procedure page to create a NAND	NAND gate simulation in graph.			Firefox,Chrom e Bandwidth: 100Mbps Hardware	complete, Please Complete it and try again" message is			
		VLSI Lab Experiment 2.				circuit. 2.Press "Simulate" button on top. 3.Observe the				Configuration: 4GBRAM , Processor:i3	displayed.			
VLSI Lab	2 Functionality	User should able to simulate working of	18 P	Positive	NOR Gate on	graph created.  1.Select and connect the components as	1.User should able to see the NOR gate simulation in	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom	1. "The circuit is not complete, Please			
		NOR gate on experiment page form of VLSI Lab			Experiment 2	Procedure page to create a NOR circuit. 2.Press	simulation in graph.			e Bandwidth: 100Mbps Hardware Configuration:	Please Complete it and try again" message is displayed.			
VLSI Lab	2 Functionality	Experiment 2.  User should be		Positive	To change	"Simulate" button on top. 3.Observe the graph created.  1.Select a	1.User should Passed	Shefali	Refer to the	4GBRAM , Processor:i3				
	Sugnailty	able to change a component's properties on experiment			component's property on experiment page of the	component of an already created circuit. 2.change the	be able to see change in the output wave form.		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps				
\/  Q    ab	2	page form of VLSI Lab Experiment 2.			Experiment 2	dialogue box. 3.Click on O.K. to save				100Mbps Hardware Configuration: 4GBRAM , Processor:i3				
VLSI Lab	2 Usability	be able to run the virtual experiment on	20 F	Positive	To be able to run the virtual experiment	changes.  1) Go to the home page, select virtual experiment.	User should be able to run the experiment successfully.	Prabhsimran	Refer to the test step id 1	OS: Max OS X Browser : Safari Bandwidth :	"Java not updated"			
		Safari.				النصر م				100Mbps Hardware Configuration : 4GB RAM				
VLSI Lab	2 Usability	User should be able to run the virtual	21 F	Positive	To be able to run the virtual experiment	1) Go to the home page, select virtual	User should be Failed able to run the experiment	Jigar	Refer to the test step id 1	Processor : Intel OS: Max OS X Browser : Safari	"Java not updated"			
		experiment on Firefox,			J. Joinnoll	experiment.	successfully.			Bandwidth : 100Mbps Hardware Configuration :				
VLSI Lab	2 Usability	User should be able to view all	22 F	Positive	To be able to view all images	homepage	All questions Failed should be	Prabhsimran	Refer to the test step id 1	4GB RAM Processor: Intel OS: Max OS X Browser:				
		the questions of the quiz and all question and answer			in the quiz	select quiz tab			, J.op IU I	Safari Bandwidth : 100Mbps Hardware				
VLSI Lab	2 Usability	images User should be	22	Positive	To be able to	1) Go to	All questions Failed	Prabhsimran	Refer to the	Configuration: 4GB RAM Processor: Intel OS: Max OS X				
	<i>,</i>	able to view all the questions of the quiz and all question and answer			view all images in the quiz	homepage	should be correctly and	J.S. ISHIH ALI	test step id 1	Browser: Firefox Bandwidth: 100Mbps Hardware				
VLSI Lab	2	images								Configuration : 4GB RAM Processor : Intel				
	Usability	User should be able to view all the questions of the quiz and all question		Positive		homepage select quiz tab	All questions should be correctly and completely visible	Prabhsimran	Refer to the test step id 1	OS: Max OS X Browser : Chrome Bandwidth : 100Mbps				
V/I OLL 5-1-	2	all question and answer images				, yw.16.				Hardware Configuration : 4GB RAM Processor :				
VLSI Lab	2 Fucntionality	User should be able to view all the options in quiz		Positive		1) Go to homepage select quiz tab and go to post	All options in each questions should be correctly and	Jigar	Refer to the test step id 1	Intel OS: Max OS X Browser: Safari Bandwidth:				
		completely			Joury	quiz.	correctly and completely visible			100Mbps Hardware Configuration : 8GB RAM				
VLSI Lab	2 Functionality	User should be able to view all the options in	22 F	Positive	To be able to view all options in the quiz	homepage select quiz tab	All options in each questions should be	Jigar	Refer to the test step id 1	Processor: Intel core i5 OS: Max OS X Browser: Safari				
		quiz completely			correctly	and go to post quiz.	correctly and completely visible			Bandwidth: 100Mbps Hardware Configuration: 8GB RAM				
VLSI Lab	2 Functionality	User should be able to view all	22 F	Positive	To be able to view all options	homepage	All options in each questions	Jigar	Refer to the test step id 1	Processor : Intel core i5 OS: Max OS X Browser :				
		the options in quiz completely			in the quiz correctly	select quiz tab and go to post quiz.	should be correctly and completely visible		۱ ۱۸۰ م د.	Safari Bandwidth: 100Mbps Hardware Configuration:				
VLSI Lab	2									Configuration : 8GB RAM Processor : Intel core i5				