Lab Name Experiment i	d Feature	Requirements		est Case	Test	Test Steps	Expected Status	Test Case	Pre/Post		Dev Owner Reviewed History	Additional			
	Usability	User should be able to view	Ту	ype	To view the Introduction	1.Click on the "start the	1.Introduction page should Passed	Owner Shefali	Conditions  Refer to the test step id 1	Environment Required OS: Linux Browsers:		Comments			
		the Introduction of VLSI Lab Experiment 2.			page of the VLSI Lab Experiment 2	experiment" link below "2. Schematic Design Of Transistor	be opened 2.Content should be present with no distortions and			Firefox,Chrom e Bandwidth: 100Mbps Hardware					
						Level NAND & NOR Gate." on "List of Experiments"	Junk			Hardware Configuration: 4GBRAM , Processor:i3					
						page of VLSI Lab. 2.It should re- direct to the Introduction									
VLSI Lab	2 Usability	User should be able to view	2 Pe	Positive	To view the Objective page	page of experiment.  1.Click on the "Objective" tab		Shefali	Refer to the test step id 1	OS: Linux Browsers:					
		the Objective of VLSI Lab Experiment 2.			of the VLSI Lab Experiment 2	of the page.	be opened 2.Content should be present with no distortions and			Firefox,Chrom e Bandwidth: 100Mbps Hardware					
VLSI Lab	2 Usability	User should be	3 Po	Positive	To view the Experiment	1.Click on the	Junk characters  1.User should Passed	Shefali	Refer to the test step id 2	Configuration: 4GBRAM , Processor:i3					
		able to view the Manual page on clicking the "Manual" link			Experiment page	Manual" link in	to the Manual page and Video should be played		test step id 2	Browsers: Firefox,Chrom e Bandwidth: 100Mbps					
		on Virtual Experiment page.				"Manual" Tab	showing the manual.  2.Content should be present with no			Hardware Configuration: 4GBRAM , Processor:i3					
VLSI Lab	2 Usability	User should be	4 Po	Positive	To view the	1.Click on the	distortions and Junk characters  1.User should Failed	Shefali	Refer to the	OS: Linux	1.In Firefox				
		able to view the Experiment page on clicking the "Experiment"			Experiment page	"Virtual Experiment " Tab. 2. Click on the "Experiment"	be re-directed to the Experiment page and UI should be		test step id 2	Browsers: Firefox,Chrom e Bandwidth: 100Mbps	browser a message is displayed saying "Java Appelt option				
		link on Virtual Experiment page.				link in "Virtual Experiment " Tab	displayed to perform test.  2.Content should be present with no			Hardware Configuration: 4GBRAM , Processor:i3	is off in your Brouser :(". 2.No such message is				
VLSI Lab	2						distortions and Junk characters				displayed in Chrome browser and the page is empty.				
	Usability	User should be able to view the Procedure of VLSI Lab Experiment 2.	5 Po	ositive	To view the Procedure page of the VLSI Lab Experiment 2	1.Click on the "Proceure" tab of the page.	1.Procedure page should be opened 2.Content should be	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e Bandwidth:					
VII OLL ST		Experiment 2.			Experiment 2		should be present with no distortions and Junk characters			100Mbps Hardware Configuration: 4GBRAM ,					
VLSI Lab	2 Usability	User should be able to view the Theory of VLSI Lab	6 Po	Positive	To view the Theory page of the VLSI Lab Experiment 2	1.Click on the "Theory" tab of the page.	2.Content	Shefali	Refer to the test step id 1	Processor:i3 OS: Linux Browsers: Firefox,Chrom e					
		Experiment 2.					should be present with no distortions and Junk			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM,					
VLSI Lab	2 Usability	User should be able to view the Quiz of	7 Po	Positive	To view the Quiz page of the VLSI Lab	1.Click on the "Quiz" tab of the page.	1.Quiz page Failed should be opened	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers: Firefox,Chrom	1. Some of the pictures are not displayed				
		VLSI Lab Experiment 2.			Experiment 2		2.Content should be present with no distortions and Junk			e Bandwidth: 100Mbps Hardware Configuration:	on the page in Firefox as well as Chrome.				
VLSI Lab	2 Usability	User should be able to view		ositive	To view the References	1.Click on the "References"	1.References Passed page should	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers:					
		the References of VLSI Lab Experiment 2.			page of the	tab of the page.	be opened 2.Content should be present with no distortions and			Firefox,Chrom e Bandwidth: 100Mbps Hardware					
VLSI Lab	2 Usability	User should be	9 P	Positive	To view the	1.Click on the	Junk characters  1.Feedback Passed	Shefali	Refer to the	Configuration: 4GBRAM , Processor:i3 OS: Linux					
		able to view the Feedback of VLSI Lab Experiment 2.			Feedback	"Feedback" tab of the page.	page should be opened 2.Content should be present with no		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps					
VLSI Lab	2		40		T	4 =	distortions and Junk characters		D .	Hardware Configuration: 4GBRAM , Processor:i3					
	Usability	User should be able to submit the Feedback form of VLSI Lab	10 P	Positive	Lab	form and click	1.Feedback form should submitted successfully. 2.Successfull	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e Bandwidth:					
VLSI Lab	2	Experiment 2.					submission message should be displayed.			100Mbps Hardware Configuration: 4GBRAM, Processor:i3					
	Functionality	User should be able to select any component	11 Po	Positive	Lab	form and click on submit	1.Feedback form should submitted successfully.	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e					
		from the available icons on experiment page form of VLSI Lab			Experiment 2	button.	2.Successfull submission message should be displayed.			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM;					
VLSI Lab	2 Functionality	User should be able to rotate any	12 Po	ositive		1.select a component. 2.Click on the	1.The Passed component	Shefali	Refer to the test step id 1	AGBRAM , Processor:i3 OS: Linux Browsers: Firefox,Chrom					
		component from the available icons on experiment page form of			components from experiment page of the	component	should be rotated successfully.			e Bandwidth: 100Mbps Hardware Configuration:					
VLSI Lab	2 Functionality	VLSI Lab Experiment 2. User should be able to	13 Po	ositive	Experiment 2  To relocate the available	1.select a component.	1.The Passed component	Shefali	Refer to the test step id 1	4GBRAM , Processor:i3 OS: Linux Browsers:					
		relocate any component from the available icons on experiment			circuit components from experiment page of the	2.Click on the component and drag it to reocate it.	should be rlocated successfully.			Firefox,Chrom e Bandwidth : 100Mbps Hardware					
VLSI Lab	2 Functionality	page form of VLSI Lab Experiment 2. User should	14 N	legative	VLSI Lab Experiment 2	1.select a	1.The Passed	Shefali		Configuration: 4GBRAM , Processor:i3 OS: Linux					
		not be able to relocate a component on any other comonent on			component could not be put over another	component. 2.Click on the component and drag it to put it over	component should not be relocated.		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps					
VLSI Lab	2 Eupatin	experiment page form of VLSI Lab Experiment 2.	15	Opeitive		another component.	1 The	Oh a f = 11	Refer to "	Hardware Configuration: 4GBRAM , Processor:i3					
	Functionality	User should be able to join the components with wire on experiment		Positive	circuit components using wire from	component to	1.The components should be connected successfully.	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e Bandwidth:					
VLSI Lab	2	page form of VLSI Lab Experiment 2.			experiment page of the	join that end point to other component's				100Mbps Hardware Configuration: 4GBRAM, Processor:i3					
	Functionality	not be able to end wire on without	16 N	Negative	page of the	component. 2.Click on the red squares of	1.User should not be able to end the wire. 2.The wire	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e					
		connecting a component's red square on its end point experiment				a component to join wire.	should be bent from that place on which user clicked.			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM,					
VLSI Lab	2 Functionality	page form of VLSI Lab Experiment 2. User should	17 Po	ositive	To simulate	1.Select and	1.User should Failed	Shefali	Refer to the	Processor:i3 OS: Linux	1. "The circuit				
		able to simulate working of NAND gate on experiment			working of NAND Gate on experiment page of the VLSI Lab	connect the components as described on Procedure page to create	able to see the		test step id 1	Browsers: Firefox,Chrom e Bandwidth: 100Mbps	is not complete, Please Complete it and try again"				
		page form of VLSI Lab Experiment 2.			Experiment 2	a NAND circuit. 2.Press "Simulate" button on top.				Hardware Configuration: 4GBRAM , Processor:i3	message is displayed.				
VLSI Lab	2 Functionality	able to	18 Po	Positive	To simulate working of	3.Observe the graph created.  1.Select and connect the	1.User should able to see the	Shefali	Refer to the test step id 1	OS: Linux Browsers:	1. "The circuit is not			_	
		simulate working of NOR gate on experiment page form of			NOR Gate on experiment page of the VLSI Lab Experiment 2	components as described on Procedure page to create a NOR circuit.	NOR gate simulation in graph.			Firefox,Chrom e Bandwidth : 100Mbps Hardware	complete, Please Complete it and try again" message is				
VLSI Lab	2	VLSI Lab Experiment 2.				2.Press "Simulate" button on top. 3.Observe the graph created.				Configuration: 4GBRAM , Processor:i3	displayed.				
	Functionality	User should be able to change a component's properties on		Positive	To change component's property on experiment	1.Select a component of an already created circuit.	1.User should be able to see change in the output wave	Shefali	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrom e					
		experiment page form of VLSI Lab Experiment 2.			page of the VLSI Lab Experiment 2	2.change the property available on dialogue box. 3.Click on O.K.	form.			Bandwidth: 100Mbps Hardware Configuration: 4GBRAM,					
VLSI Lab	2 Usability	be able to run	20 P	Positive	To be able to run the virtual	to save changes.  1) Go to the home page,	User should be Failed able to run the	Prabhsimran	Refer to the test step id 1	Processor:i3  OS: Max OS X Browser:	"Java not updated"				
		the virtual experiment on Safari.			experiment	select virtual	experiment successfully.			Safari Bandwidth : 100Mbps Hardware Configuration :					
VLSI Lab	2 Usability	User should	21 F	Positive	To be able to	1) Go to the	User should be Failed	Jigar	Refer to the	4GB RAM Processor : Intel OS: Max OS X	"Java not				
		be able to run the virtual experiment on Firefox,			run the virtual	home page, select virtual experiment.	able to run the experiment successfully.		test step id 1	Browser : Safari Bandwidth : 100Mbps Hardware	updated"				
VLSI Lab	2	1			7	4) 7				Configuration : 4GB RAM Processor : Intel					
	Usability	User should be able to view all the questions of the quiz and all question		Positive		homepage select quiz tab	All questions should be correctly and completely visible	Prabhsimran	Refer to the test step id 1	OS: Max OS X Browser : Safari Bandwidth : 100Mbps					
VISILab	2	and answer images								Hardware Configuration : 4GB RAM Processor :					
VLSI Lab	2 Usability	User should be able to view all the questions of the quiz and		Positive	To be able to view all images in the quiz	homepage select quiz tab	All questions should be correctly and completely	Prabhsimran	Refer to the test step id 1	Intel OS: Max OS X Browser: Firefox Bandwidth:					
		all question and answer images				and go to post quiz.	visible			100Mbps Hardware Configuration: 4GB RAM Processor:					
VLSI Lab	2 Usability	User should be able to view all the questions		Positive		homepage select quiz tab	All questions Failed should be correctly and	Prabhsimran	Refer to the test step id 1	Intel OS: Max OS X Browser: Chrome					
		of the quiz and all question and answer images				and go to post quiz.	correctly and completely visible			Bandwidth : 100Mbps Hardware Configuration :					
VLSI Lab	2 Fucntionality	User should be able to view all		Positive	To be able to view all options	1) Go to homepage	All options in each questions	Jigar	Refer to the test step id 1	4GB RAM Processor: Intel OS: Max OS X Browser:					
		the options in quiz completely			in the quiz	select quiz tab and go to post quiz.	should be correctly and completely visible			Safari Bandwidth : 100Mbps Hardware					
VLSI Lab	2 Functionality	User should be	22	Positive	To be able to	1) Go to	All options in Failed	Jigar	Refer to the	Configuration: 8GB RAM Processor: Intel core i5 OS: Max OS X					
	Jaconanty	able to view all the options in quiz completely			view all options in the quiz correctly.	homepage select quiz tab and go to post quiz.	each questions should be correctly and completely	الحود	test step id 1	Browser : Safari Bandwidth : 100Mbps					
VLSI Lab	2						visible.			Hardware Configuration: 8GB RAM Processor: Intel core i5					
	Functionality	User should be able to view all the options in quiz	22 P	Positive	correctly.	homepage select quiz tab and go to post	correctly and	Jigar	Refer to the test step id 1	OS: Max OS X Browser : Safari Bandwidth :					
		completely				quiz.	completely visible.			100Mbps Hardware Configuration: 8GB RAM Processor:					
VLSI Lab	2						<u> </u>			Intel core i5					