Lab Name	Experiment id	Feature	Requirements	Test Case Id	Test Case Type	Test Description	Test Steps	Expected Result	Status	Test Case Owner	Pre/Post Conditions	Data/Environment Required	Dev Owner	Reviewed	History	Additional Comments
VLSI Lab	2	Usability	User should be able to view the Introduction of VLSI Lab Experiment 2.	1	Positive	To view the Introduction page of the VLSI Lab Experiment 2	1.Click on the "start the experiment" link below "2. Schematic Design Of Transistor Level NAND & NOR Gate." on "List of Experiments" page of VLSI Lab. 2.It should re-direct to the Introduction page of experiment.	1.Introduction page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	Experiment should be display error message.	2	Negative	To view the error message if experiment fails to load.	1.Click on the "start the experiment" link below "2. Schematic Design Of Transistor Level NAND & NOR Gate." on "List of Experiments" page of VLSI Lab.	Experiment should be visible	Failed	Rishabh	Refer to the test step id 1	OS: Mac OS X El-Capitan Browsers: Firefox,Chrome, Safari Bandwidth : 100Mbps Hardware Configuration:8 GBRAM , Processor:i5				
VLSI Lab	2	Usability	Experiment should be responsive	3	Negative	To view the Experiment of the VLSI Lab Experiment	1.Click on the "start the experiment" link below "2. Schematic Design Of Transistor Level NAND & NOR Gate." on "List of Experiments" page of VLSI Lab.	Experiment should be visible	Failed	Rishabh	Refer to the test step id 1	OS: Mac OS X El-Capitan Browsers: Firefox,Chrome, Safari Bandwidth : 100Mbps Hardware Configuration:8 GBRAM , Processor:i5				
VLSI Lab	2	Usability	Experiment should display the requirements needed to be fulfilled to view it.	4	Negative	To view the Experiment of the VLSI Lab Experiment	1.Click on the "start the experiment" link below "2. Schematic Design Of Transistor Level NAND & NOR Gate." on "List of Experiments" page of VLSI Lab.	Experiment should be visible	Failed	Rishabh	Refer to the test step id 1	OS: Mac OS X El-Capitan Browsers: Firefox,Chrome, Safari Bandwidth : 100Mbps Hardware Configuration:8 GBRAM , Processor:i5				
VLSI Lab	2	Usability	User should be able to view the Objective of VLSI Lab Experiment 2.	5	Positive	To view the Objective page of the VLSI Lab Experiment 2	1.Click on the "Objective" tab of the page.	1.Objective page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to view the Manual page on clicking the "Manual" link on Virtual Experiment page.	6	Positive	To view the Experiment page	1.Click on the "Manual " Tab. 2. Click on the "Click Here For Experiment Manual" link in "Manual" Tab	1.User should be re-directed to the Manual page and Video should be played showing the manual. 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 2	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to view the Experiment page on clicking the "Experiment" link on Virtual Experiment page.	7	Positive	To view the Experiment page	1.Click on the "Virtual Experiment " Tab. 2. Click on the "Experiment" link in "Virtual Experiment " Tab	1.User should be re-directed to the Experiment page and UI should be displayed to perform test. 2.Content should be present with no distortions and Junk characters	Failed	Rishabh	Refer to the test step id 2	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3			1.In Firefox browser a message is displayed saying "Java Appelt option is off in your Brouser :(". 2.No such message is displayed in Chrome browser and the page is empty.	

VLSI Lab	2	Usability	User should be able to view the Procedure of VLSI Lab Experiment 2.	8	Positive	To view the Procedure page of the VLSI Lab Experiment 2	1.Click on the "Proceure" tab of the page.	1.Procedure page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to view the Theory of VLSI Lab Experiment 2.	9	Positive	To view the Theory page of the VLSI Lab Experiment 2	1.Click on the "Theory" tab of the page.	1.Theory page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to view the Quiz of VLSI Lab Experiment 2.	10	Positive	To view the Quiz page of the VLSI Lab Experiment 2	1.Click on the "Quiz" tab of the page.	1.Quiz page should be opened 2.Content should be present with no distortions and Junk characters	Failed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3			1. Some of the pictures are not displayed on the page in Firefox as well as Chrome.	
VLSI Lab	2	Usability	User should be able to view the References of VLSI Lab Experiment 2.	11	Positive	To view the References page of the VLSI Lab Experiment 2	1.Click on the "References" tab of the page.	1.References page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to view the Feedback of VLSI Lab Experiment 2.	12	Positive	To view the Feedback page of the VLSI Lab Experiment 2	1.Click on the "Feedback" tab of the page.	1.Feedback page should be opened 2.Content should be present with no distortions and Junk characters	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Usability	User should be able to submit the Feedback form of VLSI Lab Experiment 2.	13	Positive	To submit the Feedback form of the VLSI Lab Experiment 2	1.Fill the "Feedback" form and click on submit button.	1.Feedback form should submitted successfully. 2.Successfull submission message should be displayed.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should be able to select any component from the availabl icons on experiment page form of VLSI Lab Experiment 2.	14	Positive	To submit the Feedback form of the VLSI Lab Experiment 2	1.Fill the "Feedback" form and click on submit button.	1.Feedback form should submitted successfully. 2.Successfull submission message should be displayed.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should be able to rotate any component from the available icons on experiment page form of VLSI Lab Experiment 2.	15	Positive	To rotate the available circuit components from experiment page of the VLSI Lab Experiment 2	1.select a component. 2.Click on the component and select "Rotate" in the dialogue box to rotate it.	1.The component should be rotated successfully.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should be able to relocate any component from the available icons on experiment page form of VLSI Lab Experiment 2.	16	Positive	To relocate the available circuit components from experiment page of the VLSI Lab Experiment 2	1.select a component. 2.Click on the component and drag it to reocate it.	1.The component should be rlocated successfully.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should not be able to relocate a component on any other comonent on experiment page form of VLSI Lab Experiment 2.	17	negetive	Any component could not be put over another component	1.select a component. 2.Click on the component and drag it to put it over another component.	1.The component should not be relocated.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				

VLSI Lab	2	Functionality	User should be able to join the components with wire on experiment page form of VLSI Lab Experiment 2.	18	Positive	To join the available circuit components using wire from experiment page of the VLSI Lab Experiment 2	1.Select wire component. 2.Click on the red squares of component to join that end point to other component's end point.	1.The components should be connected successfully.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should not be able to end wire on without connecting a component's red square on its end point experiment page form of VLSI Lab Experiment 2.	19	negetive	To end the wire from experiment page of the VLSI Lab Experiment 2	1.Select wire component. 2.Click on the red squares of a component to join wire. 3.Click on the work area to end the wire.	1.User should not be able to end the wire. 2.The wire should be bent from that place on which user clicked.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				
VLSI Lab	2	Functionality	User should able to simulate working of NAND gate on experiment page form of VLSI Lab Experiment 2.	20	positive	To simulate working of NAND Gate on experiment page of the VLSI Lab Experiment 2	1.Select and connect the components as described on Procedure page to create a NAND circuit. 2.Press "Simulate" button on top. 3.Observe the graph created.	1.User should able to see the NAND gate simulation in graph.	Failed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3			1. "The circuit is not complete, Please Complete it and try again" message is displayed.	
VLSI Lab	2	Functionality	User should able to simulate working of NOR gate on experiment page form of VLSI Lab Experiment 2.	21	positive	To simulate working of NOR Gate on experiment page of the VLSI Lab Experiment 2	1.Select and connect the components as described on Procedure page to create a NOR circuit. 2.Press "Simulate" button on top. 3.Observe the graph created.	1.User should able to see the NOR gate simulation in graph.	Failed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3			1. "The circuit is not complete, Please Complete it and try again" message is displayed.	
VLSI Lab	2	Functionality	User should be able to change a component's properties on experiment page form of VLSI Lab Experiment 2.	22	positive	To change component's property on experiment page of the VLSI Lab Experiment 2	1.Select a component of an already created circuit. 2.change the property available on dialogue box. 3.Click on O.K. to save changes.	1.User should be able to see change in the output wave form.	Passed	Rishabh	Refer to the test step id 1	OS: Linux Browsers: Firefox,Chrome Bandwidth : 100Mbps Hardware Configuration:4 GBRAM , Processor:i3				