

(12) **United States Patent**
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(54) **TRACKING INTERMIX OF WRITES AND UN-MAP COMMANDS ACROSS POWER CYCLES**

(58) **Field of Classification Search**

USPC 711/103
See application file for complete search history.

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G06F 12/00 (2006.01)
G06F 3/06 (2006.01)

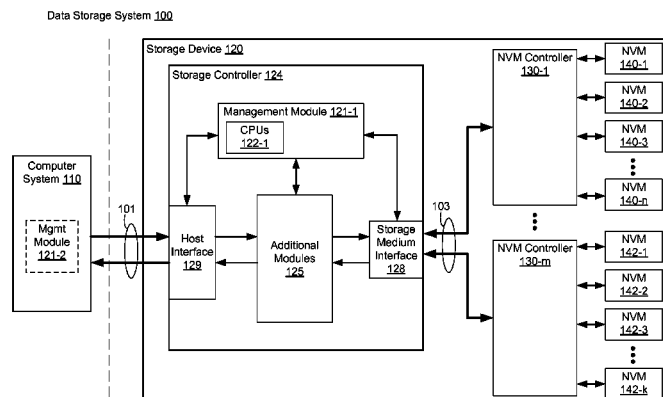
(Continued)

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(57) **ABSTRACT**

Systems, methods and/or devices are used to enable tracking intermix of writes and un-map commands across power cycles. In one aspect, the method includes (1) receiving, at a storage device, a plurality of commands from a host, the storage device including non-volatile memory, (2) maintaining a log corresponding to write commands and un-map commands from the host, (3) maintaining a mapping table in volatile memory, the mapping table used to translate logical addresses to physical addresses, (4) saving the mapping table, on a scheduled basis that is independent of the un-map commands, to the non-volatile memory of the storage device, (5) saving the log to the non-volatile memory, and (6) upon power up of the storage device, rebuilding the mapping table from the saved mapping table in the non-

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volatile memory of the storage device and from the saved log in the non-volatile memory of the storage device.

23 Claims, 6 Drawing Sheets

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G06F 11/14 (2006.01)
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2212/7201 (2013.01); **G06F 2212/7205**
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2212/7208 (2013.01)

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