ROLL NO: 71 MPL LAB

### **EXPERIMENT 10**

#### AIM:

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Check whether a given string is a palindrome or not.

**LO No :-** LO4

LO: Display assembly level programming using 8086 loop instruction set.

**SOFTWARE: -** Tasm Software

## Theory:-

Assume CS: code, DS: data

- ASSUME statement can assign up to 4 segment registers in any sequences.
- DS: Data means that the assembler is to associate the name of data segment with DS register.

Similarly CS: Code tells the assembler to associate the name of code segment with CS register and so on.

#### MOV Ax,data MOV Ds,Ax :-

Initialize Ds to point start of memory, set alongside to store data.

### <u>LEA :-</u>

Used to load the address of operand into the provided register. The LEA instruction is used to load a pointer into a register. It is actually an arithmetic instruction, and does not read RAM at all.

## <u>SHR :-</u>

The SHR instruction is an abbreviation for 'Shift Right'. This instruction simply shifts the mentioned bits in the register to the right side one by one by inserting the same number (bits that are being shifted) of zeroes from the left end. The rightmost bit that is being shifted is stored in the Carry Flag (CF).

Syntax: SHR Register, Bits to be shifted

Example: SHR AX, 2

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#### **PRINT**

Used to print the data or string present in object.

#### JMP:-

In the x86 assembly language, the JMP instruction performs an unconditional jump. Such an instruction transfers the flow of execution by changing the program counter. i.e to jump to the provided address to proceed to the next instruction.

#### JZ :-

jz is "jump if zero". cmp subtracts its two operands, and sets flags accordingly. (See here for reference.)

If the two operands are equal, the subtraction will result in zero and the ZF flag will be set.

#### JNZ:-

The jnz (or jne) instruction is a conditional jump that follows a test. It jumps to the specified location if the Zero Flag (ZF) is cleared (0). jnz is commonly used to explicitly test for something not being equal to zero whereas jne is commonly found after a cmp instruction.

#### <u>INC :-</u>

Adds 1 to the destination operand, while preserving the state of the CF flag. The destination operand can be a register or a memory location. This instruction allows a loop counter to be updated without disturbing the CF flag.

### DEC:-

Used to decrement the provided byte/word by 1. NPG – Used to negate each bit of the provided byte/word and add 1/2's complement. CMP – Used to compare 2 provided byte/word. AAS – Used to adjust ASCII codes after subtraction.

#### INT 21H:-

int 21h means, call the interrupt handler 0x21 which is the DOS Function dispatcher. the "mov ah,01h" is setting AH with 0x01, which is the Keyboard Input with Echo handler in the interrupt.

Syntax: int 21H Example: int 21H

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### Code:-

```
Assume CS: Code, DS: Data Data Segment m1
db0AH, 0DH, 'ENTER THE STRING: $' m2 db
0DH, 'STRING IS PALINDROME$' m3 db 0AH,
0DH, 'STRING IS NOT A PALINDROME$' Buff db
             db 80H dup(0) Data
      db 00H
EndsPRINT MACRO MSG
MOV AH,
09HLEA DX,
MSG INT
21H ENDM
Code Segment
Start: MOV AX,
Data
   MOV DS,
   AX PRINT
   M1 MOV
   AH, 0AH
   LEA DX,
   BUFF INT
   21H
   LEA BX,
   BUFF+2 MOV
   CH, 00H MOV
   CL, BUFF+1
   MOV DI, CX
   DEC DI
   MOV SI,
00HSHR CL,
01H
   Back: MOV AL,
   [BX+SI]MOV AH,
   [BX+DI] CMP AL,
   AH
   JNZ
   Last
   DEC DI
   INC SI
   DEC
   CX
   JNZ Back
   PRINT
   M2
```

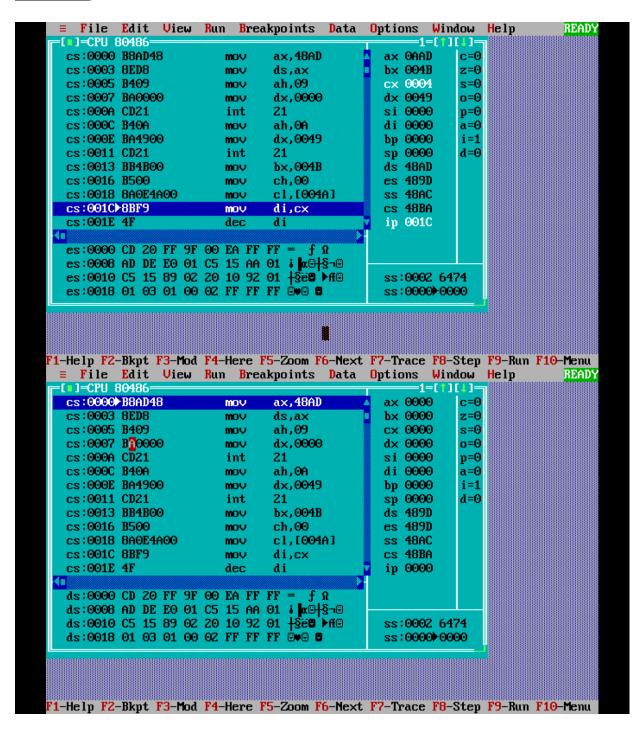
JMP Final

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Last: PRINT M3	
Final: MOV AH,	
4CHINT 21H	

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Code Ends End Start

# **Output:-**



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C:\TASM>td soham
Turbo Debugger Version 3.1 Copyright (c) 1988,92 Borland International

ENTER THE STRING: dad
STRING IS PALINDROME
ENTER THE STRING: soham
STRING IS NOT A PALINDROME

# **Conclusion:**

Thus, we learnt how to check whether a string is palindrome or not using assembly language.