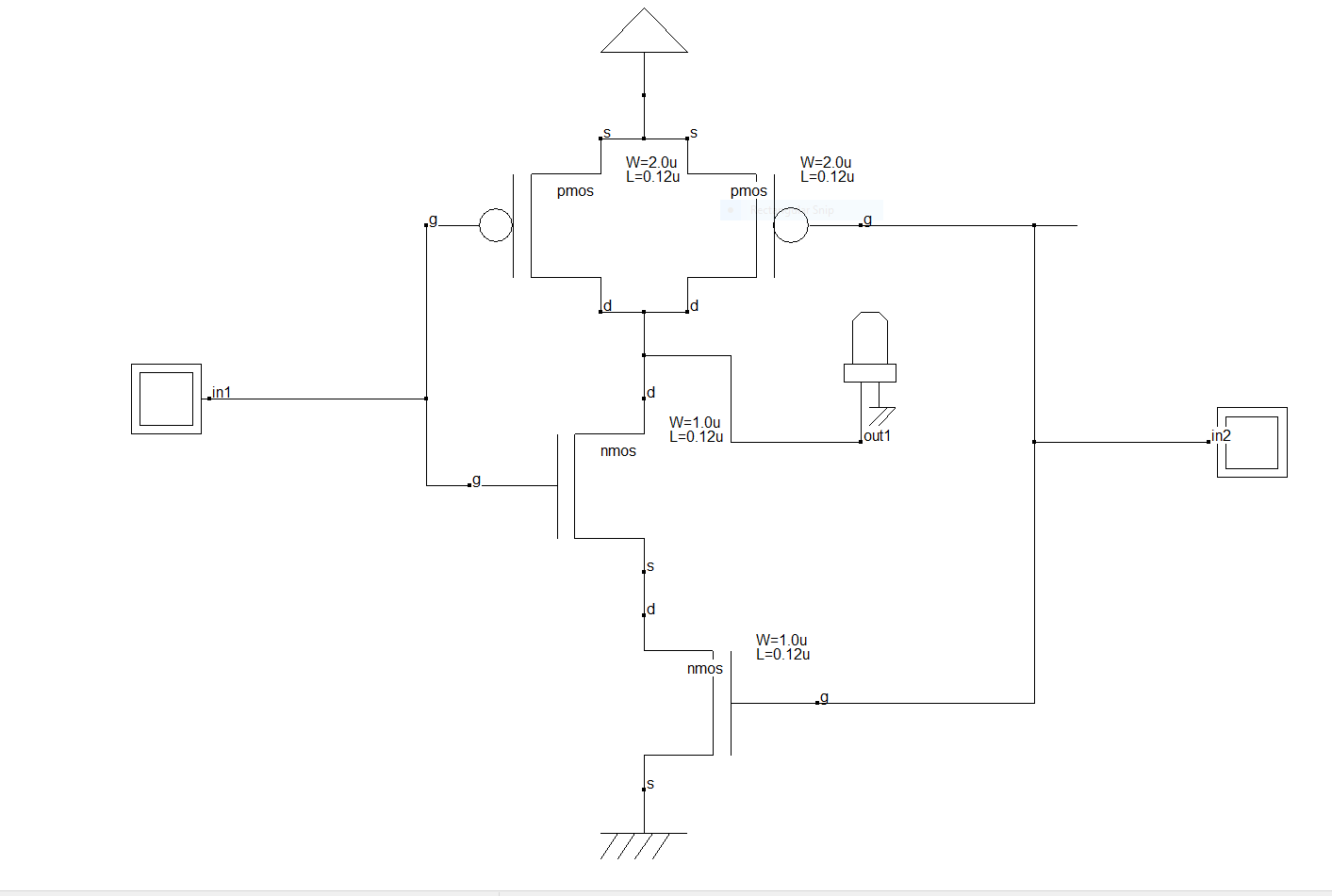
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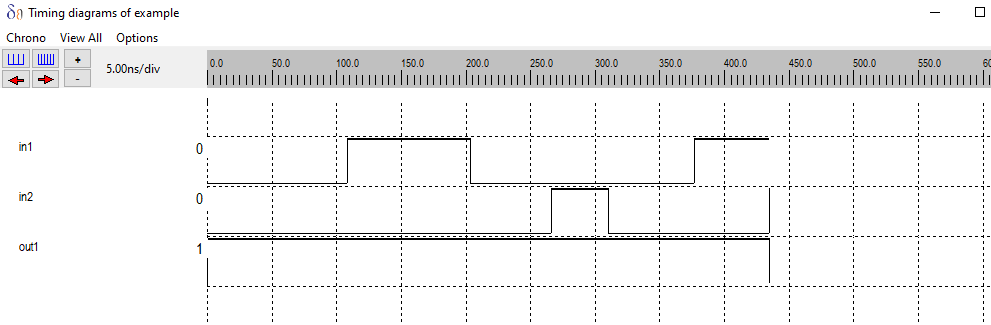
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TE {classNo}

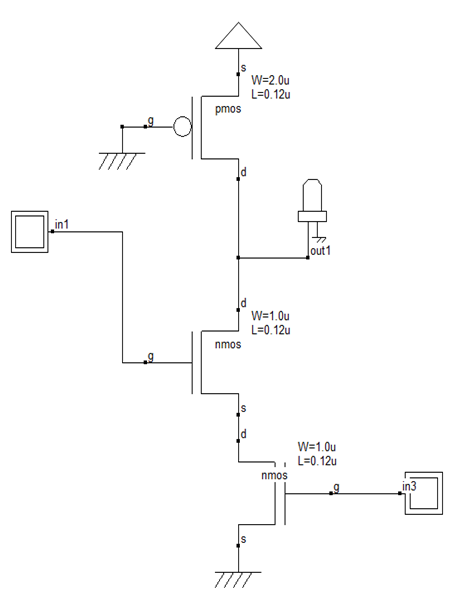
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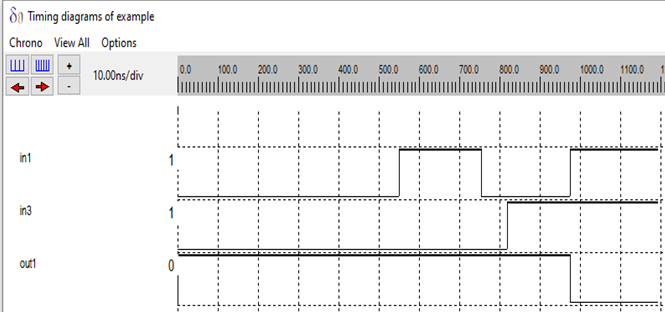
**NAND USING STATIC CMOS**



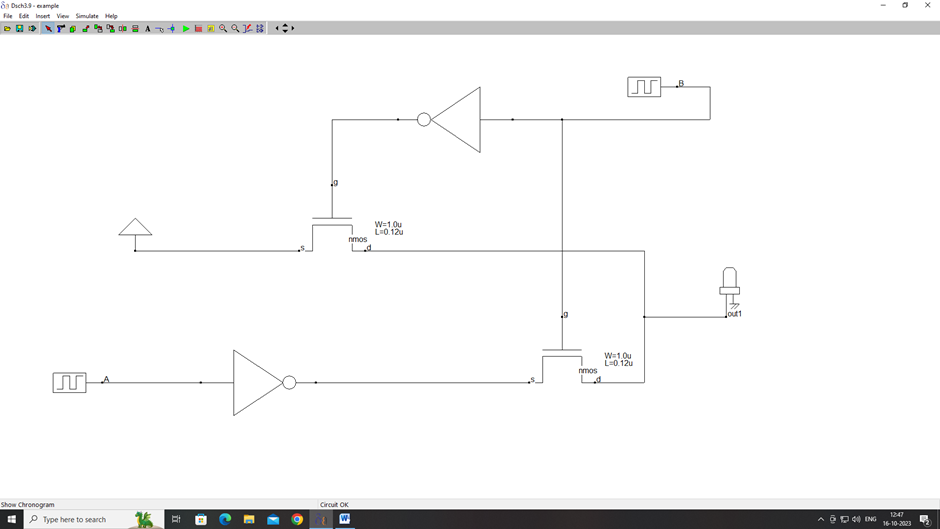


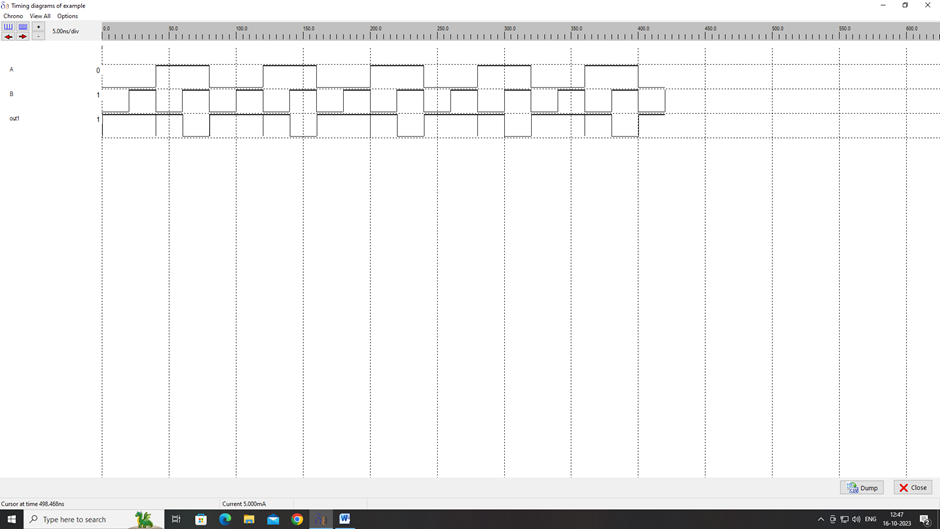
**NAND USING PSEUDO**

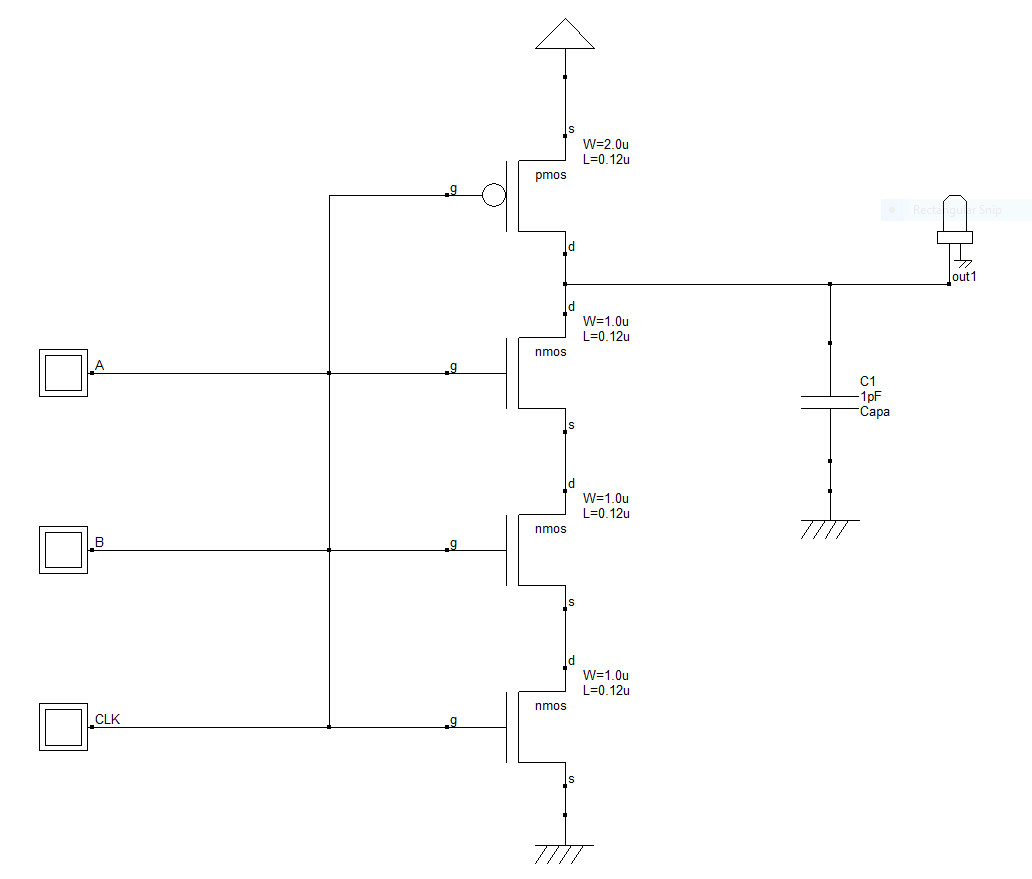
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**NAND USING PASS TRANSISTOR LOGIC**

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**NAND USING DYNAMAIC LOGIC**

