sync\_ram:1 fde **BUFT** and2b1 read Q 0 Dout(31:0) D writeEn CE writeEn\_read\_AND\_2\_o1 С Mram\_mem1 and2b2 doA(31:0) addr(7:0) addrA(7:0) diA(31:0) Din(31:0) read\_writeEn\_AND\_1\_o1 clk clkA weA Mram\_mem1

sync\_ram