

CSCE 3304 - Digital Design 2
Project 1
A small standard cell library

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6.1	Schematic

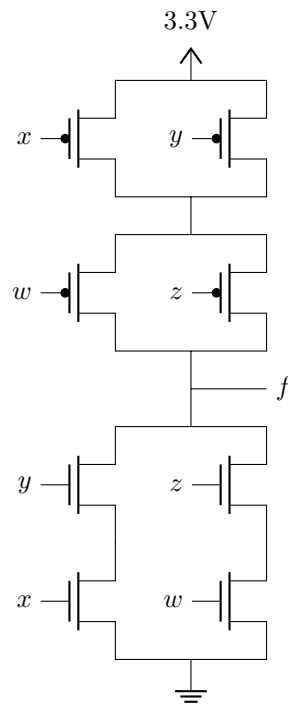


Figure 1:

6.2 Stick diagram

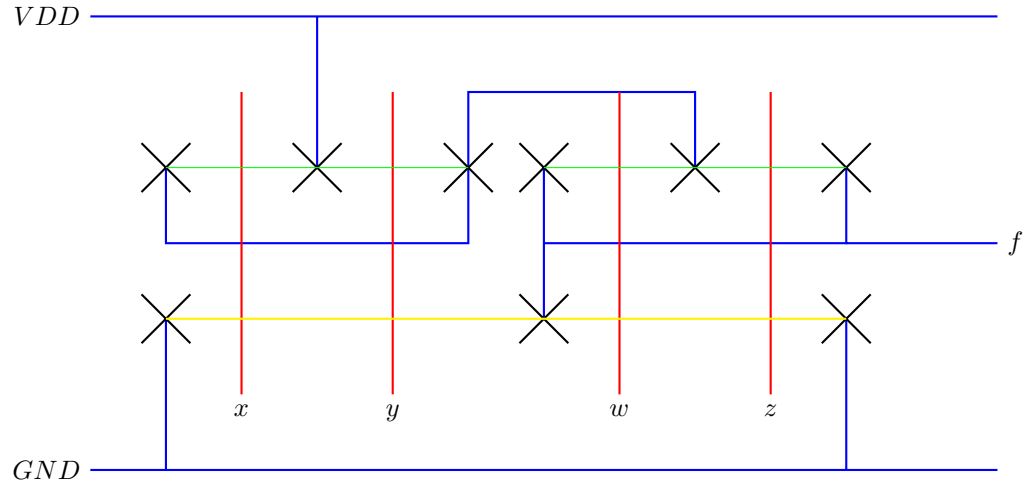


Figure 2:

6.3 Layout

6.4 Transition time

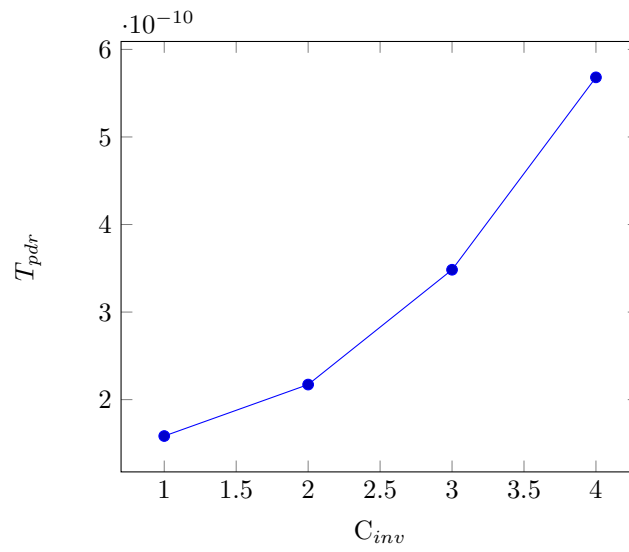


Figure 3: T_{pdr} to C_{inv} under $100ns$ transition time

6.5 Data collection

6.6 Minimum cell height