Advent of Code [About] [Events] [Shop] [Settings] [Log Out] Jarek 18* 0xffff&2022 [Calendar] [AoC++] [Sponsors] [Leaderboard] [Stats]

--- Day 10: Cathode-Ray Tube ---

You avoid the ropes, plunge into the river, and swim to shore.

The Elves yell something about meeting back up with them upriver, but the river is too loud to tell exactly what they're saying. They finish crossing the bridge and disappear from view.

Situations like this must be why the Elves prioritized getting the communication system on your handheld device working. You pull it out of your pack, but the amount of water slowly draining from a big crack in its screen tells you it probably won't be of much immediate use.

Unless, that is, you can design a replacement for the device's video system! It seems to be some kind of cathode-ray tube screen and simple CPU that are both driven by a precise clock circuit. The clock circuit ticks at a constant rate; each tick is called a cycle.

Start by figuring out the signal being sent by the CPU. The CPU has a single register, X, which starts with the value I. It supports only two instructions:

- $\boxed{\text{addx V}}$ takes two cycles to complete. After two cycles, the \boxed{X} register is increased by the value \boxed{V} . \boxed{V} can be negative.)
- noop takes one cycle to complete. It has no other effect.

The CPU uses these instructions in a program (your puzzle input) to, somehow, tell the screen what to draw.

Consider the following small program:

noop addx 3 addx -5

Execution of this program proceeds as follows:

- At the start of the first cycle, the \fbox{noop} instruction begins execution. During the first cycle, $\fbox{3}$ is $\fbox{1}$. After the first cycle, the \fbox{noop} instruction finishes execution, doing nothing.
- At the start of the second cycle, the addx 3 instruction begins execution. During the second cycle, X is still 1.
- During the third cycle, X is still 1. After the third cycle, the addx 3 instruction finishes execution, setting X to 4.
- At the start of the fourth cycle, the $\boxed{\text{addx } -5}$ instruction begins execution. During the fourth cycle, \boxed{X} is still $\boxed{4}$.
- During the fifth cycle, \mathbb{X} is still \mathbb{A} . After the fifth cycle, the addx -5 instruction finishes execution, setting \mathbb{X} to -1.

Maybe you can learn something by looking at the value of the \boxtimes register throughout execution. For now, consider the signal strength (the cycle number multiplied by the value of the \boxtimes register) during the 20th cycle and every 40 cycles after that (that is, during the 20th, 60th, 100th, 140th, 180th, and 220th cycles).

For example, consider this larger program:

Our sponsors help make Advent of Code possible:

Deepgram - AI transcription & understanding in a single API. Rust+Elm

addx 15 addx -11 addx 6 addx -3 addx 5 addx -1 addx -8 addx 13 addx 4 noop addx -1 addx 5 addx -1 addx 5 addx -1 addx 5 addx -1 addx 5 addx -1 addx -35 addx 1 addx 24 addx -19 addx 1 addx 16 addx -11 noop noop addx 21 addx -15 noop noop addx -3 addx 9 addx 1 addx -3 addx 8 addx 1 addx 5 noop noop noop noop noop addx -36 noop addx 1 addx 7 noop noop noop addx 2 addx 6 noop noop noop noop noop addx 1 noop noop addx 7 addx 1 noop

```
addxints resting signal strengths can be determined as follows:
addx 13
addxD#ring the 20th cycle, register 🛛 has the value 🔼, so the signal
\frac{1}{1000} strength is 20 * 21 = 420. (The 20th cycle occurs in the middle of the
addx second addx -1, so the value of register X is the starting value, 1,
addxplws all of the other addx values up to that point: 1 + 15 - 11 + 6 -
noop3 + 5 - 1 - 8 + 13 + 4 = 21.
noopDuring the 60th cycle, register 🛭 has the value [19], so the signal
noopstrength is 60 * 19 = 1140
addxDyring the 100th cycle, register 🛛 has the value 🔞, so the signal
_{\text{noop}} strength is 100 * 18 = \boxed{1800}
noopDuring the 140th cycle, register 🛛 has the value 🔟, so the signal
noopstrength is 140 * 21 = 2940
addxDgring the 180th cycle, register 🛛 has the value 🔟, so the signal
noopstrength is 180 * 16 = 2880
addxDuring the 220th cycle, register 🛛 has the value 🔃, so the signal
addx strength is 220 * 18 = 3960
addx 1
The sum of these signal strengths is 13140
noop
P역시청 the signal strength during the 20th, 60th, 100th, 140th, 180th, and
290th cycles. What is the sum of these six signal strengths?
addx 1
ădd¤ pBzzle answer was 14220
addx 11
ቪክթրfirst half of this puzzle is complete! It provides one gold star: *
noop
add×<sub>P</sub>art Two ---
noop
|⊉∉dšeems| like the 🏿 register controls the horizontal position of a sprite.
|\mathfrak{S}|QQifically, the sprite is 3 pixels wide, and the |\mathfrak{A}| register sets the
NOPPzontal position of the middle of that sprite. (In this system, there is
ብፅ<sup>d</sup>šuāÅ<sup>3</sup>thing as "vertical position": if the sprite's horizontal position
Bstts itst pixels where the CRT is currently drawing, then those pixels will
Bedarawn.)
addx 3
≱ddxc∂6nt the pixels on the CRT: 40 wide and 6 high. This CRT screen draws
addextoβ0row of pixels left-to-right, then the row below that, and so on.
adexle≇t-most pixel in each row is in position 回, and the right-most pixel
addeach row is in position 39.
addx 3
bable the CPU, the CRT is tied closely to the clock circuit: the CRT draws a
คิอัตติโอ plixel during each cycle. Representing each pixel of the screen as a
நெ<sub>ற</sub>்ere are the cycles during which the first and last pixel in each row
A68pdrawh:
addx -9
Gygle<sub>18</sub> 1 <del>-> ################################# <- Cycle</del>
Sysle<sub>1</sub> 41 -> ############################### <- Cycle 80
Sygle<sub>2</sub> 81 -> ############################### <- Cycle 120
Nysle 121 -> ################################# <- Cycle 160
ΑΧΕĴe 161 -> ############################## <- Cycle 200
by carefully timing the CPU instructions and the CRT drawing
 peratiohs, you should be able to determine whether the sprite is visible
bop instant each pixel is drawn. If the sprite is positioned such that one
addx 2 pixel (#); otherwise, the screen leaves the pixel dark (.).
₱₦₡×fᠯrst few pixels from the larger example above are drawn as follows:
addx 3
noop
addx 15
addx -21
addx 22
```

```
§8្សុវte<sub>6</sub>position: ###......
addx 1
Abapt cycle 1: begin executing addx 15
Byging cycle 1: CRT draws pixel in position 0
ବୃଧ୍ୟର୍ବୁ CRT row: #
noop
<code>Buging10</code>ycle 2: CRT draws pixel in position 1
คิยุธธent CRT row: ##
ลิตต่อด cycle 2: finish executing addx 15 (Register X is now 16)
Sggit@oposition: ......###....###....
addx 1
Start2cycle 3: begin executing addx -11
ดิษศุริกฐ cycle 3: CRT draws pixel in position 2
Sydrent CRT row: ##.
addx -11
คุมกลักฐ cycle 4: CRT draws pixel in position 3
გყგნent CRT row: ##..
ลือต่อof cycle 4: finish executing addx -11 (Register X is now 5)
Sprite position: ....###..........................
Start cycle 5: begin executing addx 6
During cycle 5: CRT draws pixel in position 4
Current CRT row: ##..#
During cycle 6: CRT draws pixel in position 5
Current CRT row: ##..##
End of cycle 6: finish executing addx 6 (Register X is now 11)
Sprite position: ......###.......
Start cycle 7: begin executing addx -3
During cycle 7: CRT draws pixel in position 6
Current CRT row: ##..##.
During cycle 8: CRT draws pixel in position 7
Current CRT row: ##..##..
End of cycle 8: finish executing addx -3 (Register X is now 8)
Sprite position: .....###.....
Start cycle 9: begin executing addx 5
During cycle 9: CRT draws pixel in position 8
Current CRT row: ##..##..#
During cycle 10: CRT draws pixel in position 9
Current CRT row: ##..##..##
End of cycle 10: finish executing addx 5 (Register X is now 13)
Sprite position: ......###......
Start cycle 11: begin executing addx -1
During cycle 11: CRT draws pixel in position 10
Current CRT row: ##..##..##.
During cycle 12: CRT draws pixel in position 11
Current CRT row: ##..##..##..
End of cycle 12: finish executing addx -1 (Register X is now 12)
Sprite position: ......###......
Start cycle 13: begin executing addx -8
During cycle 13: CRT draws pixel in position 12
Current CRT row: ##..##..#
During cycle 14: CRT draws pixel in position 13
Current CRT row: ##..##..##
End of cycle 14: finish executing addx -8 (Register X is now 4)
Sprite position: ...###......
```

```
Allowing the program to run to completion causes the CRT to produce the
Soakowapglėmago: begin executing addx 13
During cycle 15: CRT draws pixel in position 14
##:###:###:###:###:###:###:###:##
###...###...###...###...###...###...###.
| 芭芭芹芹ng·c芍芒芫荽·16:#芒芹芹·dra椛芸#艿ixel#芹芹position 15
世帯樹#甚幸・cvcle####inish・善裝善告棋#ing・ad樹奘##B (Register X is now 17)
Render the image given by your program, What eight capital letters appear
Start cycle, 17: begin executing addx 4
During cycle 17: CRT draws pixel in position 16
Current CRT row: ##..##..##..#
                          [Submit]
During cycle 18: CRT draws pixel in position 17
AlthonghCRT hawn'##ch##ge##.y##.c## still get your puzzle input.
End of cycle 18: finish executing addx 4 (Register X is now 21)
∖gui€anpassei68hare].this.puzzle....###....
Start cycle 19: begin executing noop
During cycle 19: CRT draws pixel in position 18
Current CRT row: ##..##..##..##..##.
End of cycle 19: finish executing noop
Start cycle 20: begin executing addx -1
During cycle 20: CRT draws pixel in position 19
Current CRT row: ##..##..##..##..
During cycle 21: CRT draws pixel in position 20
Current CRT row: ##..##..##..##..#
End of cycle 21: finish executing addx -1 (Register X is now 20)
Sprite position: ............###.....###.....
```