



SPECIFICATIONS

CUSTOMER . PTC

SAMPLE CODE . SH480272T005-IAB11

MASS PRODUCTION CODE . PH480272T005-IAB11

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 002

DRAWING NO. (Ver.) . JLMD-PH480272T005-IAB11_001

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Customer Approved

Checked

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Date:

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- ☐ Preliminary specification for design input
- Specification for sample approval

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Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): Sitronix--ST7257



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	480 * 3 (RGB) * 272 Dots
LCD Type	a-Si TFT , Normally white, Transmissive type
Screen size(inch)	4.3 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Interface	Digital 24-bits RGB
Other(controller/driver IC)	ST7257
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 5.0(H)MAX	mm
Viewing Area	96.64(W) x 55.456 (L)	mm
Active Area	95.04 (W) * 53.856 (L)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDDIO	GND=0	-0.3	4.6	V
Operating Temperature	Top	-	-20	70	°C
Storage Temperature	Tst	-	-30	80	°C

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
	VDDIO	-	3.0	3.3	3.6	V
Power Supply Voltage	VGH	-	12	15	16	V
	VGL	-	-12	-10	-7	V
Input H/L Level Voltage	VIH	-	0.7 VDDIO	-	VDDIO	V
Input 17/L Level Voltage	VIL		0	ı	0.3 VDDIO	V
Output H/L Level	VOH		VDDIO-0.4	ı	VDDIO	V
Voltage	VOL	-	0	ı	GND+0.4	V
Supply Current	I _{DD}	VDDIO = 3.3 V	-	25	40	mA



1.5 Optical Characteristics

TFT LCD Module

VDDIO= 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response time	Tr+Tf	25 ℃	-	-	26	39	ms	-
	Тор	θΥ+		-	60	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10	-	60	-	Dan	Note 4
Viewing angle	Left	θX-	CR 2 10	-	60	1	Deg.	Note 4
	Right	θX+		-	60	1		
Contrast ration	0	CR		500	600	-	-	Note 3
	White	X	X	0.27	0.32	0.37		
	Y		0.31	0.36	0.41			
Color of CIE	Red	X	IF= 20mA	0.55	0.60	0.65		
Color of CIE Coordinate	Reu	Y		0.31	0.36	0.41		Note1
(B/L & LCD & TP)	Green	Х		0.29	0.34	0.39	-	Note
	Gleen	Y		0.55	0.60	0.65		
	Blue	Х		0.10	0.15	0.20		
	blue	Υ		0.05	0.10	0.15		
Average Brightr	ness							
Pattern=white dis	splay	IV	IF= 20mA	250	300	-	cd/m2	Note1
(B/L & LCD & TP)*1								
Uniformity (B/L & LCD & T	P)*2	∆В		70	-		%	Note1



Note 1:

*1 : △B=B(min) / B(max) * 100%

*2 : Measurement Condition for Optical Characteristics:

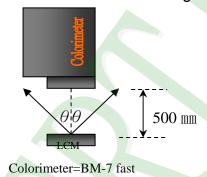
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^\circ)$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





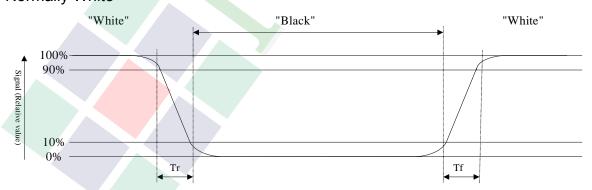
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

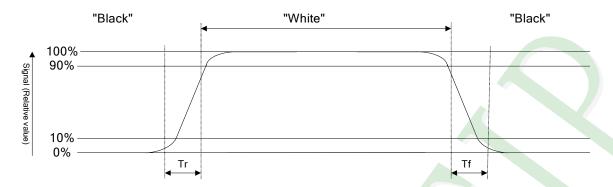
Refer to figure as below:

Normally White





Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

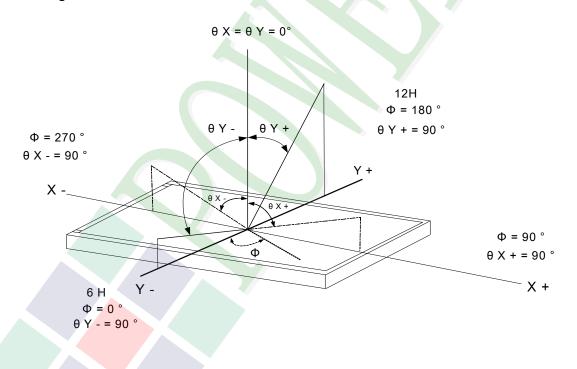
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°ℂ	-	30	mA
LED Reverse Voltage (Each LED)	VR	Ta =25°C		5.0	V
Power Dissipation	PD	Ta =25°℃	-	490	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		19.6	22.8	24.5	٧
Average Brightness	IV	IF=20mA	5600	7000	-	cd/m ²
015 0 - 1 - 2 1 1 -	Х	IF-ZUIIIA	0.26	0.30	0.34	
CIE Color Coordinate	Y		0.26	0.30	0.34	-
Color			White			

Circuit diagram:



Other Description

Item	Conditions	Description
Life Time	Ta =25°C IF= 20mA	20,000 hrs



1.7 Touch Panel Characteristics

1.7.1 Optical Characteristics

Item	Specification
1.Transparency	80% Min

1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.FPC peeling strength	500gf min(Peeling upward by 90°)
4.Activation Force	50gf(Typical 20gf) less individual point with stylus pen(R0.8mm)
	Activation force guarantee area:5.0mm inside of Active Area.
5.Linearity Force	100gf less input with stylus pen(R0.8mm)
	Linearity force guarantee area:3.0mm inside of Active Area.

1.7.3 Electrical Characteristics

Item	Specification
1.Rated Voltage	DC 5V(DC 7V Max)
2.Resistance Between	Direction X (Glass side): 350Ω~ 1240Ω
Terminals.	Direction Y (Film side): 160Ω~ 640Ω
3.Insulation Resistance	20 MΩ or more (DC 25 V 1min)
4.Linearity	 ≤1.5%. Linearity(%)= ΔV/ (EV-SV) *100. ΔV: The difference between the ideal voltage and measured voltage on the each measuring line. SV: Voltage of starting Points. EV: Voltage of Ending Points.
5.Bouncing	<10ms (Tip R 3.75mm, hardness 10°~20°, silicon rubber, 500gf operation : 40 mm/sec)



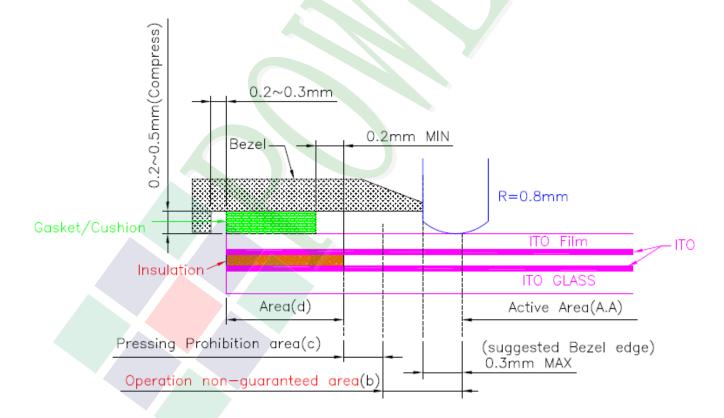
1.7.4 Reliability Characteristic

Item	Specification
1.Hitting Durability	1,000,000times min.(Tip R 8mm&R0.8mm)
2.Pen Sliding Durability	100,000 times min(Tip R0.8mm).
3.Impact Resistance	No damage when $\psi 9 \text{mm}$ steel ball is dropped on the surface from 30 cm height at 1 time.
4.Flexible pattern Bending	Bending 3 times by bending radius R1.0 mm
Resistance	
5.Flexible Pattern Insert/Pull	Estimon et la cet
Out Resistance	5times at least .



1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge. The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure.



Area(a): Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

Area(b): Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

Area(c): Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d): Non-Active area

The area does not activate even if pressed.



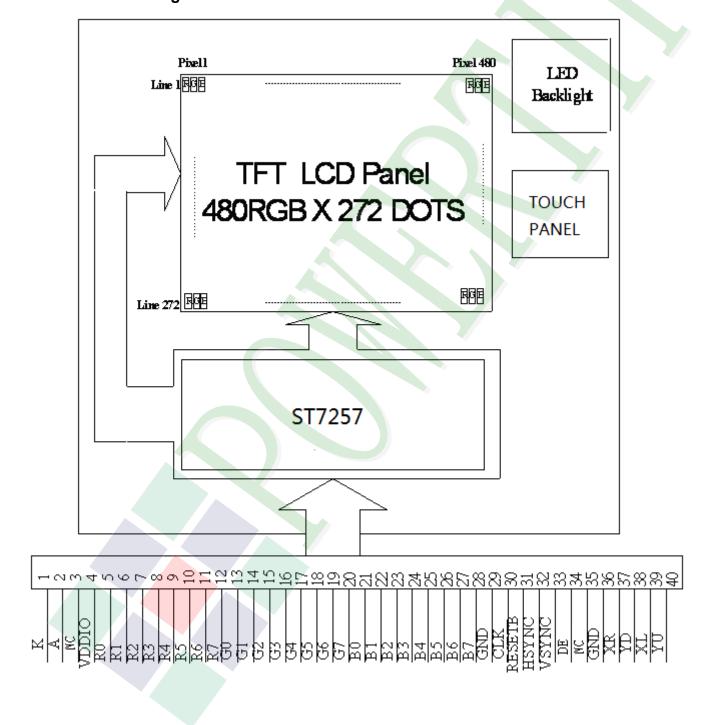
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	NC	Not Connect.
4	VDDIO	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7



Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	В3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	В6	Blue data bit 6
28	В7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	RESETB	Active low global reset signal input.
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input If not used, please leave it floating.
35	NC	Not Connect.
36	GND	Ground
37	XR	Right side of touch panel.
38	YD	Bottom side of touch panel.
39	XL	Left side of touch panel.
40	YU	Up side of touch panel.



2.3 Timing Characteristics

2.3.1 AC Characteristics

AC Electrical Characteristics (VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

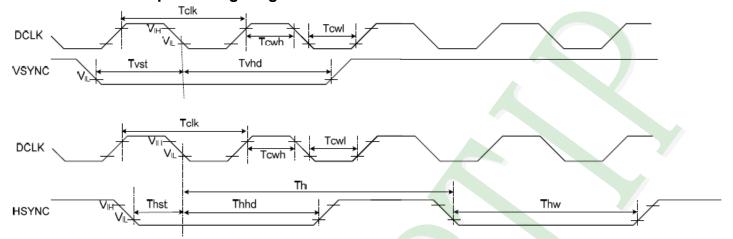
Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
System operation timing						
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
	Inpu	t/ Output	timing			
CLK pulse duty	Tcw	40	50	60	%	
HSYNC period	Th	55	60	65	us	
VSYNC setup time	Tvst	12	-	-	ns	
VSYNC hold time	Tvhd	12	4-	-	ns	
HSYNC setup time	Thst	12	-	-/-	ns	
HSYNC hold time	Thhd	12	-	-	ns	
Data setup time	Tdsu	12	-	- (ns	
Data hold time	Tdhd	12	-	-	ns	
DE setup time	Tdest	12			ns	
DE hold time	Tdehd	12			ns	
SD output stable time	Tst	-	-	12	us	Output settled within +20mV
						Loading = 6.8k+28.2pF.
GD output rise and fall time	Tgst	-	-	6	us	Output settled (5%~95%),
						Loading = 4.7k+29.8pF





2.3.2 AC Timing Diagram

Clock and Data Input Timing Diagram



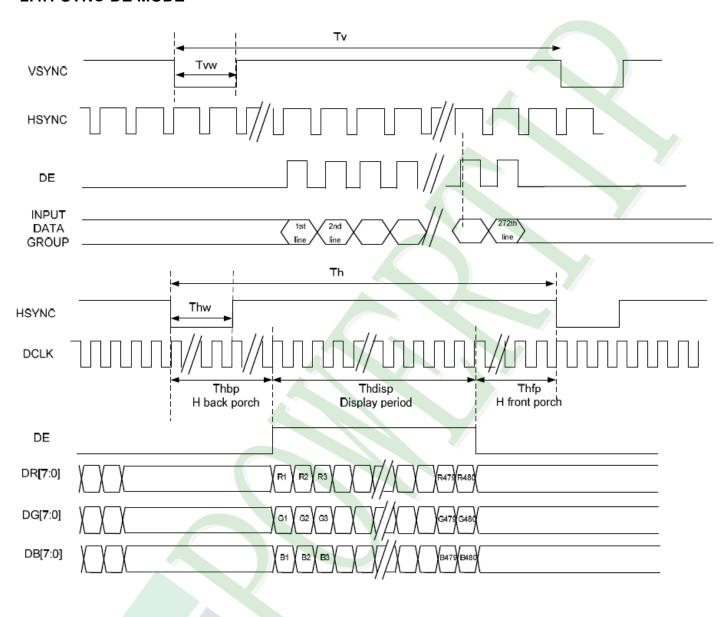






2.4 Data Format

2.4.1 SYNC-DE MODE





2.4.2 Parallel RGB Input Timing Table

	480RGB X 272 Resolution Timing Table							
Item		Symbol	Min.	Тур.	Max.	Unit	Remark	
DCLK Free	quency	Fclk	8	9	12	MHz		
DCLK Peri	od	Tclk	83	111	125	ns		
HSYNC	Period Time	Th	485	531	598	DCLK		
	Display Period	Thdisp		480		DCLK		
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting	
	Front Porch	Thfp	2	œ	75	DCLK		
	Pulse Width	Thw	2	4	75	DCLK		
VSYNC	Period Time	Tv	276	292	321	Н		
	Display Period	Tvdisp		272		Н	4/	
	Back Porch	Tvbp	2	12	12	H	By V_Blanking setting	
	Front Porch	Tvfp	2	8	37	Н		
	Pulse Width	Tvw	2	4	37	Н		

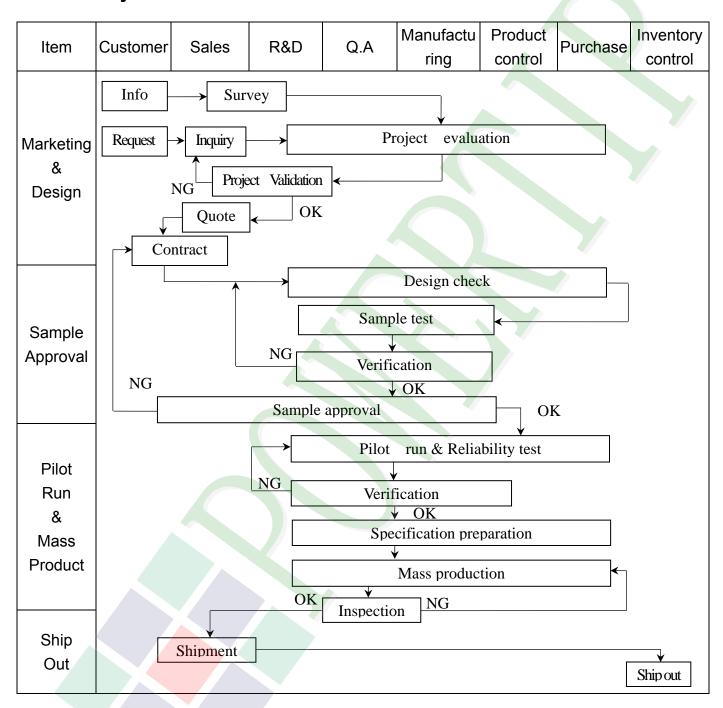
Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.



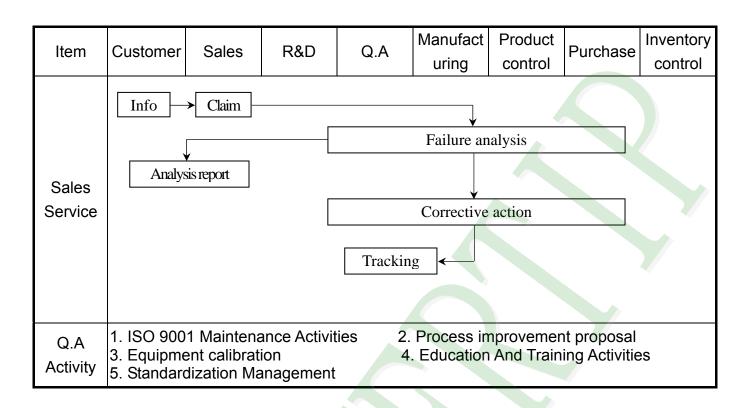


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample

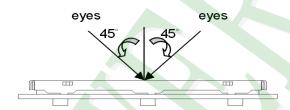
◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

OUT Going Defect Level: Sampling.

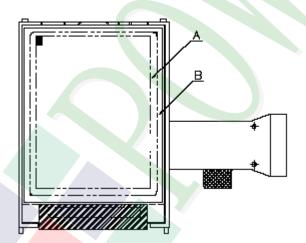
♦Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion				
		1. 1The part number is inconsistent with work order of production.	Major			
01	Product condition	1. 2 Mixed product types.	Major			
		1. 3 Assembled in inverse direction.	Major			
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3. 1 Product dimension and structure must conform to struct diagram.	ture Major			
		4. 1 Missing line character and icon.	Major			
		4. 2 No function or no display.	Major			
	Electrical Testing	4. 3 Display malfunction.				
04		4. 4 LCD viewing angle defect.				
		4. 5 Current consumption exceeds product specifications.				
		4. 6 Mura can not be seen through 5% ND filter. (Mura: Under the normal examination angle of view,the picture has the non-uniform phenomenon.)	Minor			
		Item Acceptance (Q'ty)				
		Bright Dot ≤ 4				
	Dot defect	Dot Dark Dot ≤ 5				
	(D.14.14	Defect Joint Dot ≤ 3				
05	(Bright dot ` Dark dot)	Total ≤ 7	Minor			
	On -display	 5. 1 Inspection pattern: full white, full black, Red, Green blue screens. 5. 2 It is defined as dot defect if defect area >1/2 dot. 5. 3 The distance between two dot defect ≥5 mm. 5. 4 Bright dot that can not be seen through 5% ND filt 	and			



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
110	100H	6. 1 Round type (Non-display or display):	
		Dimension (diameter : Φ)	
	Black or white dot > scratch >	$ \Phi \leq 0.25 \qquad \text{Ignore} \\ 0.25 < \Phi \leq 0.50 \qquad 5 $	
	contamination	$\begin{array}{c cccc} \Phi > 0.50 & 0 & \\ \hline Total & 5 & \\ \end{array}$	
	Round type $\rightarrow X \leftarrow Y$	6. 2 Line type(Non-display or display):	
06	<u>Y</u>		Minor
	$\Phi = (x+y)/2$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	
	Line type	3.5" to less 9" $L \leq 3.0$ $0.03 \leq W \leq 0.10$ $L \leq 3.0$ Ignore $M > 0.10$ $M >$	
	✓ † W	Total 5 W ≤ 0.05 Ignore	
	→ı _L	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	
		9" to 15" W > 0.10 As round type Ignore	
		Total 5	
		Dimension (diameter : Φ) Acceptance (Q'ty)	
		$\Phi \le 0.25 \qquad \qquad \begin{array}{c cccc} A \text{ area} & B \text{ area} \\ \hline & & & & \\ \hline & & & & \\ \hline & & & & \\ \hline & & & &$	
07	Polarizer Bubble	$0.25 < \Phi \leq 0.50 \qquad \qquad 4$	Minor
	Bubble	$0.50 < \Phi \le 0.80$ 1 Ignore	
		$\begin{array}{c cccc} \Phi > 0.80 & 0 \\ \hline Total & 5 \end{array}$	
		10tai S	



◆Specification For TFT-LCD Module 3, 5″ ~15″:

NO	Item	Criterion		Level
		Z: The thickness of crack V	Y : The width of crack. V : terminal length a : LCD side length	
		8.1 General glass chip: 8.1.1 Chip on panel surface and cra	nck between panels:	
		Z Z	Z Y	
08	The crack of glass	SP Y [OK]	[NG]	Minor
		Seal width Z	Y	
		X Y	z	
		≤ a Crack can't enter viewing area	≤1/2 t	
		≤ a Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion			
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length			
		8. 1. 2 Corner crack:			
		X Y Z			
		$\leq 1/5$ a Crack can't enter viewing area $Z \leq 1/2$ t			
		$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2 t			
00					
08	The crack of glass	8.2 Protrusion over terminal:	Minor		
		8.2.1 Chip on electrode pad:			
		Z X Y Z			
		W X			
		X Y Z			
		Front $\leq a$ $\leq 1/2 \mathrm{W}$ $\leq t$			
		Back $\leq a$ $\leq W$ $\leq 1/2 t$			



◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO Item Criterion	Level
Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass 8. 2. 2 Non-conductive portion: X	Minor



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	General	10. 1 Pin type \quantity \dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

4.1	Reliability rest condition (ver.bor)				
NO.	TEST ITEM	TEST CONDITION			
1	High Temperature	Keep in +80 ±2°C 240 hrs			
*	Storage Test	Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature	Keep in −30 ±2°C 240 hrs			
	Storage Test	Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature /	Keep in +60 ℃ / 90% R.H duration for 240 hrs			
	High Humidity Storage Test	Surrounding temperature, then storage at normal condition 4hrs.			
		(Excluding the polarizer)			
4	Temperature Cycling Storage Test	$-30^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C} \rightarrow +80^{\circ}\mathbb{C} \rightarrow +25^{\circ}\mathbb{C}$			
		(30mins) (5mins) (5mins)			
		20 Cycle			
		Surrounding temperature, then storage at normal condition 4hrs.			
5	ESD Test	Air Dischar	ge:	Contact Discharge:	
		Apply 2 KV with 5 times		Apply 250 V with 5 times	
		Discharge for each polarity +/- discharge for each polarity +/-			
		1. Temperature ambiance : 15°C ~35°C			
		2. Humidity relative : 30%~60%			
		3. Energy Storage Capacitance(Cs+Cd): 150pF±10%			
		4. Discharge Resistance(Rd): 330 Ω±10%			
		5. Discharge, mode of operation :			
		Single Discharge (time between successive discharges at least 1 sec)			
		(Tolerance if the output voltage indication: ±5%)			
6	Vibration Test (Packaged)	1. Sine wave 10~55 Hz frequency (1 min/sweep)			
		2. The amplitude of vibration :1.5 mm			
		3. Each direction (X \ Y \ Z) duration for 2 Hrs			
7	Drop Test (Packaged)		Packing Weight (Kg)	Drop Height (cm)	
			0 ~ 45.4	122	
			45.4 ~ 90.8	76	
			90.8 ~ 454	61	
			0ver 454	46	
		Drop Direction: 1 corner / 3 edges / 6 sides each 1 time			



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

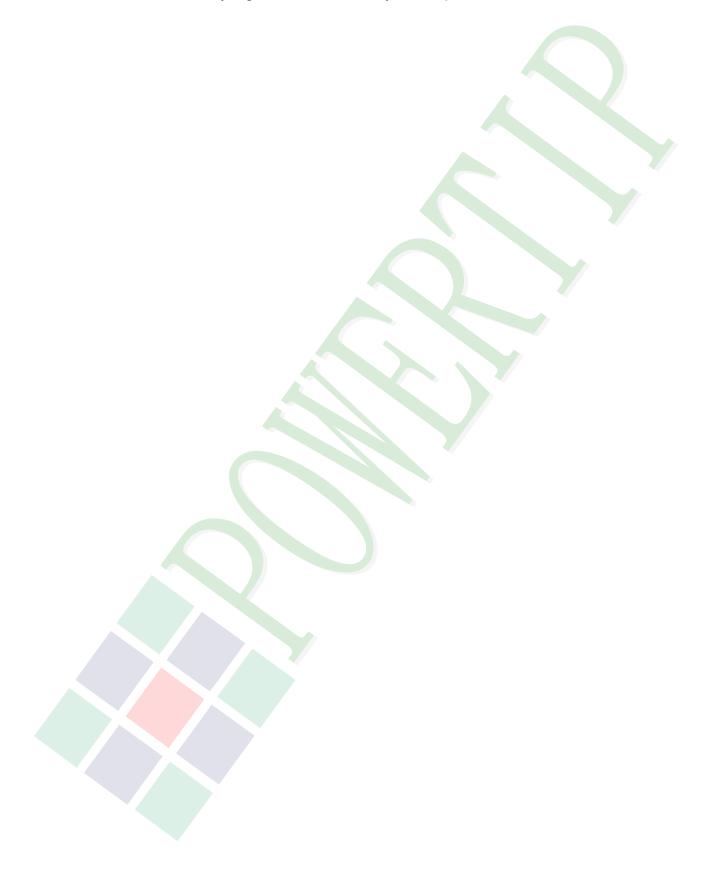
- 5.4.1 Applicable warrant period

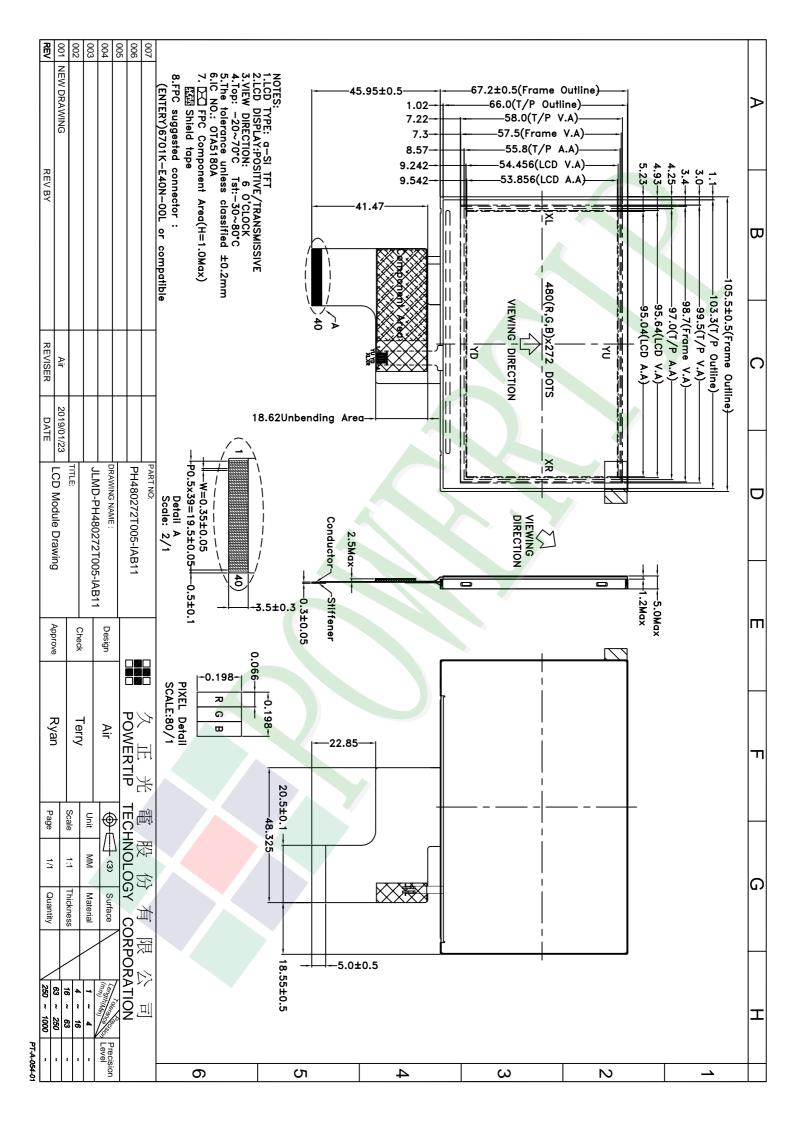
 The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security



systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





Approve Check Contact LCM包裝規格書 Ver.001 LCM Packaging Specifications Ryan Terry Air Documents NO. JPKG-PH480272T005-IAB11 (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) Item 1Pcs Weight Total Weight No. Dimensions (mm) Quantity 成品 (LCM) PH480272T005-IAB11 105.5 X 67.2 X 5.0 144 1 0.064 9.216 2 多層薄膜(1)POF 19"X350X0.015 6 OTFILM0BA03ABA 3 352 X 260 X 12.8 TRAY 盤 (2)Tray TYSG000000202 42 4.2 0.1 内盒(3)Product Box BX00000000005 4 393 X 274 X 68 0.2692 6 1.6152 OTPLB00PL08ABA 5 550 X 393 X 20 0.0284 2 0.0568 保利龍板(4)Polylon board 6 外紙箱(5)Carton BX0000000044 570 X 410 X 265 1.4208 1 1.4208 7 8 9 2. 一整箱總重量 (Total LCD Weight in carton): 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCM quantity per box : no per tray x no of tray 6 24 (2) Total LCM quantity in carton: quantity per box x no of boxes 24 6 144 Use empty tray 空盤 (4)保利龍板 (1)多層薄膜 Polylon board POF Put products into the tray (2)TRAY 盤 Tray (5)外紙箱 Carton Tray stacking (3)内盒 Product Box 特 記 事 項 (REMARK) Detail B Trav 1 4.TRAY盤相疊時,需旋轉180度,請詳見B視圖

Rotate tray 180 degrees and place on top of stack

Check the tray stack using Fig. B.