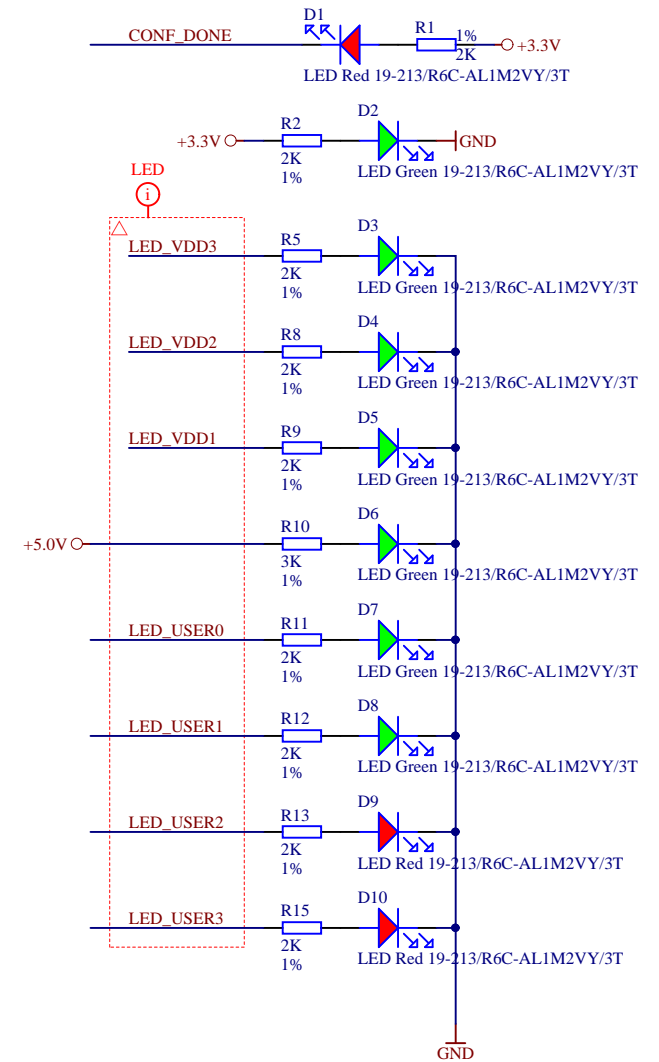
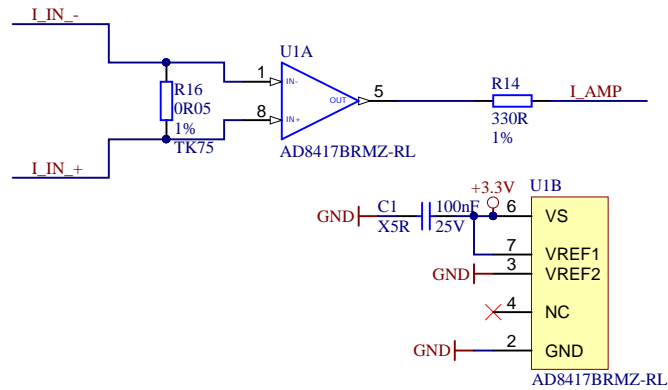
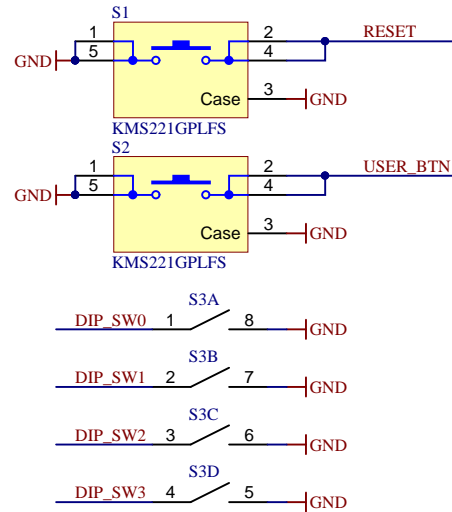
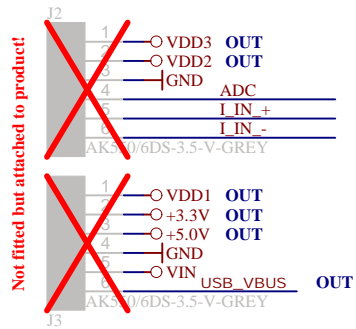
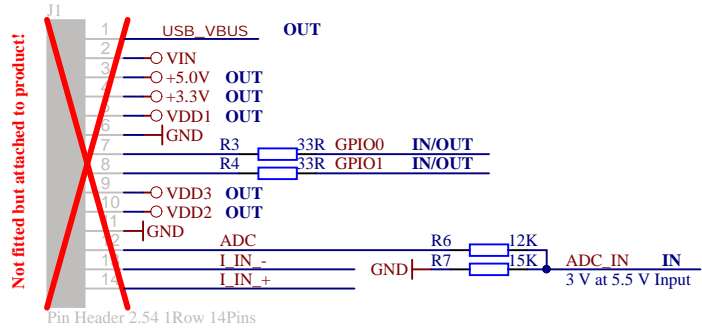


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1

2

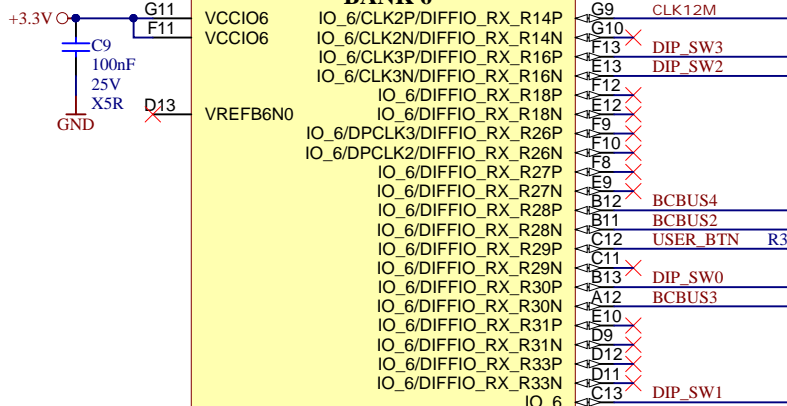
3

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U2E

RIGHT

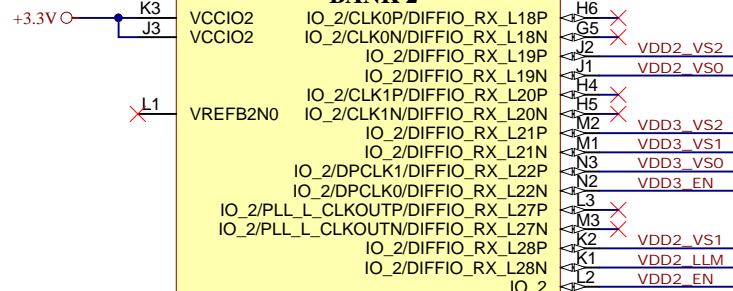
BANK 6



U2B

LEFT

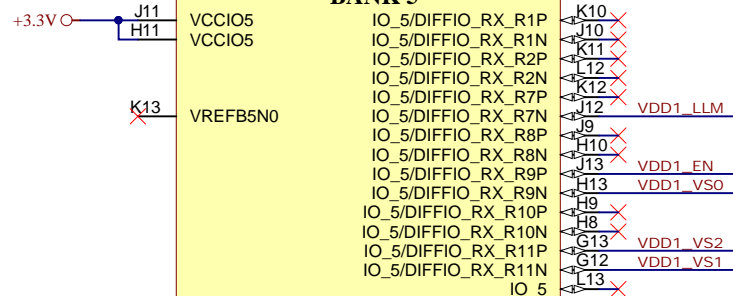
BANK 2



U2D

RIGHT

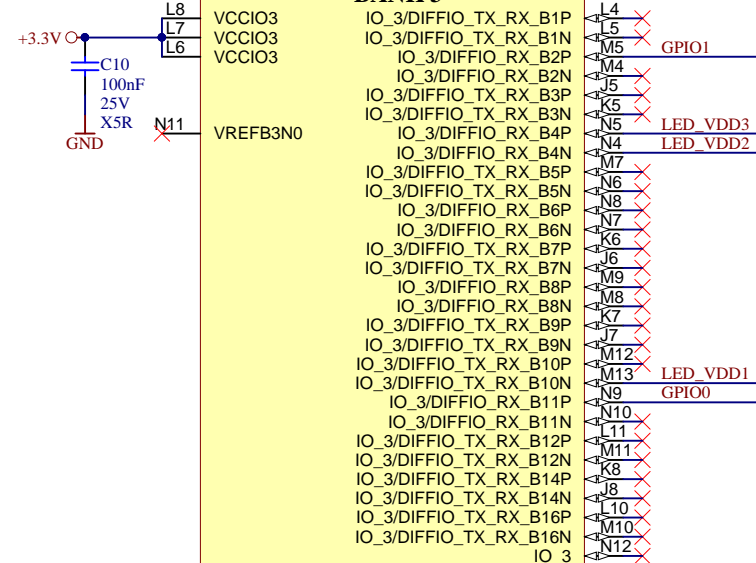
BANK 5



U2C

BOTTOM

BANK 3



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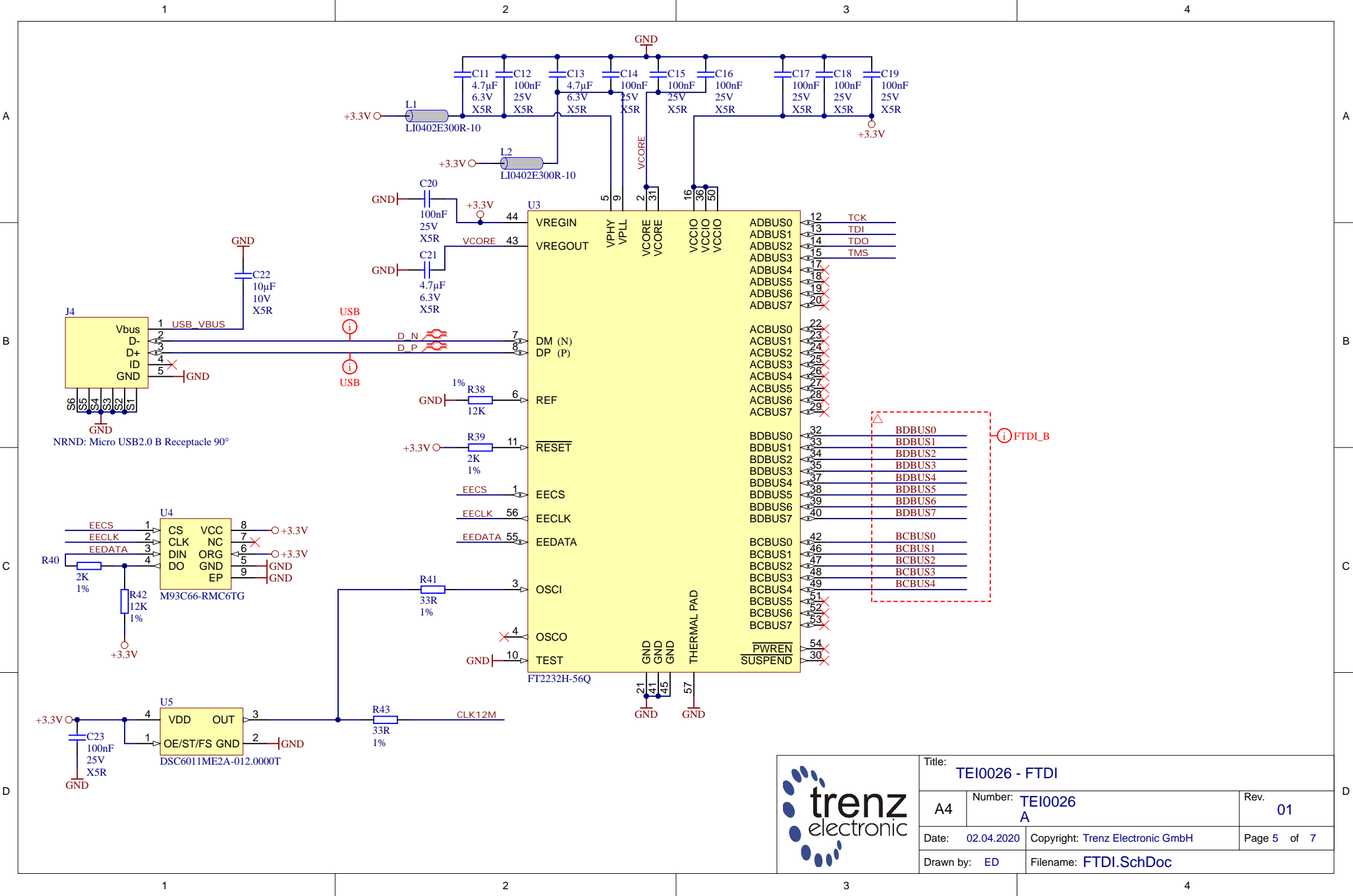
Filename: FPGA.SchDoc


1

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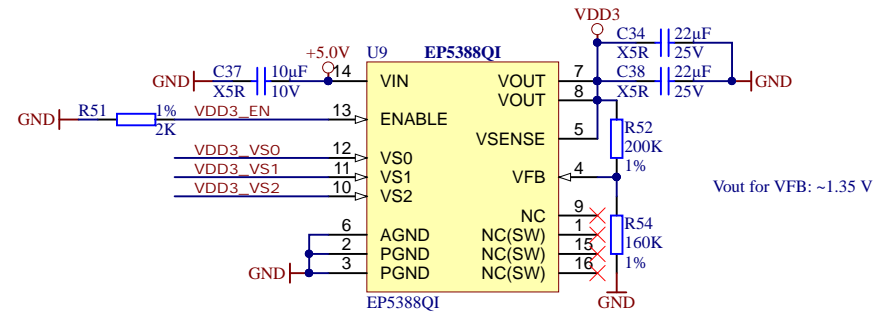
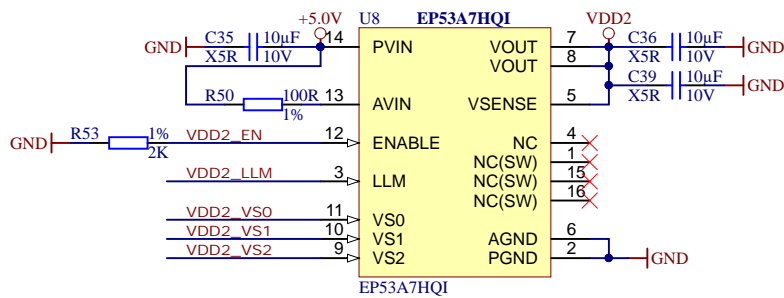
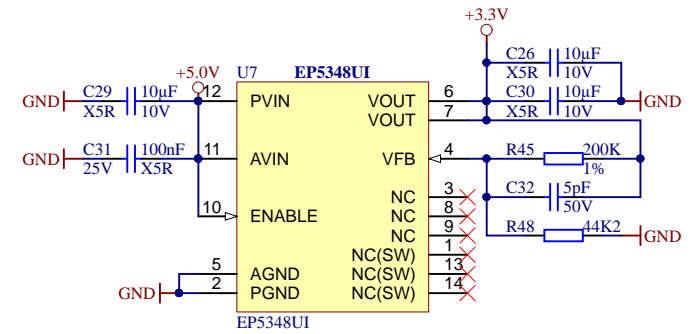
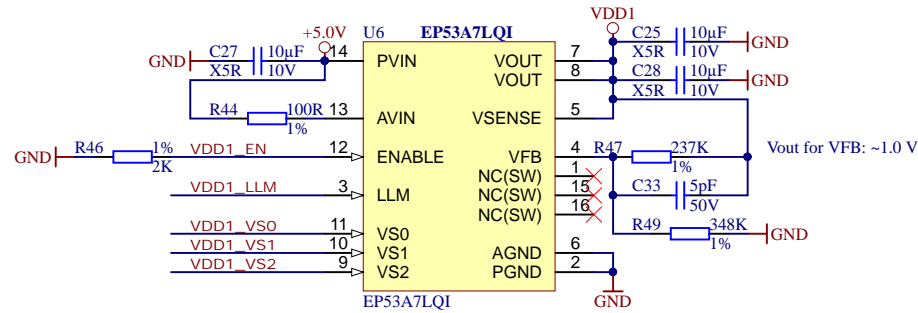
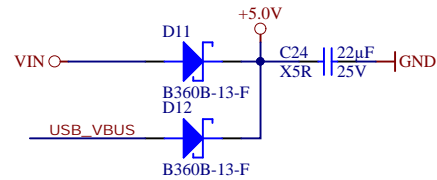
3


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Revision 01

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