Roll No. | | | | | | | | | | | | |  Total No. of Pages : 02

Total No. of Questions : 16

BCA (2014 to 2018) (Sem.–2)

# COMPUTER SYSTEM ARCHITECTURE

Subject Code : BSBC-204

M.Code : 10053

Time : 3 Hrs.  Max. Marks : 60

**INSTRUCTIONS TO CANDIDATES :**

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

## SECTION-A

**Answer briefly :**

1. What is Interrupt Cycle?
2. What are Different types of Addressing Modes?
3. What do you understand by Cache memory?
4. Define Shift Operators and its types.
5. What does locality of reference mean?
6. What is difference between computer organization computer architecture?
7. Explain the term Handshaking.
8. Briefly explain the DMA controlled data transfer technique.
9. What is Set-Associative Mapping?
10. What do you understand by Register Stack?

## SECTION-B

11. Write short notes on :

    a) Layered Approach Architecture

    b) Replacement Algorithms

12. What is meant by SISD, SIMD and MIMD architecture? Differentiate **SIMD and MIMD** architectures.

13. What do you mean by addressing mode? What are various types of **addressing modes and** their advantages and disadvantages?

14. Explain Von Neumann Architecture in detail.

15. What is an interrupt in computer organisation? Discuss interrupt **types and interrupt** cycle.

16. What is difference between synchronous and asynchronous data **transfer?**

**NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.**