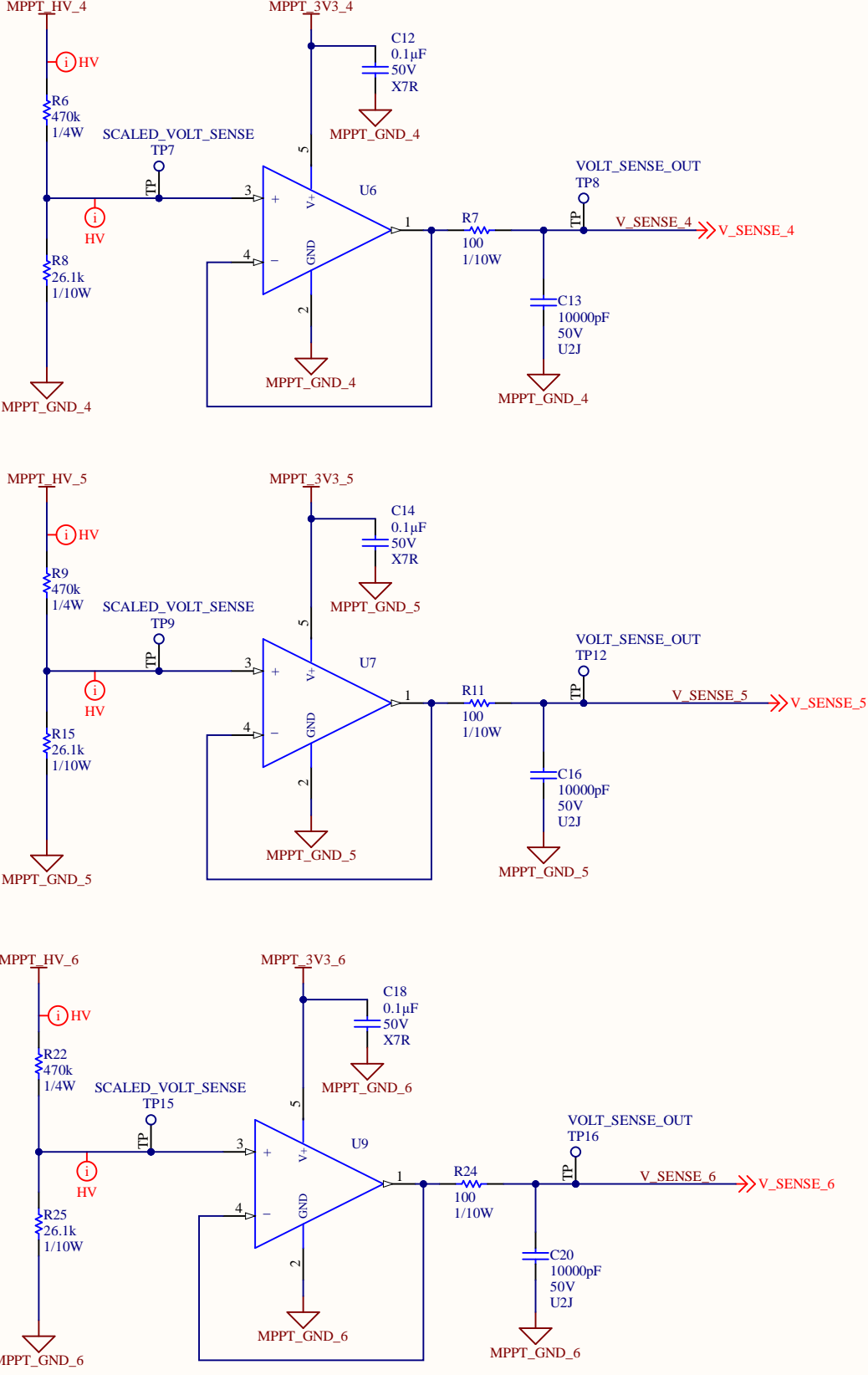
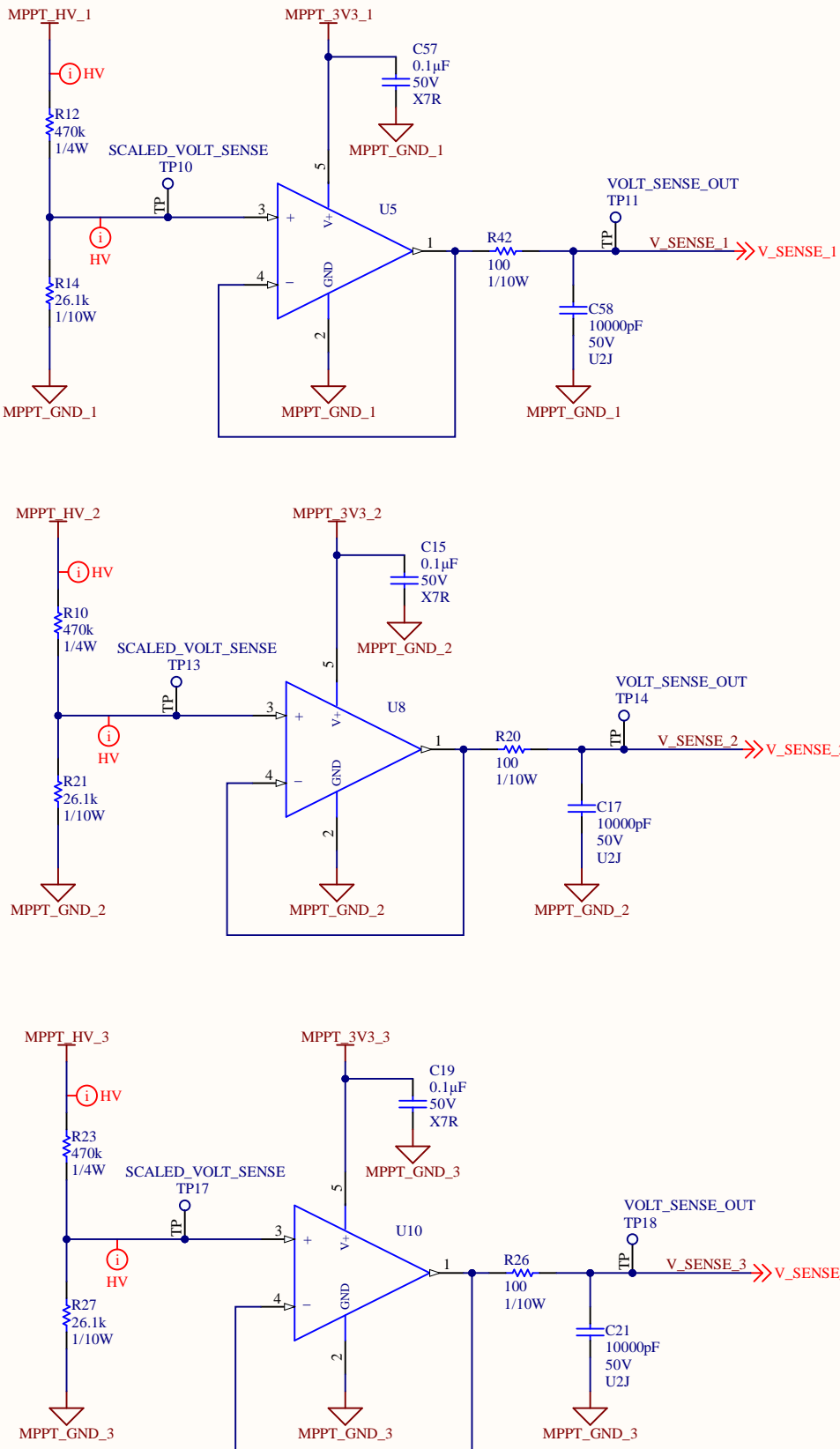
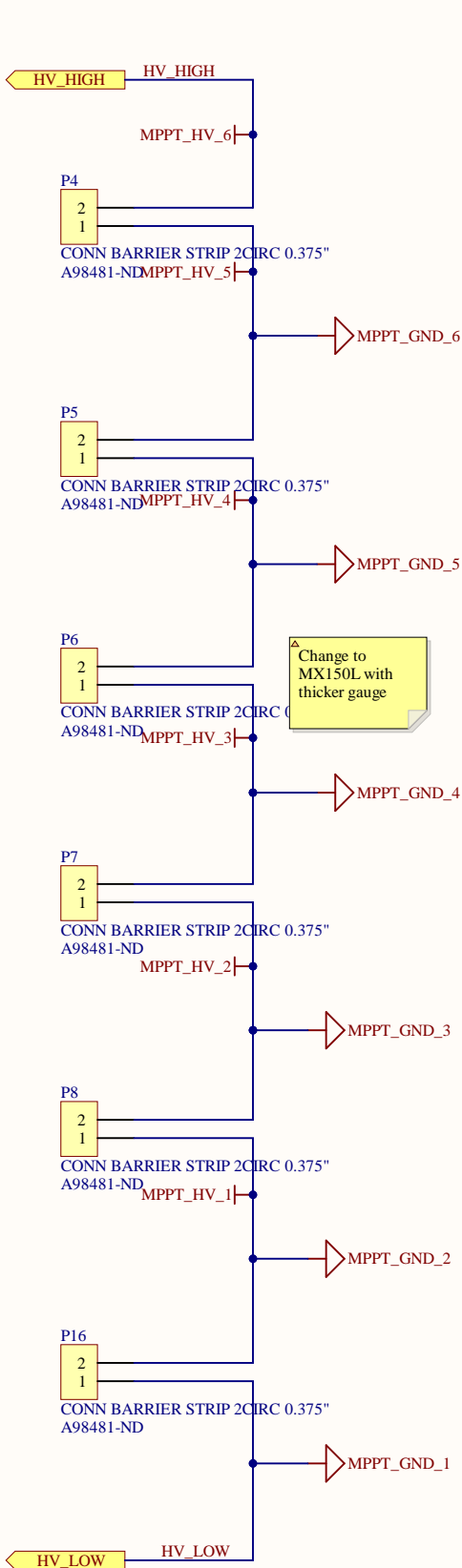
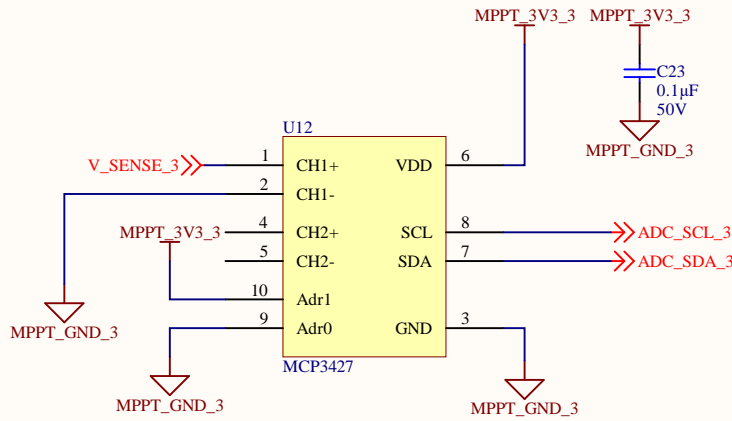
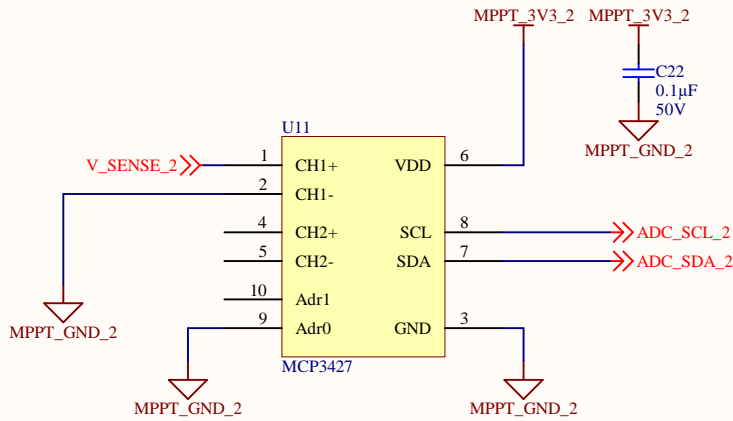
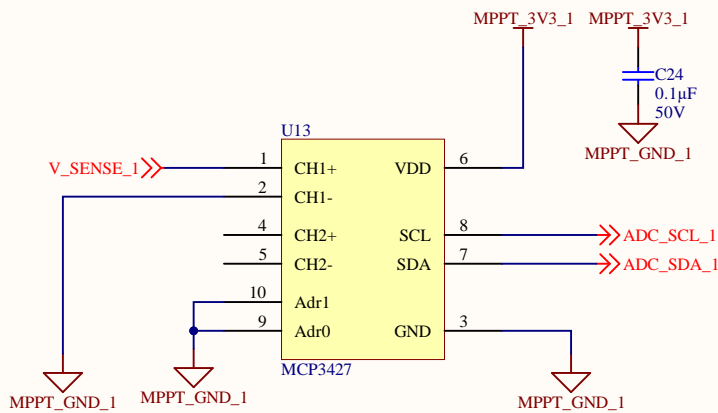


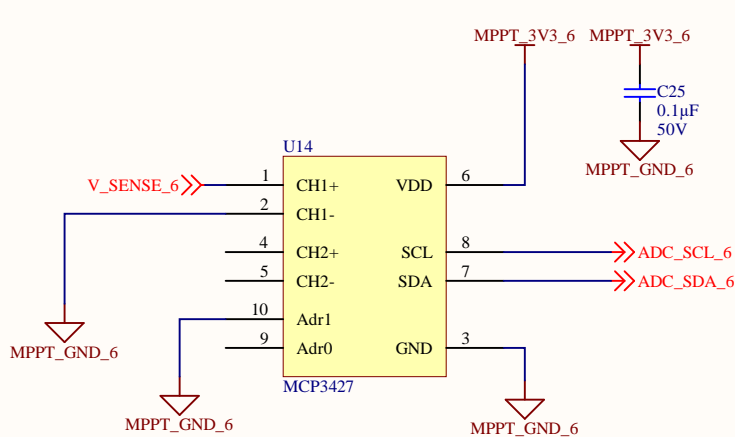
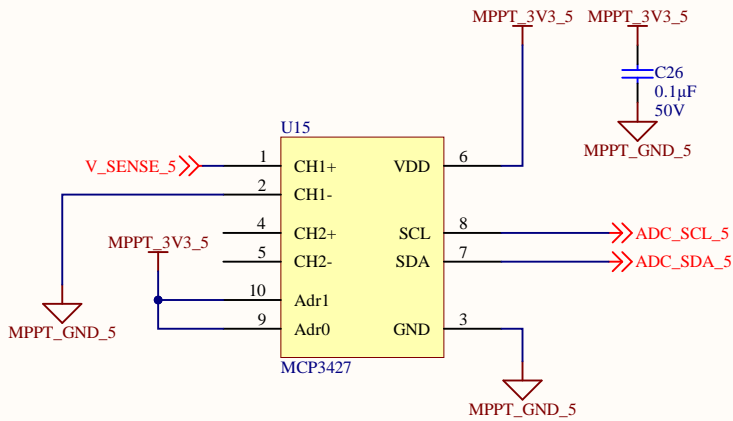
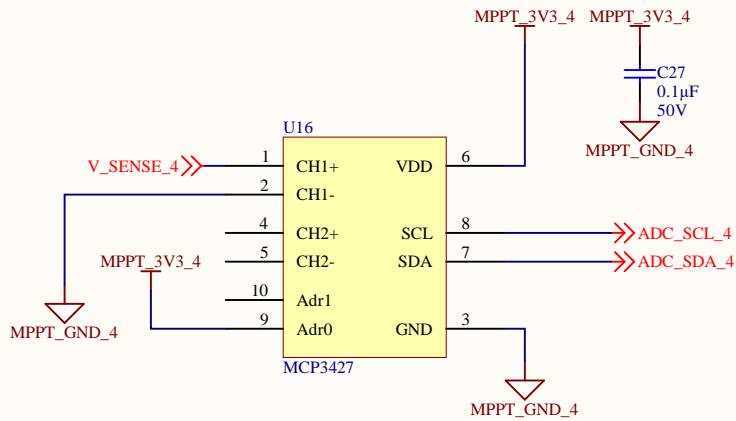
Voltage Sense





ADCs

I <sup>2</sup> C Device Address Bits			Logic Status of Address Selection Pins	
A2	A1	A0	Adr0 Pin	Adr1 Pin
0	0	0	0 (Addr_Low)	0 (Addr_Low)
0	0	1	0 (Addr_Low)	Float
0	1	0	0 (Addr_Low)	1 (Addr_High)
1	0	0	1 (Addr_High)	0 (Addr_Low)
1	0	1	1 (Addr_High)	Float
1	1	0	1 (Addr_High)	1 (Addr_High)
0	1	1	Float	0 (Addr_Low)
1	1	1	Float	1 (Addr_High)
0	0	0	Float	Float



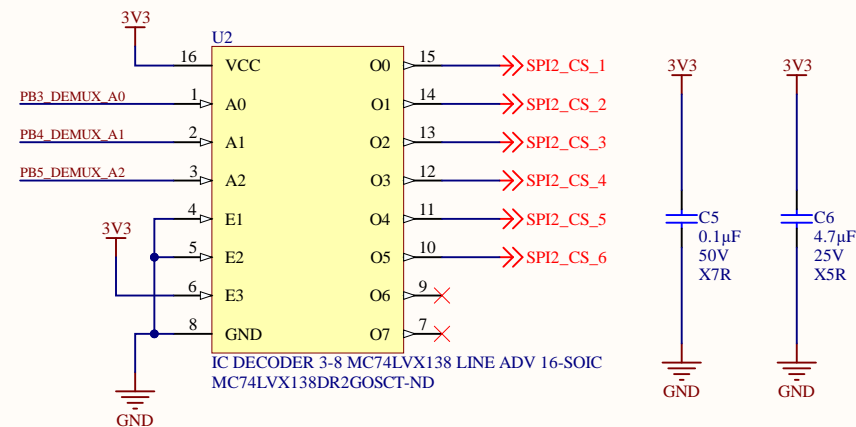
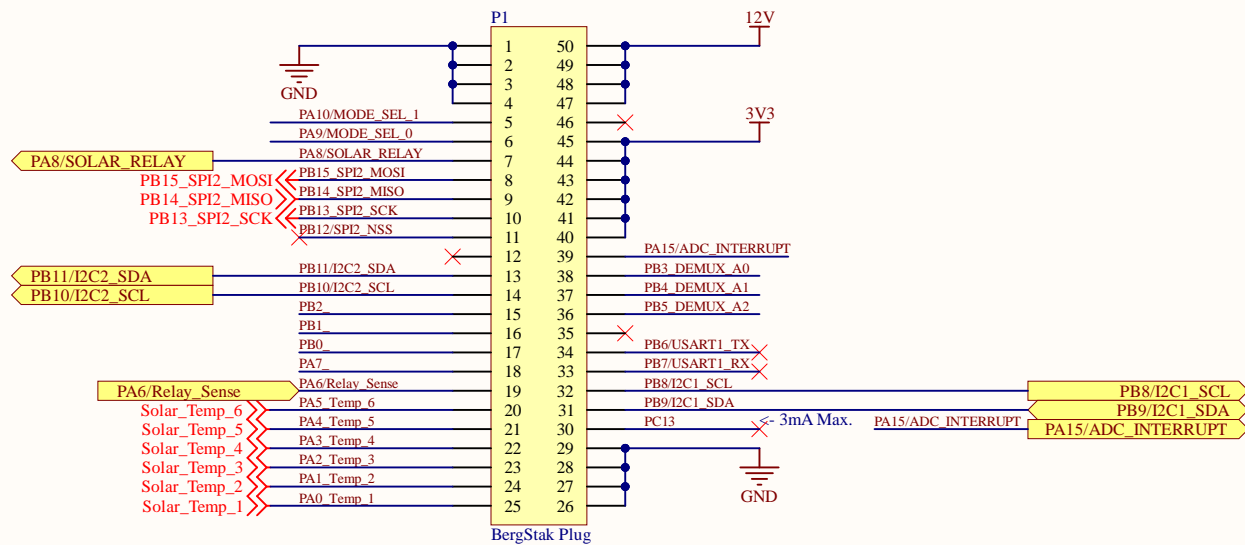
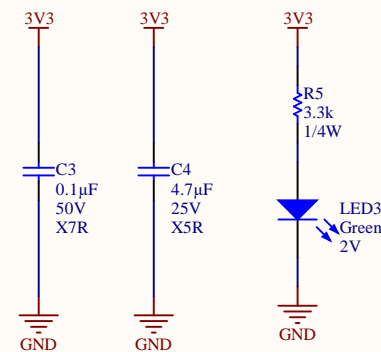
Make combination of pulled high, low and floating (for address pins)


PROJECT MSXIV_SolarSense.PrjPcb	
DOCUMENT *	
PART NUMBER	VARIANT [No Variations]
DRAWN BY Aashmika Mali	REVISION 1.0
LAST MODIFIED 2020-02-02	SHEET * OF *

MIDNIGHT SUN

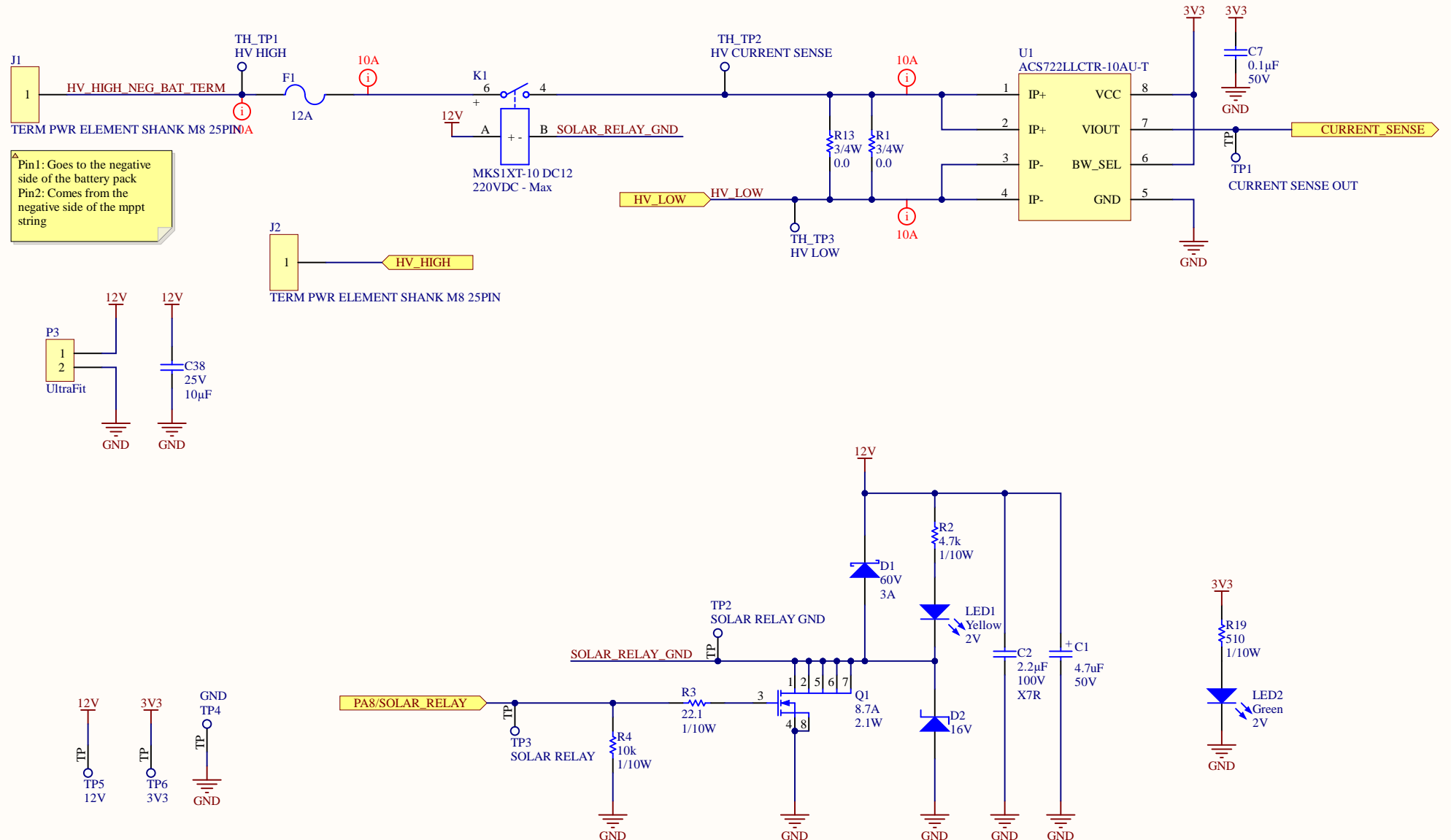
Engineering 5 - 1002  
University of Waterloo  
(519) 888-4567 x32978  
hardware@uwmidsun.com

Add 6 gpio pins for slave select on spi, and 6 gpios for temperature sensing



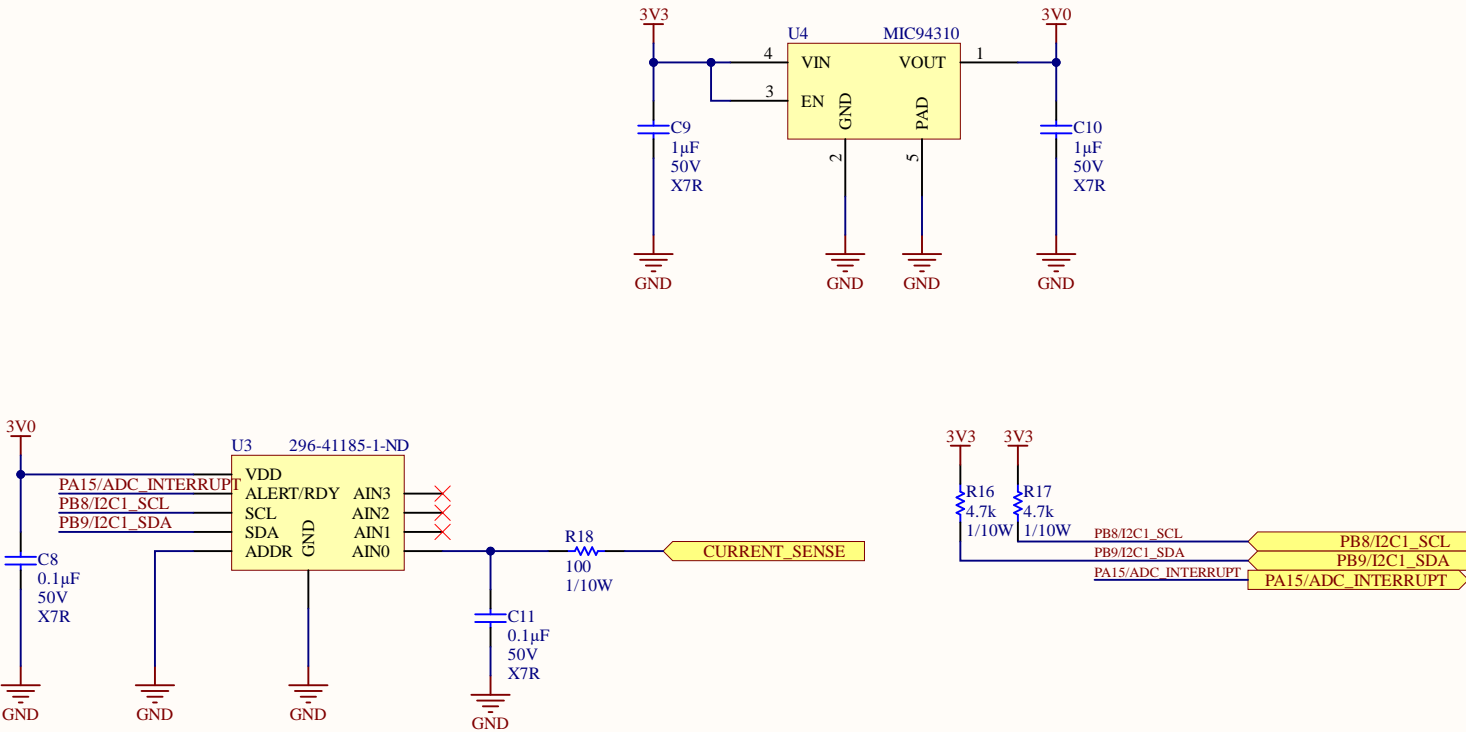
Project: <i>MSXIV_SolarSense.PrjPcb</i>		
Title: *		
Project Author: Aashmika Mali		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.0	
Date: 2020-02-02	Sheet* of *	
		Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a>

# Passthrough - Current Sense, Fuse, and Relay



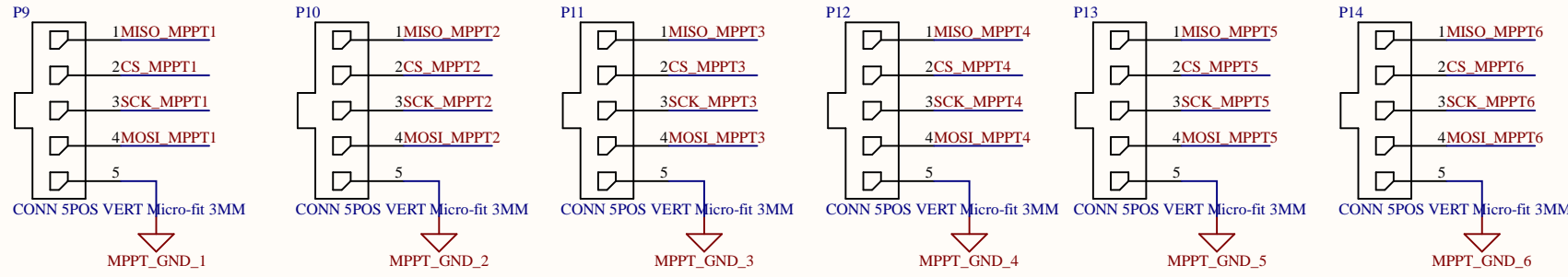
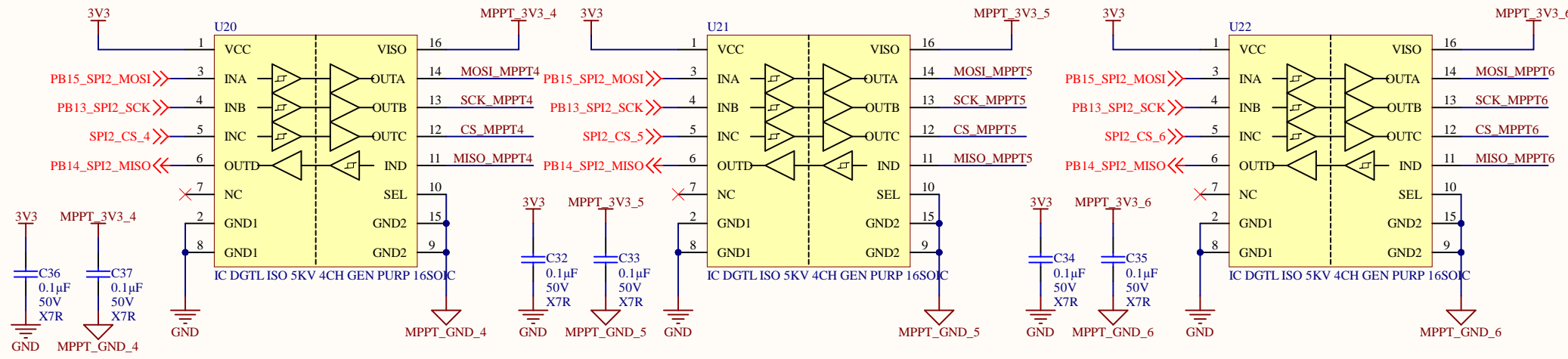
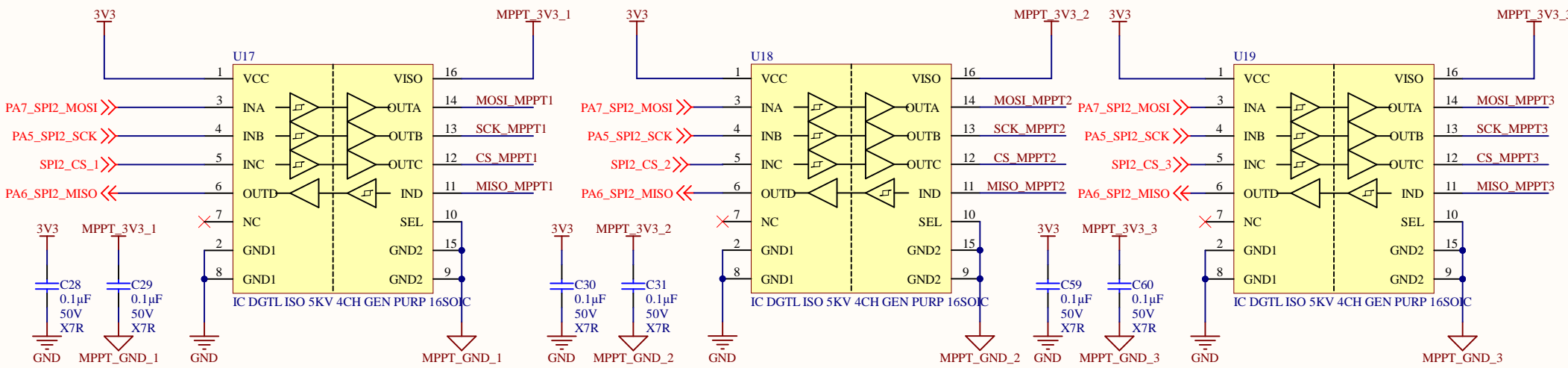
Project: <i>MSXIV_SolarSense.PrjPcb</i>		<div><div>MIDNIGHT</div><div>SUN</div></div> <div>University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9 Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a></div>
Title: *		
Project Author: <i>Aashmika Mali</i>		
Size: <i>Letter</i>	Revision: <i>1.0</i>	
Date: <i>2020-02-02</i>	Sheet* of *	

I2C Interface (for Current Sense)



Project: <i>MSXIV_SolarSense.PrjPcb</i>		<div><div>MIDNIGHT</div><div>SUN</div></div>
Title: *		
Project AuthorAashmika Mali		University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9
Size: Letter	Revision: 1.0	
Date: 2020-02-02	Sheet * of *	
		Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a>

# SPI Isolators

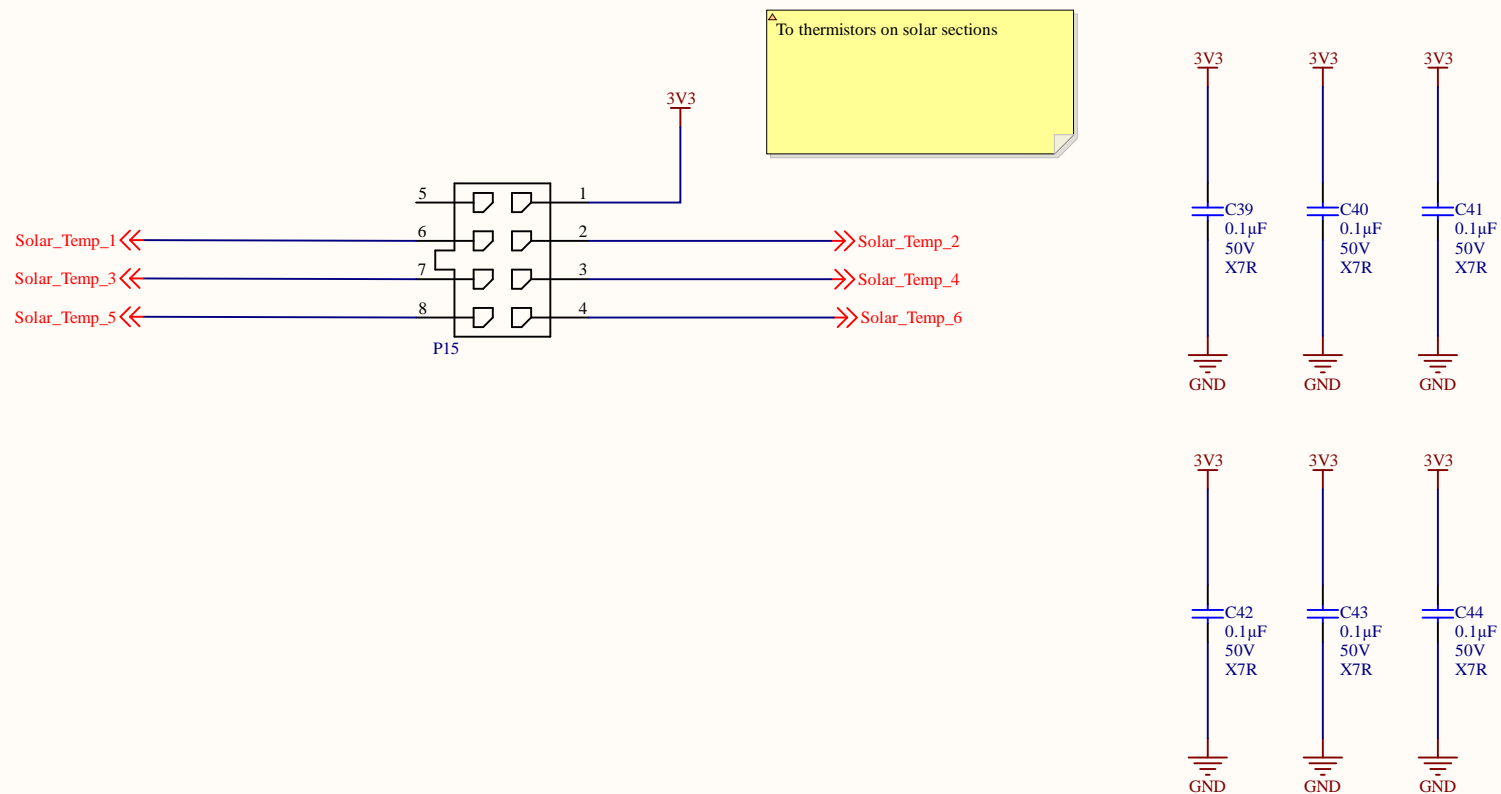


PROJECT	MSXIV_SolarSense.PrjPcb		
DOCUMENT	*		
PART NUMBER	VARIANT	[No Variations]	
DRAWN BY	Aashmika Mali	REVISION	1.0
LAST MODIFIED	2020-02-02	SHEET	* OF *

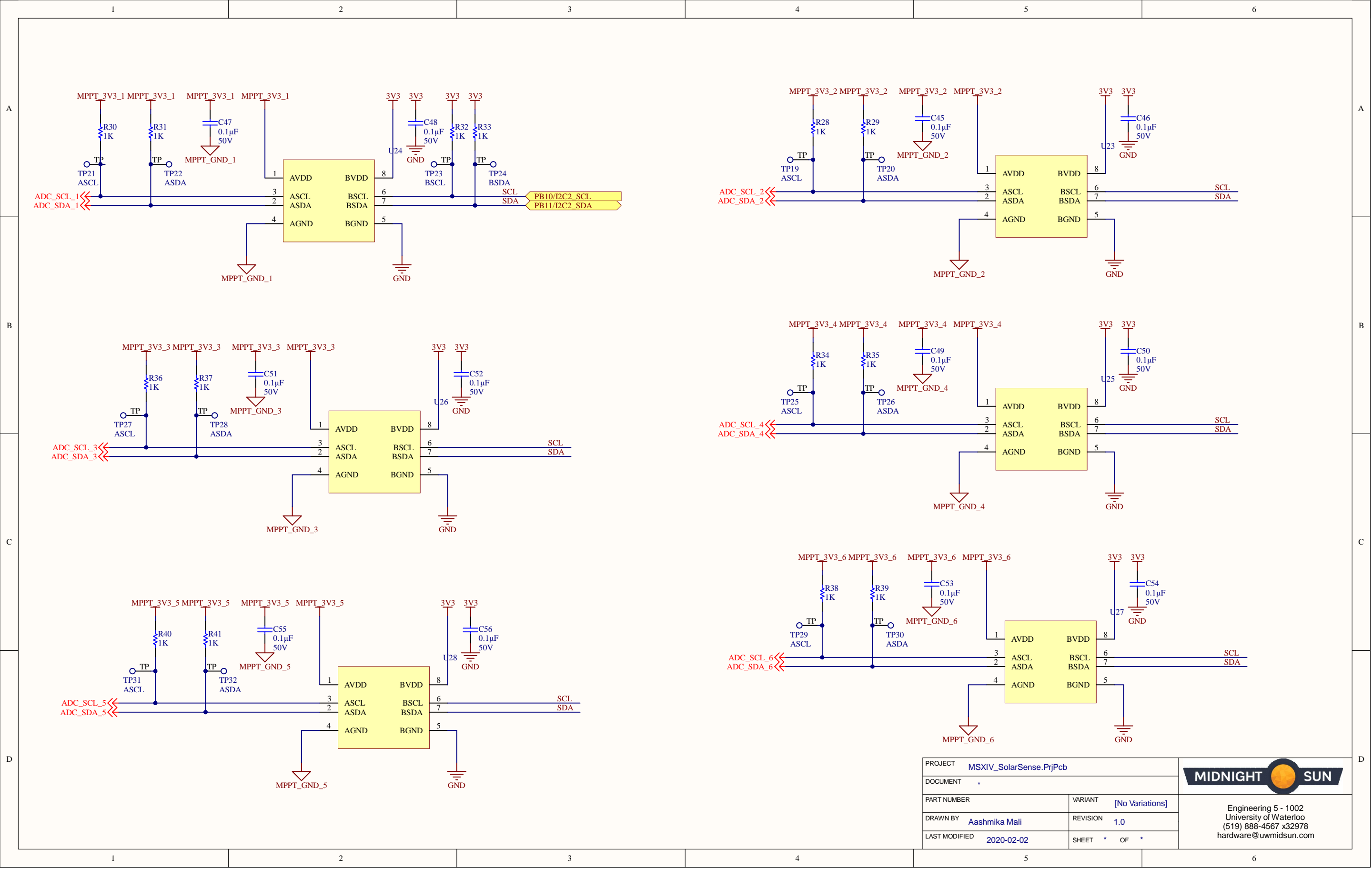
MIDNIGHT

SUN

Engineering 5 - 1002  
University of Waterloo  
(519) 888-4567 x32978  
hardware@uwmidsun.com

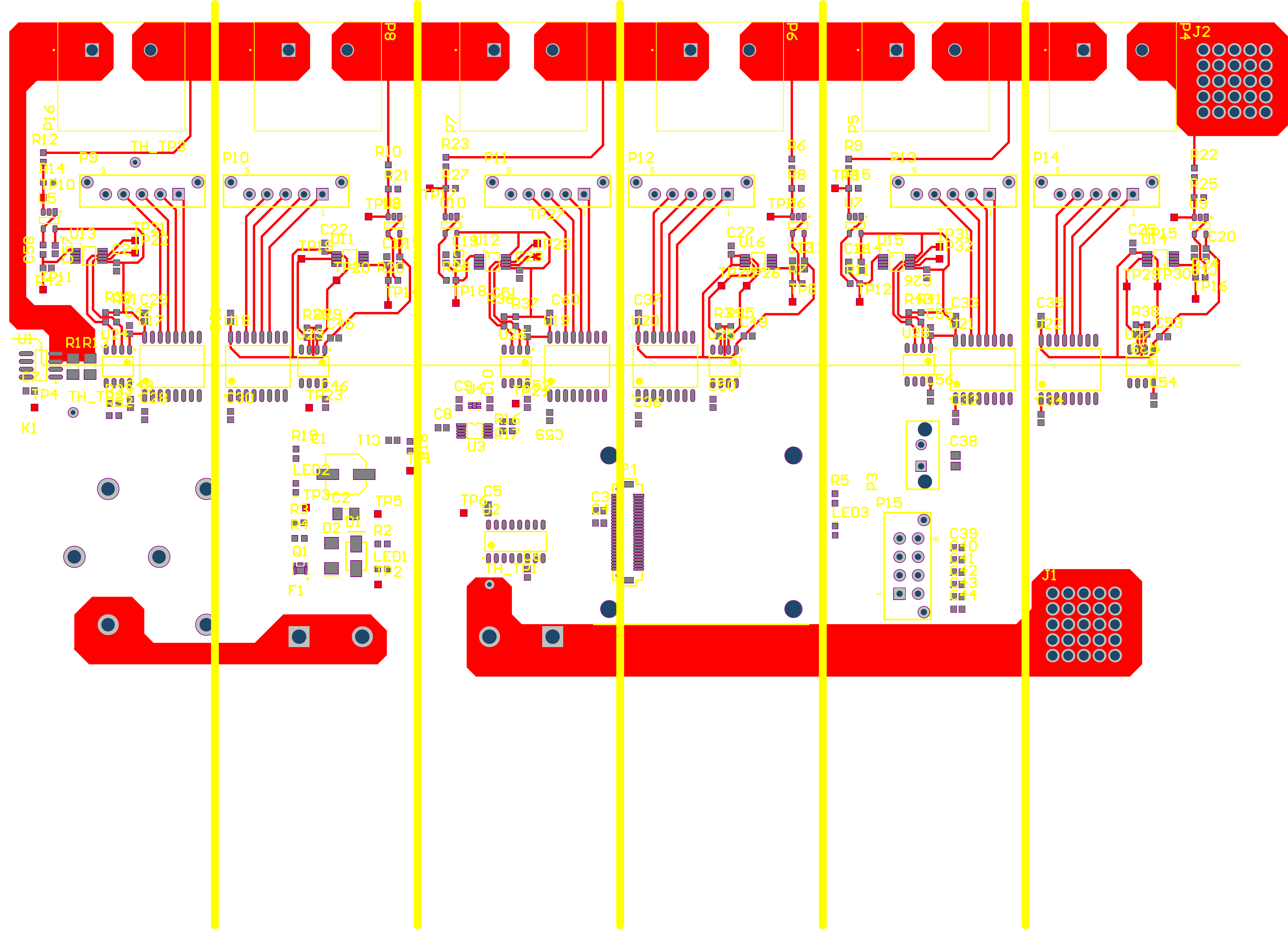


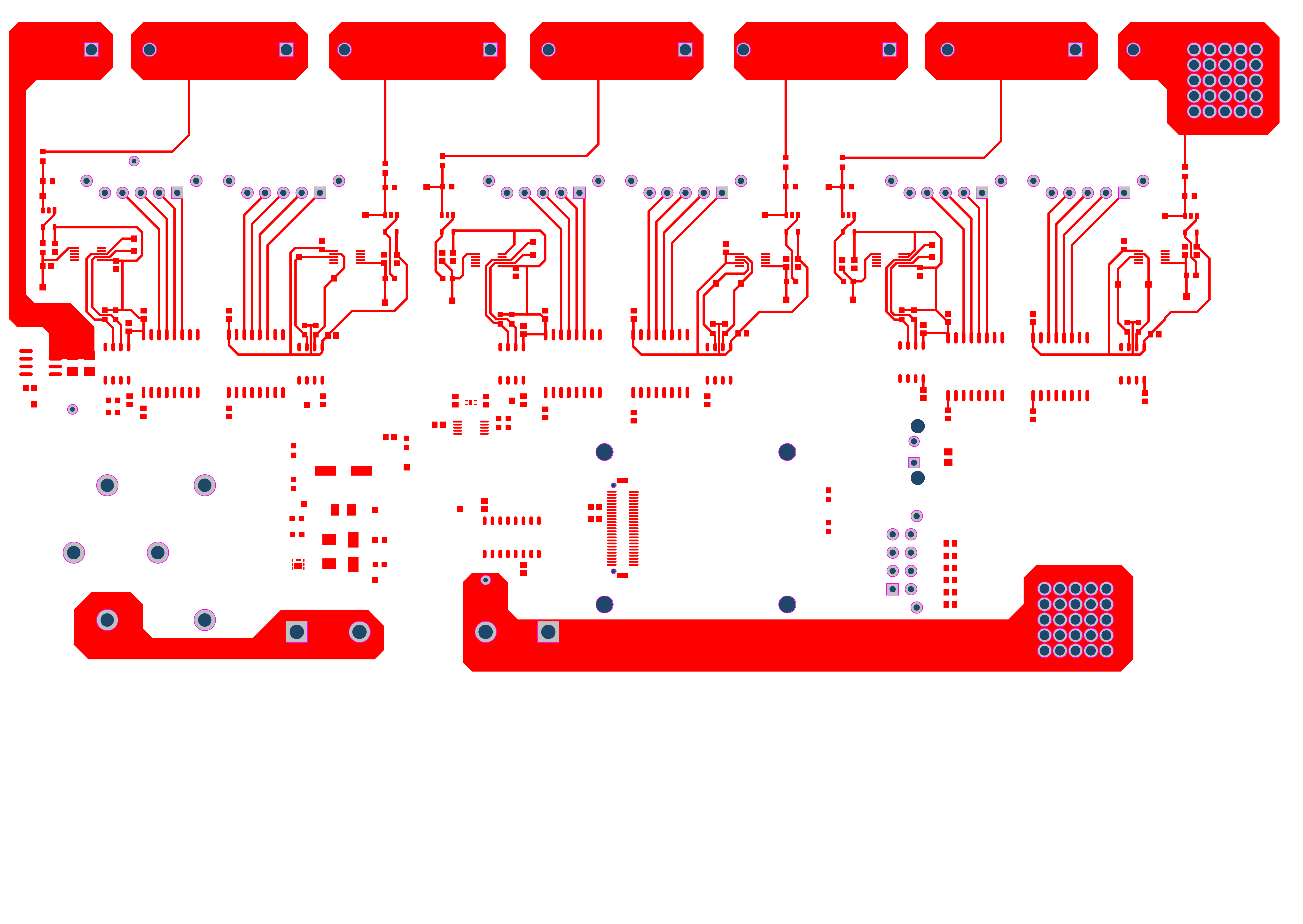
Project: <i>MSXIV_SolarSense.PrjPcb</i>		<div><div>MIDNIGHT</div><div>SUN</div></div> <div>University of Waterloo 200 University Ave W Waterloo, ON, Canada N2L 3E9</div> <div>Website: <a href="http://www.uwmidsun.com">www.uwmidsun.com</a></div>
Title: *		
Project AuthorAashmika Mali		
Size: Letter	Revision: 1.0	
Date: 2020-02-02	Sheet* of *	





[illegible]







# Electrical Rules Check Report

[illegible]

Class	Document	Message
Warning	Controller_Board_Interface.SchDoc	Cross-Sheet Connector Solar_Temp_1, Net Label PA0_Temp_1
Warning	Controller_Board_Interface.SchDoc	Nets Wire PA1_Temp_2 has multiple names (Cross-Sheet Connector Solar_Temp_2, Cross-Sheet Connector Solar_Temp_2, Net Label PA1_Temp_2)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PA2_Temp_3 has multiple names (Cross-Sheet Connector Solar_Temp_3, Cross-Sheet Connector Solar_Temp_3, Net Label PA2_Temp_3)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PA3_Temp_4 has multiple names (Cross-Sheet Connector Solar_Temp_4, Cross-Sheet Connector Solar_Temp_4, Net Label PA3_Temp_4)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PA4_Temp_5 has multiple names (Cross-Sheet Connector Solar_Temp_5, Cross-Sheet Connector Solar_Temp_5, Net Label PA4_Temp_5)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PA5_Temp_6 has multiple names (Cross-Sheet Connector Solar_Temp_6, Cross-Sheet Connector Solar_Temp_6, Net Label PA5_Temp_6)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PB10/I2C2_SCL has multiple names (Net Label PB10/I2C2_SCL, Net Label SCL, Net Label SCL, Net Label SCL, Net Label SCL, Net Label SCL)
Warning	Controller_Board_Interface.SchDoc	Nets Wire PB11/I2C2_SDA has multiple names (Net Label PB11/I2C2_SDA, Net Label SDA, Net Label SDA, Net Label SDA, Net Label SDA, Net Label SDA)
Warning	ADCs.SchDoc	Off grid C24 at 3818.11mil,8674.41mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector ADC_SCL_1 at 3600mil,7694.41mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector ADC_SDA_1 at 3600mil,7544.41mil
Warning	SPI_Interface.SchDoc	Off grid Cross-Sheet Connector PA5_SPI2_SCK at 6574.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid Cross-Sheet Connector PA6_SPI2_MISO at 6574.41mil,6251.811mil
Warning	SPI_Interface.SchDoc	Off grid Cross-Sheet Connector PA7_SPI2_MOSI at 6574.41mil,6851.811mil
Warning	TemperatureSense.SchDoc	Off grid Cross-Sheet Connector Solar_Temp_2 at 6687.008mil,4796.063mil
Warning	TemperatureSense.SchDoc	Off grid Cross-Sheet Connector Solar_Temp_4 at 6687.008mil,4596.063mil
Warning	Controller_Board_Interface.SchDoc	Off grid Cross-Sheet Connector Solar_Temp_6 at 2247.008mil,4300mil
Warning	TemperatureSense.SchDoc	Off grid Cross-Sheet Connector Solar_Temp_6 at 6700mil,4396.063mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_1 at 1350mil,8044.41mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_2 at 6281.89mil,8100mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_3 at 11381.89mil,8100mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_4 at 1631.89mil,3500mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_5 at 6531.89mil,3500mil
Warning	ADCs.SchDoc	Off grid Cross-Sheet Connector V_SENSE_6 at 11231.89mil,3400mil
Warning	SPI_Interface.SchDoc	Off grid Net Label CS_MPPT2 at 8674.41mil,6451.811mil
Warning	SPI_Interface.SchDoc	Off grid Net Label MISO_MPPT2 at 8624.41mil,6251.811mil
Warning	SPI_Interface.SchDoc	Off grid Net Label MOSI_MPPT2 at 8674.41mil,6851.811mil
Warning	SPI_Interface.SchDoc	Off grid Net Label SCK_MPPT2 at 8674.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid No ERC at 6774.41mil,6051.811mil
Warning	TemperatureSense.SchDoc	Off grid P15 at 4521.654mil,5096.063mil
Warning	ADCs.SchDoc	Off grid Pin C24-1 at 3818.11mil,8674.41mil
Warning	ADCs.SchDoc	Off grid Pin C24-2 at 3818.11mil,8374.41mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-1 at 5421.654mil,4996.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-2 at 5421.654mil,4796.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-3 at 5421.654mil,4596.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-4 at 5421.654mil,4396.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-5 at 4521.654mil,4996.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-6 at 4521.654mil,4796.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-7 at 4521.654mil,4596.063mil
Warning	TemperatureSense.SchDoc	Off grid Pin P15-8 at 4521.654mil,4396.063mil
Warning	ADCs.SchDoc	Off grid Pin U13-1 at 1658.11mil,8044.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-2 at 1658.11mil,7894.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-3 at 3108.11mil,7194.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-4 at 1658.11mil,7694.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-5 at 1658.11mil,7544.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-6 at 3108.11mil,8044.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-7 at 3108.11mil,7544.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-8 at 3108.11mil,7694.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-9 at 1658.11mil,7194.41mil
Warning	ADCs.SchDoc	Off grid Pin U13-10 at 1658.11mil,7344.41mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-1 at 6774.41mil,7051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-2 at 6774.41mil,5851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-3 at 6774.41mil,6851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-4 at 6774.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-5 at 6774.41mil,6451.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-6 at 6774.41mil,6251.811mil

Class	Document	Message
Warning	SPI_Interface.SchDoc	Off grid Pin U18-7 at 6774.41mil,6051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-8 at 6774.41mil,5651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-9 at 8574.41mil,5651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-10 at 8574.41mil,6051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-11 at 8574.41mil,6251.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-12 at 8574.41mil,6451.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-13 at 8574.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-14 at 8574.41mil,6851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-15 at 8574.41mil,5851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-16 at 8574.41mil,7051.811mil
Warning	TemperatureSense.SchDoc	Off grid Power Object 3V3 at 5899.606mil,5389.764mil
Warning	SPI_Interface.SchDoc	Off grid Power Object 3V3 at 6174.41mil,7351.811mil
Warning	SPI_Interface.SchDoc	Off grid Power Object GND at 6774.41mil,5351.811mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_3V3_1 at 3218.11mil,8674.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_3V3_1 at 3818.11mil,8674.41mil
Warning	SPI_Interface.SchDoc	Off grid Power Object MPPT_3V3_2 at 9074.41mil,7351.811mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 818.11mil,7074.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 3468.11mil,7074.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 3818.11mil,8374.41mil
Warning	SPI_Interface.SchDoc	Off grid Power Object MPPT_GND_2 at 8574.41mil,5351.811mil
Warning	ADCs.SchDoc	Off grid U13 at 1958.11mil,8194.41mil
Warning	SPI_Interface.SchDoc	Off grid U18 at 7474.41mil,6651.811mil

## Design Rules Verification Report

Filename : C:\Users\Aashmika Mali\Documents\First Year\Midnight Sun\hardware\MSXIV\_

Warnings 14  
Rule Violations 500

Warnings	
Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommended to	14
Total	14

Rule Violations	
Clearance Constraint (Gap=10mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( (All) )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	14
Width Constraint (Min=10mil) (Max=50mil) (Preferred=10mil) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=10mil) (All),(All)	94
Silk To Solder Mask (Clearance=10mil) (IsPad),(All)	267
Silk to Silk (Clearance=10mil) (All),(All)	125
Net Antennae (Tolerance=0mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	500

Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommended to	
Polygon named: MPPT_GND_1 In net GND On Top Layer	
Polygon named: MPPT_GND_6 In net MPPT_GND_6 On Top Layer	
Polygon named: MPPT_GND_5 In net MPPT_GND_5 On Top Layer	
Polygon named: MPPT_GND_4 In net MPPT_GND_4 On Top Layer	
Polygon named: MPPT_GND_3 In net MPPT_GND_3 On Top Layer	
Polygon named: MPPT_GND_2 In net MPPT_GND_2 On Top Layer	
Polygon named: HV_LOW In net HV_LOW On Top Layer	
Polygon named: MPPT_GND_6 In net MPPT_GND_6 On Bottom Layer	
Polygon named: MPPT_GND_5 In net MPPT_GND_5 On Bottom Layer	
Polygon named: MPPT_GND_4 In net MPPT_GND_4 On Bottom Layer	
Polygon named: MPPT_GND_3 In net MPPT_GND_3 On Bottom Layer	
Polygon named: MPPT_GND_2 In net MPPT_GND_2 On Bottom Layer	
Polygon named: MPPT_GND_1 In net GND On Bottom Layer	
Polygon named: HV_LOW In net HV_LOW On Bottom Layer	



Modified Polygon (Allow modified: No), (Allow shelved: No)
Modified Polygon: Polygon Shelved (HV_LOW) on Bottom Layer
Modified Polygon: Polygon Shelved (HV_LOW) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_1) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_1) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_2) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_2) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_3) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_3) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_4) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_4) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_5) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_5) on Top Layer
Modified Polygon: Polygon Shelved (MPPT_GND_6) on Bottom Layer
Modified Polygon: Polygon Shelved (MPPT_GND_6) on Top Layer

Minimum Solder Mask Sliver (Gap=10mil) (All),(All)

[illegible]

Minimum Solder Mask Sliver (Gap=10mil) (All),(All)
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7836.221mil,5411.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7836.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7836.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7836.221mil,5611.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7936.221mil,5411.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7936.221mil,5411.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7936.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7936.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(7936.221mil,5611.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(8036.221mil,5211.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(8036.221mil,5311.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(8036.221mil,5411.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(8036.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.386mil < 10mil) Between Pad J2-1(8036.221mil,5511.811mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (4.145mil < 10mil) Between Pad P1-(3885.827mil,2242.127mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (4.145mil < 10mil) Between Pad P1-(3885.827mil,2797.245mil) on Multi-Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.842mil < 10mil) Between Pad Q1-1(1883.858mil,2261.504mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.843mil < 10mil) Between Pad Q1-2(1883.858mil,2287.095mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.843mil < 10mil) Between Pad Q1-3(1883.858mil,2312.685mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (7.945mil < 10mil) Between Pad Q1-3(1883.858mil,2312.685mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.843mil < 10mil) Between Pad Q1-4(1811.024mil,2312.685mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.843mil < 10mil) Between Pad Q1-4(1811.024mil,2312.685mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (7.945mil < 10mil) Between Pad Q1-4(1811.024mil,2312.685mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (1.842mil < 10mil) Between Pad Q1-5(1811.024mil,2287.095mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (7.354mil < 10mil) Between Pad Q1-7(1847.441mil,2275.284mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U10-1(2850.394mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U10-2(2812.992mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U4-1(2992.126mil,3319.882mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.465mil < 10mil) Between Pad U4-1(2992.126mil,3319.882mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.465mil < 10mil) Between Pad U4-2(2992.126mil,3343.504mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U4-3(2935.433mil,3343.504mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.858mil < 10mil) Between Pad U4-3(2935.433mil,3343.504mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.858mil < 10mil) Between Pad U4-4(2935.433mil,3319.882mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U5-1(273.623mil,4572.047mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U5-2(236.222mil,4572.047mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U6-1(5076.772mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U6-2(5039.37mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U7-1(5441.929mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U7-2(5404.528mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U8-1(2484.449mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U8-2(2447.047mil,4542.323mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.78mil < 10mil) Between Pad U9-1(7653.543mil,4537.402mil) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (5.779mil < 10mil) Between Pad U9-2(7616.142mil,4537.402mil) on Top Layer And Pac

<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C1-2(2023.583mil,2891.246mil) on Top Layer And Text "LED2'
Silk To Solder Mask Clearance Constraint: (7.874mil < 10mil) Between Pad C1-2(2023.583mil,2891.246mil) on Top Layer And Text "LED2'
Silk To Solder Mask Clearance Constraint: (7.874mil < 10mil) Between Pad C1-2(2023.583mil,2891.246mil) on Top Layer And Text "LED2'
Silk To Solder Mask Clearance Constraint: (3.539mil < 10mil) Between Pad C12-1(5078.74mil,4258.858mil) on Top Layer And Text "R7'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C12-2(5078.74mil,4205.709mil) on Top Layer And Text "R7'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C13-1(5001.969mil,4205.709mil) on Top Layer And Text "R7'
Silk To Solder Mask Clearance Constraint: (3.539mil < 10mil) Between Pad C13-2(5001.969mil,4258.858mil) on Top Layer And Text "R7'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C14-1(5441.929mil,4252.041mil) on Top Layer And Text "R11'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C14-2(5441.929mil,4198.891mil) on Top Layer And Text "R11'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C15-2(2484.449mil,4231.693mil) on Top Layer And Text "R20'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C16-1(5363.189mil,4198.891mil) on Top Layer And Text "R11'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C16-2(5363.189mil,4252.041mil) on Top Layer And Text "R11'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C17-1(2401.575mil,4232.283mil) on Top Layer And Text "R20'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C18-2(7653.543mil,4284.449mil) on Top Layer And Text "R24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C19-1(2850.394mil,4298.228mil) on Top Layer And Text "C21'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C19-2(2850.394mil,4245.079mil) on Top Layer And Text "C21'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C19-2(2850.394mil,4245.079mil) on Top Layer And Text "R26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C20-1(7578.74mil,4284.449mil) on Top Layer And Text "R24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C2-1(2193.858mil,2637.795mil) on Top Layer And Text "D1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C21-1(2779.901mil,4245.079mil) on Top Layer And Text "R26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C22-2(2003.441mil,4374.213mil) on Top Layer And Text "TP19'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C26-2(5864.173mil,4149.425mil) on Top Layer And Text "C26'
Silk To Solder Mask Clearance Constraint: (9.323mil < 10mil) Between Pad C29-1(848.622mil,3871.063mil) on Top Layer And Text "C47'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C29-1(848.622mil,3871.063mil) on Top Layer And Text "U17'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C3-1(3792.126mil,2657.48mil) on Top Layer And Text "C4'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C31-1(1399.803mil,3871.063mil) on Top Layer And Text "U18'
Silk To Solder Mask Clearance Constraint: (3.511mil < 10mil) Between Pad C31-2(1399.803mil,3924.213mil) on Top Layer And Text "U18'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C3-2(3738.976mil,2657.48mil) on Top Layer And Text "C4'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C33-1(6047.638mil,3851.378mil) on Top Layer And Text "U21'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C35-1(6596.654mil,3851.378mil) on Top Layer And Text "U22'
Silk To Solder Mask Clearance Constraint: (3.55mil < 10mil) Between Pad C35-2(6596.654mil,3904.528mil) on Top Layer And Text "U22'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C37-1(4015.748mil,3871.063mil) on Top Layer And Text "U20'
Silk To Solder Mask Clearance Constraint: (4.775mil < 10mil) Between Pad C37-2(4015.748mil,3924.213mil) on Top Layer And Text "U20'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C39-1(6036.417mil,2421.26mil) on Top Layer And Text "C40'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C39-2(6089.567mil,2421.26mil) on Top Layer And Text "C40'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C40-1(6036.417mil,2342.519mil) on Top Layer And Text "C41'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C40-2(6089.567mil,2342.519mil) on Top Layer And Text "C41'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C41-2(6089.567mil,2263.78mil) on Top Layer And Text "C42'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C42-1(6036.417mil,2185.039mil) on Top Layer And Text "C43'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C42-2(6089.567mil,2185.039mil) on Top Layer And Text "C43'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C43-2(6089.567mil,2106.299mil) on Top Layer And Text "C44'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C46-1(2007.874mil,3373.032mil) on Top Layer And Text "TP23'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C47-1(752.165mil,3792.321mil) on Top Layer And Text "U24'
Silk To Solder Mask Clearance Constraint: (4.676mil < 10mil) Between Pad C47-2(752.165mil,3845.471mil) on Top Layer And Text "U24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C48-1(758.268mil,3373.032mil) on Top Layer And Text "R32'
Silk To Solder Mask Clearance Constraint: (8.764mil < 10mil) Between Pad C48-1(758.268mil,3373.032mil) on Top Layer And Text "R33'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C48-2(758.268mil,3319.882mil) on Top Layer And Text "R32'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C5-1(3051.181mil,2643.701mil) on Top Layer And Text "U2'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C51-1(3303.347mil,3772.55mil) on Top Layer And Text "U26'
Silk To Solder Mask Clearance Constraint: (9.813mil < 10mil) Between Pad C51-2(3303.347mil,3825.7mil) on Top Layer And Text "U26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C55-1(5887.402mil,3779.528mil) on Top Layer And Text "U28'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C55-2(5887.402mil,3832.677mil) on Top Layer And Text "U28'

<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C58-1(196.85mil,4212.598mil) on Top Layer And Text "TP11"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C58-2(250mil,4212.598mil) on Top Layer And Text "TP11"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C60-1(3444.882mil,3871.063mil) on Top Layer And Text "U19"
Silk To Solder Mask Clearance Constraint: (1.898mil < 10mil) Between Pad C60-2(3444.882mil,3924.213mil) on Top Layer And Text "U19"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C6-1(3303.347mil,2283.465mil) on Top Layer And Text "TH_TP1"
Silk To Solder Mask Clearance Constraint: (8.318mil < 10mil) Between Pad C6-2(3303.347mil,2230.316mil) on Top Layer And Text "TH_TP1"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad C7-2(140.846mil,3425.197mil) on Top Layer And Text "TP4"
Silk To Solder Mask Clearance Constraint: (6.535mil < 10mil) Between Pad D1-1(2204.724mil,2287.095mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (6.535mil < 10mil) Between Pad D1-1(2204.724mil,2287.095mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (6.535mil < 10mil) Between Pad D1-2(2204.724mil,2444.575mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (6.535mil < 10mil) Between Pad D1-2(2204.724mil,2444.575mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.724mil < 10mil) Between Pad D2-2(2047.244mil,2289.278mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.35mil < 10mil) Between Pad K1-B(1244.094mil,1927.165mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad LED1-2(2344.488mil,2283.465mil) on Top Layer And Text "TP2"
Silk To Solder Mask Clearance Constraint: (8.213mil < 10mil) Between Pad LED1-2(2344.488mil,2283.465mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (8.213mil < 10mil) Between Pad LED2-2(1818.467mil,2774.89mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (8.213mil < 10mil) Between Pad LED3-2(5275.591mil,2559.055mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-(3944.882mil,2212.599mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-(3944.882mil,2826.772mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.053mil < 10mil) Between Pad P10-0(1401.575mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P10-0(1401.575mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P10-0(2110.236mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.4mil < 10mil) Between Pad P10-0(2110.236mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.78mil < 10mil) Between Pad P1-1(4015.748mil,2755.905mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.053mil < 10mil) Between Pad P11-0(3078.74mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P11-0(3078.74mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P12-0(4708.661mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.4mil < 10mil) Between Pad P12-0(4708.661mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.767mil < 10mil) Between Pad P1-25(4015.748mil,2283.465mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.4mil < 10mil) Between Pad P1-26(3874.016mil,2283.465mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-26(3874.016mil,2283.465mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-27(3874.016mil,2303.15mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-28(3874.016mil,2322.835mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-29(3874.016mil,2342.52mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-30(3874.016mil,2362.205mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.053mil < 10mil) Between Pad P13-0(5680.118mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P13-0(5680.118mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P13-0(6388.779mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.4mil < 10mil) Between Pad P13-0(6388.779mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-31(3874.016mil,2381.89mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-32(3874.016mil,2401.575mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-33(3874.016mil,2421.26mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-34(3874.016mil,2440.945mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-35(3874.016mil,2460.63mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-36(3874.016mil,2480.315mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-37(3874.016mil,2500mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-38(3874.016mil,2519.685mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-39(3874.016mil,2539.37mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-40(3874.016mil,2559.055mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.053mil < 10mil) Between Pad P14-0(6598.425mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P14-0(6598.425mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P14-0(7307.087mil,4762.992mil) on Multi-Layer And Track

<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (8.4mil < 10mil) Between Pad P14-0(7307.087mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-41(3874.016mil,2578.74mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-42(3874.016mil,2598.425mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-43(3874.016mil,2618.11mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-44(3874.016mil,2637.795mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-45(3874.016mil,2657.48mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-46(3874.016mil,2677.165mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-47(3874.016mil,2696.85mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-48(3874.016mil,2716.535mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-49(3874.016mil,2736.22mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.401mil < 10mil) Between Pad P1-50(3874.016mil,2755.905mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad P1-50(3874.016mil,2755.905mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.376mil < 10mil) Between Pad P15-0(5844.095mil,2007.874mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P15-0(5844.095mil,2007.874mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (8.4mil < 10mil) Between Pad P9-0(1188.976mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (9.281mil < 10mil) Between Pad P9-0(1188.976mil,4762.992mil) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (1.969mil < 10mil) Between Pad Q1-1(1883.858mil,2261.504mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (1.969mil < 10mil) Between Pad Q1-3(1883.858mil,2312.685mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (1.969mil < 10mil) Between Pad Q1-4(1811.024mil,2312.685mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (4.415mil < 10mil) Between Pad Q1-5(1811.024mil,2287.095mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (4.415mil < 10mil) Between Pad Q1-5(1811.024mil,2287.095mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (1.969mil < 10mil) Between Pad Q1-6(1811.024mil,2261.504mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R10-2(2410.433mil,4813.307mil) on Top Layer And Text "R21"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R11-2(5433.258mil,4114.173mil) on Top Layer And Text "TP12"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R12-2(198.82mil,4890.748mil) on Top Layer And Text "R14"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R14-1(198.82mil,4762.992mil) on Top Layer And Text "TP10"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R14-2(259.844mil,4762.992mil) on Top Layer And Text "TP10"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R16-1(3205.118mil,3169.291mil) on Top Layer And Text "R17"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R16-2(3144.095mil,3169.291mil) on Top Layer And Text "R17"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R17-1(3205.118mil,3228.347mil) on Top Layer And Text "R16"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R17-2(3144.095mil,3228.347mil) on Top Layer And Text "R16"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R18-1(2549.675mil,3040.354mil) on Top Layer And Text "TP1"
Silk To Solder Mask Clearance Constraint: (7.018mil < 10mil) Between Pad R18-1(2549.675mil,3040.354mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.018mil < 10mil) Between Pad R18-2(2549.675mil,3101.378mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R22-2(7578.74mil,4792.323mil) on Top Layer And Text "R25"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R23-2(2779.901mil,4862.205mil) on Top Layer And Text "R27"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R24-1(7648.622mil,4153.543mil) on Top Layer And Text "C18"
Silk To Solder Mask Clearance Constraint: (2.582mil < 10mil) Between Pad R24-1(7648.622mil,4153.543mil) on Top Layer And Text "TP16"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R24-2(7587.599mil,4153.543mil) on Top Layer And Text "C18"
Silk To Solder Mask Clearance Constraint: (2.582mil < 10mil) Between Pad R24-2(7587.599mil,4153.543mil) on Top Layer And Text "TP16"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R25-1(7576.772mil,4665.354mil) on Top Layer And Text "U9"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R25-2(7637.795mil,4665.354mil) on Top Layer And Text "U9"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R27-1(2779.901mil,4724.409mil) on Top Layer And Text "TP17"
Silk To Solder Mask Clearance Constraint: (1.448mil < 10mil) Between Pad R27-2(2840.925mil,4724.409mil) on Top Layer And Text "TP17"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R28-1(1889.764mil,3829.724mil) on Top Layer And Text "U23"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R28-2(1889.764mil,3768.701mil) on Top Layer And Text "U23"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R29-1(1962.559mil,3829.724mil) on Top Layer And Text "U23"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R29-2(1962.559mil,3768.701mil) on Top Layer And Text "U23"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R3-1(1808.465mil,2580.905mil) on Top Layer And Text "R4"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R33-1(682.677mil,3346.457mil) on Top Layer And Text "R32"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R33-1(682.677mil,3346.457mil) on Top Layer And Text "TH_TP2"
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R33-2(621.654mil,3346.457mil) on Top Layer And Text "R32"

<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R33-2(621.654mil,3346.457mil) on Top Layer And Text "TH_TP2'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R34-2(4527.559mil,3777.559mil) on Top Layer And Text "U25'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R35-1(4606.299mil,3838.583mil) on Top Layer And Text "U25'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R35-2(4606.299mil,3777.559mil) on Top Layer And Text "U25'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R36-2(3153.347mil,3839.468mil) on Top Layer And Text "U26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R37-2(3228.347mil,3839.468mil) on Top Layer And Text "U26'
Silk To Solder Mask Clearance Constraint: (8.224mil < 10mil) Between Pad R38-1(7204.724mil,3849.409mil) on Top Layer And Text "U27'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R38-2(7204.724mil,3788.386mil) on Top Layer And Text "U27'
Silk To Solder Mask Clearance Constraint: (8.224mil < 10mil) Between Pad R39-1(7275.591mil,3849.409mil) on Top Layer And Text "U27'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R39-2(7275.591mil,3788.386mil) on Top Layer And Text "U27'
Silk To Solder Mask Clearance Constraint: (5.527mil < 10mil) Between Pad R5-2(5275.591mil,2705.709mil) on Top Layer And Text "LED3'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R6-2(4998.032mil,4851.378mil) on Top Layer And Text "R8'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R7-1(5062.992mil,4114.173mil) on Top Layer And Text "TP8'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R7-2(5001.969mil,4114.173mil) on Top Layer And Text "TP8'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R9-2(5363.189mil,4851.378mil) on Top Layer And Text "R15'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad R9-2(5363.189mil,4851.378mil) on Top Layer And Text "TP9'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP10-TP(196.85mil,4665.354mil) on Top Layer And Text "U5'
Silk To Solder Mask Clearance Constraint: (4.38mil < 10mil) Between Pad TP11-TP(196.85mil,4074.803mil) on Top Layer And Text "R42'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP17-TP(2677.165mil,4724.409mil) on Top Layer And Text "TP17'
Silk To Solder Mask Clearance Constraint: (4.065mil < 10mil) Between Pad TP1-TP(2549.675mil,2913.386mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (4.652mil < 10mil) Between Pad TP21-TP(787.402mil,4389.764mil) on Top Layer And Text "C24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP21-TP(787.402mil,4389.764mil) on Top Layer And Text "TP22'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP27-TP(3366.142mil,4370.079mil) on Top Layer And Text "TP28'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP31-TP(5944.882mil,4348.425mil) on Top Layer And Text "TP32'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad TP3-TP(1883.858mil,2677.165mil) on Top Layer And Text "R3'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U10-5(2850.394mil,4434.055mil) on Top Layer And Text "C19'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U11-1(2251.968mil,4232.283mil) on Top Layer And Text "TP20'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U11-10(2078.74mil,4232.283mil) on Top Layer And Text "TP20'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U11-2(2251.968mil,4251.968mil) on Top Layer And Text "TP20'
Silk To Solder Mask Clearance Constraint: (6.997mil < 10mil) Between Pad U11-6(2078.74mil,4311.024mil) on Top Layer And Text "TP19'
Silk To Solder Mask Clearance Constraint: (9.734mil < 10mil) Between Pad U1-2(278.307mil,3564.37mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (8.92mil < 10mil) Between Pad U1-2(278.307mil,3564.37mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.691mil < 10mil) Between Pad U1-3(278.307mil,3614.37mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (8.973mil < 10mil) Between Pad U1-3(278.307mil,3614.37mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U13-2(404.661mil,4311.024mil) on Top Layer And Text "C57'
Silk To Solder Mask Clearance Constraint: (2.44mil < 10mil) Between Pad U13-3(404.661mil,4291.339mil) on Top Layer And Text "C57'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U13-4(404.661mil,4271.654mil) on Top Layer And Text "C57'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U13-5(404.661mil,4251.968mil) on Top Layer And Text "C57'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U16-1(4870.079mil,4212.598mil) on Top Layer And Text "TP26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U16-10(4696.85mil,4212.598mil) on Top Layer And Text "TP25'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U16-10(4696.85mil,4212.598mil) on Top Layer And Text "TP26'
Silk To Solder Mask Clearance Constraint: (6.618mil < 10mil) Between Pad U16-2(4870.079mil,4232.283mil) on Top Layer And Text "TP26'
Silk To Solder Mask Clearance Constraint: (6.618mil < 10mil) Between Pad U16-9(4696.85mil,4232.283mil) on Top Layer And Text "TP25'
Silk To Solder Mask Clearance Constraint: (6.618mil < 10mil) Between Pad U16-9(4696.85mil,4232.283mil) on Top Layer And Text "TP26'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U17-1(848.622mil,3395.669mil) on Top Layer And Text "C28'
Silk To Solder Mask Clearance Constraint: (8.343mil < 10mil) Between Pad U17-1(848.622mil,3395.669mil) on Top Layer And Text "C48'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U17-2(898.622mil,3395.669mil) on Top Layer And Text "C28'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U17-2(898.622mil,3395.669mil) on Top Layer And Text "C48'
Silk To Solder Mask Clearance Constraint: (1.87mil < 10mil) Between Pad U17-3(948.622mil,3395.669mil) on Top Layer And Text "C28'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U17-4(998.622mil,3395.669mil) on Top Layer And Text "C28'
Silk To Solder Mask Clearance Constraint: (1.927mil < 10mil) Between Pad U1-8(89.331mil,3514.37mil) on Top Layer And Text "C7'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U18-1(1399.803mil,3395.669mil) on Top Layer And Text "C30'

<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U18-2(1449.803mil,3395.669mil) on Top Layer And Text "C30'
Silk To Solder Mask Clearance Constraint: (2.169mil < 10mil) Between Pad U18-3(1499.803mil,3395.669mil) on Top Layer And Text "C30'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U18-4(1549.803mil,3395.669mil) on Top Layer And Text "C30'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U19-1(3447.047mil,3395.669mil) on Top Layer And Text "C52'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U19-1(3447.047mil,3395.669mil) on Top Layer And Text "TP24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U20-1(4012.598mil,3395.669mil) on Top Layer And Text "C36'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U20-2(4062.598mil,3395.669mil) on Top Layer And Text "C36'
Silk To Solder Mask Clearance Constraint: (4.409mil < 10mil) Between Pad U20-3(4112.598mil,3395.669mil) on Top Layer And Text "C36'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U20-4(4162.598mil,3395.669mil) on Top Layer And Text "C36'
Silk To Solder Mask Clearance Constraint: (3.792mil < 10mil) Between Pad U2-1(3053.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U21-1(6047.638mil,3375.984mil) on Top Layer And Text "C32'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U21-2(6097.638mil,3375.984mil) on Top Layer And Text "C32'
Silk To Solder Mask Clearance Constraint: (2.563mil < 10mil) Between Pad U21-3(6147.638mil,3375.984mil) on Top Layer And Text "C32'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U21-4(6197.638mil,3375.984mil) on Top Layer And Text "C32'
Silk To Solder Mask Clearance Constraint: (3.808mil < 10mil) Between Pad U2-2(3103.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U22-1(6596.654mil,3375.984mil) on Top Layer And Text "C34'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U22-2(6646.654mil,3375.984mil) on Top Layer And Text "C34'
Silk To Solder Mask Clearance Constraint: (1.625mil < 10mil) Between Pad U22-3(6696.654mil,3375.984mil) on Top Layer And Text "C34'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U22-4(6746.654mil,3375.984mil) on Top Layer And Text "C34'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U23-8(2004.134mil,3475.394mil) on Top Layer And Text "C46'
Silk To Solder Mask Clearance Constraint: (8.676mil < 10mil) Between Pad U2-4(3203.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U24-4(602.165mil,3689.961mil) on Top Layer And Text "R13'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U24-6(652.165mil,3475.394mil) on Top Layer And Text "R33'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U24-7(702.165mil,3475.394mil) on Top Layer And Text "R33'
Silk To Solder Mask Clearance Constraint: (3.792mil < 10mil) Between Pad U2-5(3253.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U25-6(4541.929mil,3475.394mil) on Top Layer And Text "C50'
Silk To Solder Mask Clearance Constraint: (1.886mil < 10mil) Between Pad U25-7(4591.929mil,3475.394mil) on Top Layer And Text "C50'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U2-6(3303.347mil,2353.346mil) on Top Layer And Text "C6'
Silk To Solder Mask Clearance Constraint: (3.792mil < 10mil) Between Pad U2-6(3303.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (8.752mil < 10mil) Between Pad U26-6(3203.347mil,3475.394mil) on Top Layer And Text "TP24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U26-7(3253.347mil,3475.394mil) on Top Layer And Text "TP24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U26-8(3303.347mil,3475.394mil) on Top Layer And Text "C52'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U26-8(3303.347mil,3475.394mil) on Top Layer And Text "TP24'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U2-7(3353.347mil,2353.346mil) on Top Layer And Text "C6'
Silk To Solder Mask Clearance Constraint: (3.813mil < 10mil) Between Pad U2-7(3353.347mil,2353.346mil) on Top Layer And Text "TH_TP1'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U27-1(7315.158mil,3689.961mil) on Top Layer And Text "R39'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U27-3(7215.158mil,3689.961mil) on Top Layer And Text "R39'
Silk To Solder Mask Clearance Constraint: (8.565mil < 10mil) Between Pad U27-4(7165.158mil,3689.961mil) on Top Layer And Text "R39'
Silk To Solder Mask Clearance Constraint: (Collision < 10mil) Between Pad U28-8(5887.402mil,3486.22mil) on Top Layer And Text "C56'
Silk To Solder Mask Clearance Constraint: (6.89mil < 10mil) Between Pad U3-1(2877.953mil,3208.661mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.583mil < 10mil) Between Pad U3-1(2877.953mil,3208.661mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.827mil < 10mil) Between Pad U3-10(3051.181mil,3208.661mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-10(3051.181mil,3208.661mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.193mil < 10mil) Between Pad U3-2(2877.953mil,3188.976mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.193mil < 10mil) Between Pad U3-3(2877.953mil,3169.291mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.193mil < 10mil) Between Pad U3-4(2877.953mil,3149.606mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.193mil < 10mil) Between Pad U3-5(2877.953mil,3129.921mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.827mil < 10mil) Between Pad U3-5(2877.953mil,3129.921mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-6(3051.181mil,3129.921mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-6(3051.181mil,3129.921mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-7(3051.181mil,3149.606mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-8(3051.181mil,3169.291mil) on Top Layer And Track



<b>Silk To Solder Mask (Clearance=10mil) (IsPad),(All)</b>
Silk To Solder Mask Clearance Constraint: (9.192mil < 10mil) Between Pad U3-9(3051.181mil,3188.976mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.843mil < 10mil) Between Pad U4-2(2992.126mil,3343.504mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (9.843mil < 10mil) Between Pad U4-3(2935.433mil,3343.504mil) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (7.317mil < 10mil) Between Pad U8-4(2409.646mil,4434.055mil) on Top Layer And Text "C17"
Silk To Solder Mask Clearance Constraint: (7.878mil < 10mil) Between Pad U8-5(2484.449mil,4434.055mil) on Top Layer And Text "C15"
Silk To Solder Mask Clearance Constraint: (6.207mil < 10mil) Between Pad U8-5(2484.449mil,4434.055mil) on Top Layer And Text "C17"
Silk To Solder Mask Clearance Constraint: (9.221mil < 10mil) Between Pad U9-5(7653.543mil,4429.134mil) on Top Layer And Text "C20"

<b>Silk to Silk (Clearance=10mil) (All),(All)</b>
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (1366.339mil,3395.669mil) on Top Overlay And Text "C30" (1379.716mil,3351.732mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (2190.354mil,4231.654mil) on Top Overlay And Text "TP20" (2065.24mil,4186.965mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (2220.354mil,4196.654mil) on Top Overlay And Text "TP20" (2065.24mil,4186.965mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (3413.583mil,3395.669mil) on Top Overlay And Text "TP24" (3214.847mil,3397.004mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (3454.724mil,3488.189mil) on Top Overlay And Text "C52" (3283.118mil,3430.732mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (4808.465mil,4211.968mil) on Top Overlay And Text "TP26" (4695.161mil,4153.894mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (4838.465mil,4176.968mil) on Top Overlay And Text "TP26" (4695.161mil,4153.894mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (6014.173mil,3375.984mil) on Top Overlay And Text "C32" (6027.622mil,3338.882mil)
Silk To Silk Clearance Constraint: (1.686mil < 10mil) Between Arc (6055.315mil,3468.504mil) on Top Overlay And Text "C56" (5867.236mil,3469.787mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (6563.189mil,3375.984mil) on Top Overlay And Text "C34" (6575.866mil,3331.787mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (7348.622mil,3689.961mil) on Top Overlay And Text "R39" (7336.51mil,3730.423mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (7417.205mil,4196.654mil) on Top Overlay And Text "TP30" (7329.02mil,4147.988mil)
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (815.157mil,3395.669mil) on Top Overlay And Text "C28" (828.236mil,3351.732mil) or
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Arc (856.299mil,3488.189mil) on Top Overlay And Text "C48" (737.472mil,3430.102mil) or
Silk To Silk Clearance Constraint: (7.806mil < 10mil) Between Text "1" (5543.307mil,2129.921mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (1496.063mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (3173.229mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (4094.488mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (574.804mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (5774.607mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "5" (6692.914mil,4824.409mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.795mil < 10mil) Between Text "8" (5905.512mil,2488.189mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C1" (1924.646mil,3091.262mil) on Top Overlay And Text "R19"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C10" (3079.289mil,3406.323mil) on Top Overlay And Text "U4"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C12" (5058.173mil,4315.937mil) on Top Overlay And Text "C13"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C12" (5058.173mil,4315.937mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C14" (5421.094mil,4309.119mil) on Top Overlay And Text "C16"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C14" (5421.094mil,4309.119mil) on Top Overlay And Text "U15"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C15" (2464.236mil,4342.102mil) on Top Overlay And Text "C17"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C15" (2464.236mil,4342.102mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C18" (7716.511mil,4230.935mil) on Top Overlay And Text "R24"
Silk To Silk Clearance Constraint: (7.611mil < 10mil) Between Text "C18" (7716.511mil,4230.935mil) on Top Overlay And Text "TP30"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C19" (2829.724mil,4356.252mil) on Top Overlay And Text "U12"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C2" (2058.472mil,2713.866mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C21" (2835.029mil,4328.025mil) on Top Overlay And Text "R26"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C22" (1982.803mil,4431.717mil) on Top Overlay And Text "U11"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C24" (648.661mil,4304.921mil) on Top Overlay And Text "TP22"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C25" (7164.236mil,4431.575mil) on Top Overlay And Text "TP15"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C25" (7164.236mil,4431.575mil) on Top Overlay And Text "U14"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C27" (4590.236mil,4412.102mil) on Top Overlay And Text "U16"
Silk To Silk Clearance Constraint: (9.019mil < 10mil) Between Text "C29" (828.236mil,3982.102mil) on Top Overlay And Text "C47"
Silk To Silk Clearance Constraint: (8.389mil < 10mil) Between Text "C3" (3721.236mil,2717.677mil) on Top Overlay And Text "C4"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C31" (1330.508mil,3814.865mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (5.836mil < 10mil) Between Text "C33" (6027.622mil,3962.102mil) on Top Overlay And Text "C55"
Silk To Silk Clearance Constraint: (9.019mil < 10mil) Between Text "C39" (6019.236mil,2482.213mil) on Top Overlay And Text "C40"
Silk To Silk Clearance Constraint: (9.019mil < 10mil) Between Text "C40" (6019.236mil,2403.213mil) on Top Overlay And Text "C41"
Silk To Silk Clearance Constraint: (9.019mil < 10mil) Between Text "C41" (6019.236mil,2324.213mil) on Top Overlay And Text "C42"
Silk To Silk Clearance Constraint: (8.019mil < 10mil) Between Text "C42" (6019.236mil,2245.213mil) on Top Overlay And Text "C43"
Silk To Silk Clearance Constraint: (9.019mil < 10mil) Between Text "C43" (6019.236mil,2167.213mil) on Top Overlay And Text "C44"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C45" (2022.425mil,3825.386mil) on Top Overlay And Text "R28"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C45" (2022.425mil,3825.386mil) on Top Overlay And Text "R29"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C46" (1987.236mil,3430.102mil) on Top Overlay And Text "TP23"

<b>Silk to Silk (Clearance=10mil) (All),(All)</b>
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C47" (731.236mil,3903.102mil) on Top Overlay And Text "U17'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C48" (737.472mil,3430.102mil) on Top Overlay And Text "R33'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C48" (737.472mil,3430.102mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C48" (737.472mil,3430.102mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.109mil < 10mil) Between Text "C49" (4673.661mil,3837.764mil) on Top Overlay And Text "R34'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C49" (4673.661mil,3837.764mil) on Top Overlay And Text "R35'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C5" (3030.236mil,2754.842mil) on Top Overlay And Text "TP6'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C51" (3083.369mil,4025.128mil) on Top Overlay And Text "R36'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C51" (3083.369mil,4025.128mil) on Top Overlay And Text "R37'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C52" (3283.118mil,3430.732mil) on Top Overlay And Text "TP24'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C52" (3283.118mil,3430.732mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C52" (3283.118mil,3430.732mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (8.089mil < 10mil) Between Text "C53" (7337.921mil,3833.528mil) on Top Overlay And Text "R38'
Silk To Silk Clearance Constraint: (0.24mil < 10mil) Between Text "C54" (7298.496mil,3450.528mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (4.691mil < 10mil) Between Text "C54" (7298.496mil,3450.528mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C55" (5867.126mil,3890.307mil) on Top Overlay And Text "U21'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C56" (5867.236mil,3469.787mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (1.206mil < 10mil) Between Text "C56" (5867.236mil,3469.787mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C56" (5867.236mil,3469.787mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C6" (3282.976mil,2340.584mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C7" (70.027mil,3485.63mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (7.369mil < 10mil) Between Text "C7" (70.027mil,3485.63mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "C9" (2843.48mil,3427.654mil) on Top Overlay And Text "U4"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "D1" (2145.224mil,2558.595mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "LED2" (1809.826mil,2891.246mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (1.582mil < 10mil) Between Text "P1" (3893.835mil,2887.331mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R13" (460.441mil,3706.102mil) on Top Overlay And Text "U24'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R15" (5345.89mil,4783.102mil) on Top Overlay And Text "TP9'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R18" (2658.406mil,2994.525mil) on Top Overlay And Text "TP1'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R18" (2658.406mil,2994.525mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R28" (1871.236mil,3886.929mil) on Top Overlay And Text "R29'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R30" (602.703mil,3998.552mil) on Top Overlay And Text "R31'
Silk To Silk Clearance Constraint: (8.39mil < 10mil) Between Text "R32" (604.591mil,3326.142mil) on Top Overlay And Text "R33'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R32" (604.591mil,3326.142mil) on Top Overlay And Text "TH_TP2'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R33" (603.961mil,3404.512mil) on Top Overlay And Text "TH_TP2'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R34" (4509.063mil,3896.591mil) on Top Overlay And Text "R35'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R36" (3049.947mil,3987.125mil) on Top Overlay And Text "R37'
Silk To Silk Clearance Constraint: (7.119mil < 10mil) Between Text "R36" (3049.947mil,3987.125mil) on Top Overlay And Text "TP18'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R40" (5729.543mil,3985.472mil) on Top Overlay And Text "R41'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "R42" (157.034mil,4103.868mil) on Top Overlay And Text "TP11'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP1" (2536.175mil,2966.886mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP13" (2269.965mil,4595.823mil) on Top Overlay And Text "U8'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP14" (2396.146mil,4029.878mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP15" (7250.349mil,4407.018mil) on Top Overlay And Text "U14'
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP17" (2631.255mil,4655.049mil) on Top Overlay And Text "U10'
Silk To Silk Clearance Constraint: (2.829mil < 10mil) Between Text "TP17" (2631.255mil,4655.049mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP19" (1840.634mil,4325.154mil) on Top Overlay And Text "U11'
Silk To Silk Clearance Constraint: (5.461mil < 10mil) Between Text "TP2" (2330.988mil,2238.539mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP20" (2065.24mil,4186.965mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP20" (2065.24mil,4186.965mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP20" (2065.24mil,4186.965mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (8.76mil < 10mil) Between Text "TP21" (773.902mil,4443.264mil) on Top Overlay And Text "TP22'

<b>Silk to Silk (Clearance=10mil) (All),(All)</b>
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP24" (3214.847mil,3397.004mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP24" (3214.847mil,3397.004mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP25" (4533.744mil,4153.894mil) on Top Overlay And Text "TP26"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP25" (4533.744mil,4153.894mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP25" (4533.744mil,4153.894mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP26" (4695.161mil,4153.894mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP26" (4695.161mil,4153.894mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP26" (4695.161mil,4153.894mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (5.876mil < 10mil) Between Text "TP27" (3315.232mil,4528.081mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP29" (7130.193mil,4143.238mil) on Top Overlay And Text "TP30"
Silk To Silk Clearance Constraint: (0.524mil < 10mil) Between Text "TP29" (7130.193mil,4143.238mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.443mil < 10mil) Between Text "TP29" (7130.193mil,4143.238mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP3" (1870.358mil,2730.665mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP3" (1870.358mil,2730.665mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (2.194mil < 10mil) Between Text "TP30" (7329.02mil,4147.988mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP30" (7329.02mil,4147.988mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP30" (7329.02mil,4147.988mil) on Top Overlay And Track
Silk To Silk Clearance Constraint: (6.791mil < 10mil) Between Text "TP31" (5931.382mil,4401.925mil) on Top Overlay And Text "TP32"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP6" (2880.201mil,2697.201mil) on Top Overlay And Text "U2"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "TP7" (4848.705mil,4595.823mil) on Top Overlay And Text "U6"
Silk To Silk Clearance Constraint: (Collision < 10mil) Between Text "U1" (44.236mil,3730.102mil) on Top Overlay And Track