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C20 TP18 R25 TP17 R24 TP16 TP15 C19 R22C18 C16 TP14 R15 TP13 R11 TP12 TP9 R9 C14 C13 TP11 R8 R7 TP8 TP7

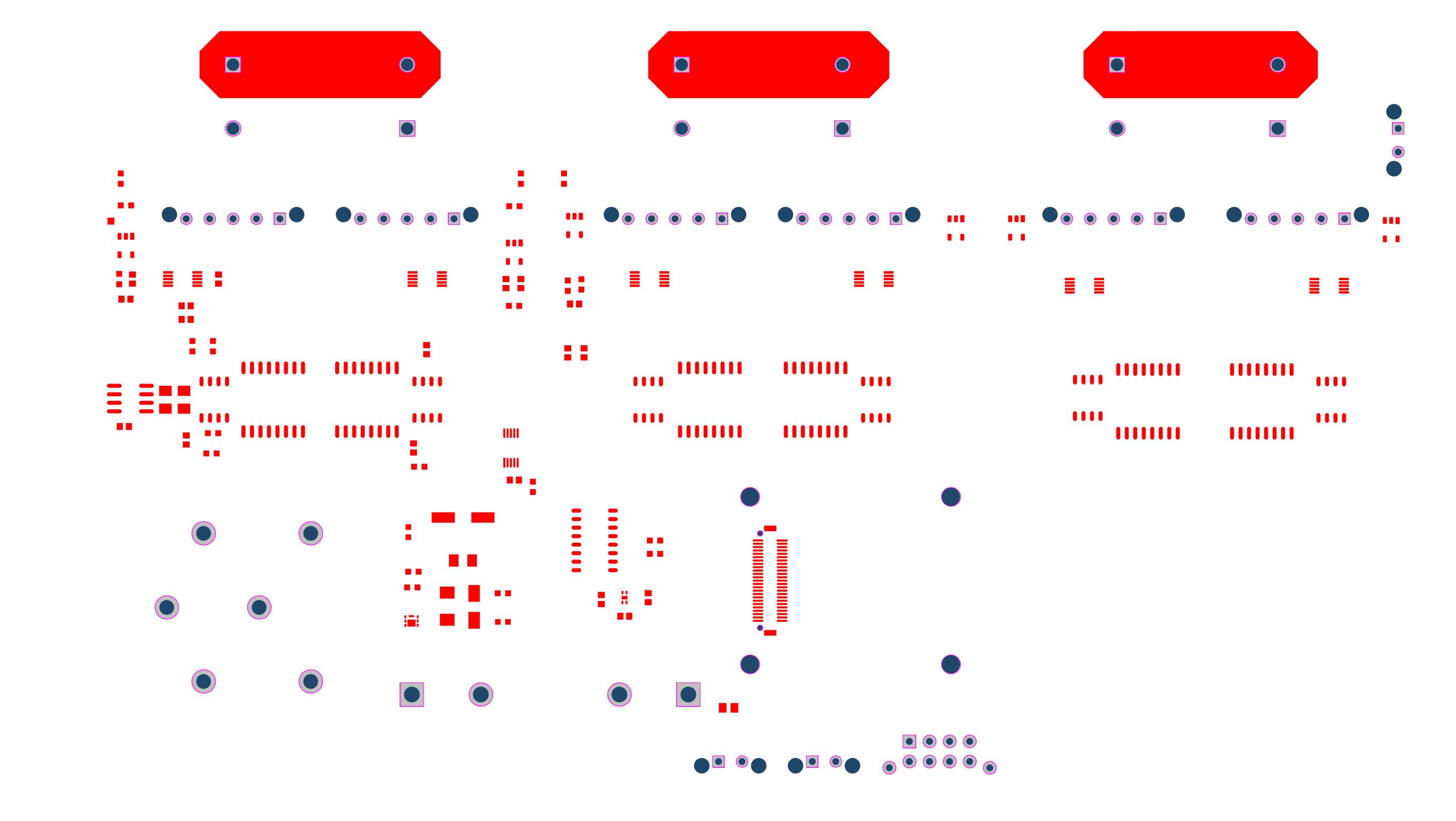
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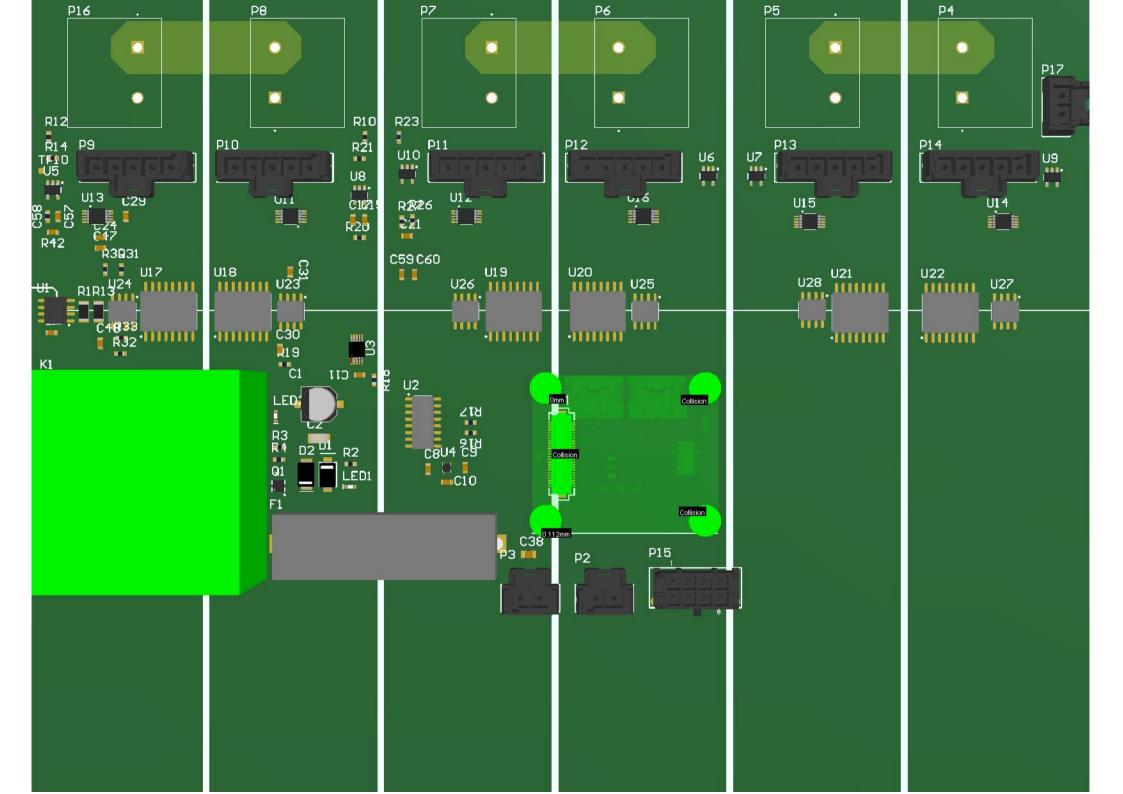
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Electrical Rules Check Report

Class	Document	Message
Error	Voltage_Sense.SchDoc	HV_LOW contains Power Pin and Output Port objects (Pin U5-2, Port HV_LOW).
Error	TemperatureSense.SchDoc	Net 3.3V has only one pin (Pin P15-1)
Error	ADCs.SchDoc	Net ADC_SCL_4 has only one pin (Pin U16-8)
Error	ADCs.SchDoc	Net ADC_SCL_5 has only one pin (Pin U15-8)
Error	ADCs.SchDoc	Net ADC_SCL_6 has only one pin (Pin U14-8)
Error	ADCs.SchDoc	Net ADC_SDA_4 has only one pin (Pin U16-7)
Error	ADCs.SchDoc	Net ADC_SDA_5 has only one pin (Pin U15-7)
Error	ADCs.SchDoc	Net ADC_SDA_6 has only one pin (Pin U14-7)
Error	Controller_Board_Interface.SchDoc Controller_Board_Interface.SchDoc	Net PA6/Relay_Sense has only one pin (Pin P1-19) Net PA7_ has only one pin (Pin P1-18)
Error Error	Controller_Board_Interface.SchDoc	Net PA9/MODE_SEL_0 has only one pin (Pin P1-6)
Error	Controller_Board_Interface.SchDoc	Net PA10/MODE_SEL_1 has only one pin (Pin P1-5)
Error	Controller_Board_Interface.SchDoc	Net PBO_ has only one pin (Pin P1-17)
Error	Controller_Board_Interface.SchDoc	Net PB1_ has only one pin (Pin P1-16)
Error	Controller_Board_Interface.SchDoc	Net PB2_ has only one pin (Pin P1-15)
Warning	Voltage_Sense.SchDoc	Global Power-Object MPPT_GND_1 at 3650mil,1830mil has been reduced to local level by
	3 =	presence of port at 2000mil,1300mil
Warning	Voltage_Sense.SchDoc	Global Power-Object MPPT_HV_6 at 3350mil,9100mil has been reduced to local level by
\\\ / = !	V-H C C D	presence of port at 2000mil,9400mil
Warning	Voltage_Sense.SchDoc	Net NetR6_2 has no driving source (Pin R6-2, Pin R8-1, Pin TP7-TP, Pin U6-3)
Warning Warning	Voltage_Sense.SchDoc Voltage_Sense.SchDoc	Net NetR9_2 has no driving source (Pin R9-2, Pin R15-1, Pin TP9-TP, Pin U7-3) Net NetR10_2 has no driving source (Pin R10-2, Pin R21-1, Pin TP13-TP, Pin U8-3)
Warning	Voltage_Sense.SchDoc	Net NetR12_2 has no driving source (Pin R12-2, Pin R14-1, Pin TP10-TP, Pin U5-3)
Warning	Voltage_Sense.SchDoc	Net NetR22_2 has no driving source (Pin R22-2, Pin R14-1, Pin TP15-TP, Pin U9-3)
Warning	Voltage_Sense.SchDoc	Net NetR23_2 has no driving source (Pin R23-2, Pin R23-1, Pin TP17-TP, Pin U10-3)
Warning	Controller Board Interface.SchDoc	Net PA15/ADC_INTERRUPT has no driving source (Pin P1-39, Pin U3-2)
Warning	Controller_Board_Interface.SchDoc	Net PB3_DEMUX_A0 has no driving source (Pin P1-38, Pin U2-1)
Warning	Controller_Board_Interface.SchDoc	Net PB4_DEMUX_A1 has no driving source (Pin P1-37, Pin U2-2)
Warning	Controller_Board_Interface.SchDoc	Net PB5_DEMUX_A2 has no driving source (Pin P1-36, Pin U2-3)
Warning	Current_and_Relay_Sense.SchDoc	Nets Wire HV_HIGH has multiple names (Net Label HV_HIGH, Power Object MPPT_HV_6,
Morning	ADCs.SchDoc	Power Object MPPT HV 6) Note Wise IIV LOW has multiple names (Not Label IIV LOW Not Label IIV LOW Days)
Warning	ADCS.SCHDOC	Nets Wire HV_LOW has multiple names (Net Label HV_LOW, Net Label HV_LOW, Power
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		Object MPPT_GND_1, Power Object MPPT_GND_1, Power Object MPPT_GND_1, Power
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Warning	ADCs.SchDoc	Neis WHENT-POND PRIES Maling HARTES (PHONE) Object MPPT_GND_2, Power Object
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Warning	7.603.3611600	MPPT_GND_4, Power Object MPPT_GND_4, Power Object MPPT_GND_4, Power Object
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Frida: 21 1 0	0.20 0.20.10 DN	MPPT_GND_5, Power Object MPPT_HV_4, Power Object MPPT_HV_4)
Friday 31 Jan 20	020 8:28:10 PN,	MPPT_GND_5, Power Object MPPT_HV_4, Power Object MPPT_HV_4) Page Page

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Warning	warning	Controller_board_interface.Scribbe	·
Warning	Warning	Controller_Board_Interface.SchDoc	Nets Wire PB10/I2C2_SCL has multiple names (Net Label PB10/I2C2_SCL, Net Label SCL)
Net Label SDA, Net Naming ADCs, SchDoc Off. grid Cross. Sheel Connector ADC_SCL_1 at 3600mil, 7694.41mil Marning ADCs, SchDoc Off. grid Cross. Sheel Connector ADC_SCL_1 at 3600mil, 7694.41mil Marning ADCs, SchDoc Off. grid Cross. Sheel Connector ADC_SDA, 1 at 3600mil, 7694.41mil Marning SPL Interface. SchDoc Off. grid Cross. Sheel Connector ADC_SDA, 1 at 3600mil, 7694.41mil Mid651.811mil Marning SPL Interface. SchDoc Off. grid Cross. Sheel Connector PA6. SPL2. MISO at 6574.41mil.6551.811mil SPL Interface. SchDoc Off. grid Cross. Sheel Connector PA6. SPL2. MISO at 6574.41mil.6521.811mil SPL Interface. SchDoc Off. grid Cross. Sheel Connector Solar_Temp_2 at 6687.008mil.4796.063mil Temperature. Sense SchDoc Off. grid Cross. Sheel Connector Solar_Temp_4 at 6687.008mil.4796.063mil Warning Temperature. Sense SchDoc Off. grid Cross. Sheel Connector Solar_Temp_6 at 2747.008mil.4309.063mil Warning Temperature. Sense SchDoc Off. grid Cross. Sheel Connector Solar_Temp_6 at 2747.008mil.4309.063mil Warning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_1 at 135044.41mil Marning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_1 at 13504.441mil. Shomil. Shomil Marning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_2 at 16318.89mil.3100mil Warning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_3 at 13138.89mil.3300mil Warning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_3 at 13138.89mil.3300mil Warning ADCs. SchDoc Off. grid Cross. Sheel Connector V. SENSE_4 at 16318.39mil.3300mil Warning SPL Interface. SchDoc Off. grid Cross. Sheel Connector V. SENSE_4 at 16318.39mil.3300mil Warning SPL Interface. SchDoc Off. grid Post. Sheel Connector V. SENSE_4 at 16318.39mil.3300mil Warning SPL Interface. SchDoc Off. grid Post. Sheel Connector V. SENSE_4 at 16318.30mil.300mil Warning SPL Interface. SchDoc Off. grid Post. Sheel Connector V. SENSE_4 at 16318.30mil.30	Ů		Net Label SCL, Net Label SCL, Net Label SCL, Net Label SCL, Net Label SCL)
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Warning ADCs SchDoc			
Warning ADCs SchDoc Off grid Cross-Sheet Connector ADC, SDA, 1 at 3600mil, 7544.41mil			
Warning SPL.Interface.SchDoc Off.grid Cross-Sheet Connector PAG_SPI2_MISO at 6574.41mil.6651.811mil Warning SPL.Interface.SchDoc Off.grid Cross-Sheet Connector PAG_SPI2_MISO at 6574.41mil.6551.811mil Warning TemperatureSense.SchDoc Off.grid Cross-Sheet Connector Solar_Temp_2 at 6687.08mil.1459.063mil Warning TemperatureSense.SchDoc Off.grid Cross-Sheet Connector Solar_Temp_2 at 6687.08mil.1459.063mil Warning Controller_Board_Interface.SchDoc Off.grid Cross-Sheet Connector Solar_Temp_6 at 2247.008mil.1459.063mil Warning Controller_Board_Interface.SchDoc Off.grid Cross-Sheet Connector Solar_Temp_6 at 2247.008mil.1459.063mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector Solar_Temp_6 at 247.008mil.1459.063mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_1 at 1350mil.1459.063mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_2 at 1338.9mil.8100mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_2 at 1338.9mil.8100mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_2 at 1338.9mil.3500mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_2 at 1338.9mil.3500mil Warning ADCs.SchDoc Off.grid Cross-Sheet Connector V.SchSE_3 At 1338.9mil.3500mil Warning ADCs.SchDoc Off.grid Met Label CS.MPPT.2 at 8674.41mil.6451.811mil Warning SPL.Interface.SchDoc Off.grid Met Label (MSC.MPPT.2 at 8674.41mil.6451.811mil Warning SPL.Interface.SchDoc Off.grid Met Label (MSC.MPPT.2 at 8674.41mil.6551.811mil Warning SPL.Interface.SchDoc Off.grid Met Label MSC.MPPT.2 at 8674.41mil.6551.811mil Warning SPL.Interface.SchDoc Off.grid Met Label Sch.MPPT.2 at 8674.41mil.6551.811mil Warning SPL.Interface.SchDoc Off.grid Met Label Sch.MPPT.2 at 8674.41mil.6551.811mil Warning SPL.Interface.SchDoc Off.grid Met Label Sch.MPPT.2 at 8674.41mil.6551.811mil Warning TemperatureSense.SchDoc Off.grid Met Label Sch.Mil.13996.063mil TemperatureSense.SchDoc Off.grid Met Labe			
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The second of th	Warning	ADCs.SchDoc	Off grid Pin U13-9 at 1658.11mil,7194.41mil

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Class	Document	Message
Warning	ADCs.SchDoc	Off grid Pin U13-10 at 1658.11mil,7344.41mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-1 at 6774.41mil,7051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-2 at 6774.41mil,5851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-3 at 6774.41mil,6851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-4 at 6774.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-5 at 6774.41mil,6451.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-6 at 6774.41mil,6251.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-7 at 6774.41mil,6051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-8 at 6774.41mil,5651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-9 at 8574.41mil,5651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-10 at 8574.41mil,6051.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-11 at 8574.41mil,6251.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-12 at 8574.41mil,6451.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-13 at 8574.41mil,6651.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-14 at 8574.41mil,6851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-15 at 8574.41mil,5851.811mil
Warning	SPI_Interface.SchDoc	Off grid Pin U18-16 at 8574.41mil,7051.811mil
Warning	TemperatureSense.SchDoc	Off grid Power Object 3.3V at 5899.606mil,5389.764mil
Warning	SPI_Interface.SchDoc	Off grid Power Object 3V3 at 6174.41mil,7351.811mil
Warning	SPI_Interface.SchDoc	Off grid Power Object GND at 6774.41mil,5351.811mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_3V3_1 at 3218.11mil,8674.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_3V3_1 at 3818.11mil,8674.41mil
Warning	SPI_Interface.SchDoc	Off grid Power Object MPPT_3V3_3 at 9074.41mil,7351.811mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 818.11mil,7074.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 3468.11mil,7074.41mil
Warning	ADCs.SchDoc	Off grid Power Object MPPT_GND_1 at 3818.11mil,8374.41mil
Warning	SPI_Interface.SchDoc	Off grid Power Object MPPT_GND_2 at 8574.41mil,5351.811mil
Warning	ADCs.SchDoc	Off grid U13 at 1958.11mil,8194.41mil
Warning	SPI_Interface.SchDoc	Off grid U18 at 7474.41mil,6651.811mil

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Design Rules Verification ReportFilename : C:\Users\Aashmika Mali\Documents\First Year\Midnight Sun\hardware\MSXIV_

Warnings 0 Rule Violations 500

Warnings Total 0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All),(All)	80
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	231
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	4
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	29
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	127
Silk to Silk (Clearance=0.254mm) (All),(All)	28
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	1
Total	500

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Clearance Constraint (Gap=0.254mm) (AII),(AII)
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-10(102mm,65.5mm) on Top Layer And Pad P1-11(102mm,65mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-10(102mm,65.5mm) on Top Layer And Pad P1-9(102mm,66mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-11(102mm,65mm) on Top Layer And Pad P1-12(102mm,64.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-12(102mm,64.5mm) on Top Layer And Pad P1-13(102mm,64mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-13(102mm,64mm) on Top Layer And Pad P1-14(102mm,63.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-14(102mm,63.5mm) on Top Layer And Pad P1-15(102mm,63mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-15(102mm,63mm) on Top Layer And Pad P1-16(102mm,62.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-16(102mm,62.5mm) on Top Layer And Pad P1-17(102mm,62mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-17(102mm,62mm) on Top Layer And Pad P1-18(102mm,61.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-18(102mm,61.5mm) on Top Layer And Pad P1-19(102mm,61mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-19(102mm,61mm) on Top Layer And Pad P1-20(102mm,60.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-20(102mm,60.5mm) on Top Layer And Pad P1-21(102mm,60mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-21(102mm,60mm) on Top Layer And Pad P1-22(102mm,59.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-22(102mm,59.5mm) on Top Layer And Pad P1-23(102mm,59mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-23(102mm,59mm) on Top Layer And Pad P1-24(102mm,58.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-24(102mm,58.5mm) on Top Layer And Pad P1-25(102mm,58mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-29(98.4mm,59.5mm) on Top Layer And Pad P1-30(98.4mm,60mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-30(98.4mm,60mm) on Top Layer And Pad P1-31(98.4mm,60.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-31(98.4mm,60.5mm) on Top Layer And Pad P1-32(98.4mm,61mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-32(98.4mm,61mm) on Top Layer And Pad P1-33(98.4mm,61.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-33(98.4mm,61.5mm) on Top Layer And Pad P1-34(98.4mm,62mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-34(98.4mm,62mm) on Top Layer And Pad P1-35(98.4mm,62.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-35(98.4mm,62.5mm) on Top Layer And Pad P1-36(98.4mm,63mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-36(98.4mm,63mm) on Top Layer And Pad P1-37(98.4mm,63.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-37(98.4mm,63.5mm) on Top Layer And Pad P1-38(98.4mm,64mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-38(98.4mm,64mm) on Top Layer And Pad P1-39(98.4mm,64.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-39(98.4mm,64.5mm) on Top Layer And Pad P1-40(98.4mm,65mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-4(102mm,68.5mm) on Top Layer And Pad P1-5(102mm,68mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-45(98.4mm,67.5mm) on Top Layer And Pad P1-46(98.4mm,68mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-46(98.4mm,68mm) on Top Layer And Pad P1-47(98.4mm,68.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-5(102mm,68mm) on Top Layer And Pad P1-6(102mm,67.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-6(102mm,67.5mm) on Top Layer And Pad P1-7(102mm,67mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-7(102mm,67mm) on Top Layer And Pad P1-8(102mm,66.5mm) on Top Layer
Clearance Constraint: (0.2mm < 0.254mm) Between Pad P1-8(102mm,66.5mm) on Top Layer And Pad P1-9(102mm,66mm) on Top Layer
Clearance Constraint: (0.25mm < 0.254mm) Between Pad Q1-2(47.571mm,57.96mm) on Top Layer And Pad Q1-3(47.571mm,58.61mm) on Top Layer
Clearance Constraint: (0.25mm < 0.254mm) Between Pad Q1-4(45.721mm,58.61mm) on Top Layer And Pad Q1-5(45.721mm,57.96mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-1(51.2mm,108mm) on Top Layer And Pad U11-2(51.2mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-10(46.8mm,108mm) on Top Layer And Pad U11-9(46.8mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-3(51.2mm,109mm) on Top Layer And Pad U11-4(51.2mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-6(46.8mm,110mm) on Top Layer And Pad U11-7(46.8mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-7(46.8mm,109.5mm) on Top Layer And Pad U11-8(46.8mm,109mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U11-8(46.8mm,109mm) on Top Layer And Pad U11-9(46.8mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-1(80mm,110mm) on Top Layer And Pad U12-2(80mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-10(84.4mm,110mm) on Top Layer And Pad U12-9(84.4mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-3(80mm,109mm) on Top Layer And Pad U12-4(80mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-6(84.4mm,108mm) on Top Layer And Pad U12-7(84.4mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-7(84.4mm,108.5mm) on Top Layer And Pad U12-8(84.4mm,109mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U12-8(84.4mm,109mm) on Top Layer And Pad U12-9(84.4mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U13-1(10.278mm,110mm) on Top Layer And Pad U13-2(10.278mm,109.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U13-3(10.278mm,109mm) on Top Layer And Pad U13-4(10.278mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U13-6(14.678mm,108mm) on Top Layer And Pad U13-7(14.678mm,108.5mm) on Top Layer
Clearance Constraint: (0.155mm < 0.254mm) Between Pad U13-7(14.678mm,108.5mm) on Top Layer And Pad U13-8(14.678mm,109mm) on Top Layer
Organistic Constraint, (c. 100mm) > 0.204mm) Detricent au O 10-7(14.070mm, 100.0mm) on 1 op Layer And Fau O 10-0(14.070mm, 104mm) on 1 op Layer

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Clearance Constraint (Gap=0.254mm) (All),(All) Clearance Constraint: (0.155mm < 0.254mm) Between Pad U13-8(14.678mm,109mm) on Top Layer And Pad U13-9(14.678mm,109.5mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U14-1(181.486mm,109mm) on Top Layer And Pad U14-2(181.486mm,108.5mm) on Top Cayerance Constraint: (0.155mm < 0.254mm) Between Pad U14-10(185.886mm,109mm) on Top Layer And Pad U14-9(185.886mm,108.5mm) on Top Clearance Constraint: (0.155mm < 0.254mm) Between Pad U14-3(181.486mm,108mm) on Top Layer And Pad U14-4(181.486mm,107.5mm) on Top CRY@Fance Constraint: (0.155mm < 0.254mm) Between Pad U14-6(185.886mm,107mm) on Top Layer And Pad U14-7(185.886mm,107.5mm) on Top Cayerance Constraint: (0.155mm < 0.254mm) Between Pad U14-7(185.886mm,107.5mm) on Top Layer And Pad U14-8(185.886mm,108mm) on Top Cayerance Constraint: (0.155mm < 0.254mm) Between Pad U14-8(185.886mm,108mm) on Top Layer And Pad U14-9(185.886mm,108.5mm) on Top Cayarance Constraint: (0.155mm < 0.254mm) Between Pad U15-1(144.938mm,109mm) on Top Layer And Pad U15-2(144.938mm,108.5mm) on Top CAY@rance Constraint: (0.155mm < 0.254mm) Between Pad U15-3(144.938mm,108mm) on Top Layer And Pad U15-4(144.938mm,107.5mm) on Top Cayerance Constraint: (0.155mm < 0.254mm) Between Pad U15-6(149.338mm,107mm) on Top Layer And Pad U15-7(149.338mm,107.5mm) on Top Cayarance Constraint: (0.155mm < 0.254mm) Between Pad U15-7(149.338mm,107.5mm) on Top Layer And Pad U15-8(149.338mm,108mm) on Top Cayerance Constraint: (0.155mm < 0.254mm) Between Pad U15-8(149.338mm,108mm) on Top Layer And Pad U15-9(149.338mm,108.5mm) on Top CAYOF ance Constraint: (0.155mm < 0.254mm) Between Pad U16-1(113.5mm,110mm) on Top Layer And Pad U16-2(113.5mm,109.5mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U16-10(117.9mm,110mm) on Top Layer And Pad U16-9(117.9mm,109.5mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U16-3(113.5mm,109mm) on Top Layer And Pad U16-4(113.5mm,108.5mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U16-6(117.9mm.108mm) on Top Layer And Pad U16-7(117.9mm.108.5mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U16-7(117.9mm,108.5mm) on Top Layer And Pad U16-8(117.9mm,109mm) on Top Layer Clearance Constraint: (0.155mm < 0.254mm) Between Pad U16-8(117.9mm,109mm) on Top Layer And Pad U16-9(117.9mm,109.5mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-1(60.5mm,81.6mm) on Top Layer And Pad U3-2(61mm,81.6mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-10(60.5mm,86mm) on Top Layer And Pad U3-9(61mm,86mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-2(61mm,81.6mm) on Top Layer And Pad U3-3(61.5mm,81.6mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-3(61.5mm.81.6mm) on Top Layer And Pad U3-4(62mm.81.6mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-4(62mm,81.6mm) on Top Layer And Pad U3-5(62.5mm,81.6mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-7(62mm,86mm) on Top Layer And Pad U3-8(61.5mm,86mm) on Top Layer Clearance Constraint: (0.2mm < 0.254mm) Between Pad U3-8(61.5mm,86mm) on Top Layer And Pad U3-9(61mm,86mm) on Top Layer Clearance Constraint: (0.215mm < 0.254mm) Between Pad U4-1(78.164mm,60.752mm) on Top Layer And Pad U4-5(78.464mm,61.467mm) on Top Catyarance Constraint: (0.225mm < 0.254mm) Between Pad U4-3(78.764mm,62.192mm) on Top Layer And Pad U4-5(78.464mm,61.467mm) on Top CAYOF TABLE 1.0.225 mm < 0.254 mm) Between Pad U4-4(78.164 mm.62.192 mm) on Top Laver And Pad U4-5(78.464 mm.61.467 mm) Layer

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Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net NetC11_1 Between Pad C11-1(62.675mm,79mm) on Top Layer And Pad R18-2(64.762mm,78.775mm) on Top Layer
Un-Routed Net Constraint: Net NetC11_1 Between Pad U3-4(62mm,81.6mm) on Top Layer And Pad C11-1(62.675mm,79mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 4 Between Pad C12-2(265.831mm, 376.121mm) on Top Layer And Pad R8-2(266.719mm, 369.161mm) or
Un-Routed Net Constraint: Net V_SENSE_4 Between Pad C13-1(247.529mm, 370.471mm) on Top Layer And Pad R7-2(319.297mm, 369.161mm) on
UARRouted Net Constraint: Net V_SENSE_4 Between Pad U16-1(113.5mm,110mm) on Top Layer And Pad C13-1(247.529mm,-370.471mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 4 Between Pad C49-2(51.949mm,-422.207mm) on Top Layer And Pad C13-2(247.529mm,-369.121mm) or
Un-Routed Net Constraint: Net MPPT GND 4 Between Pad C13-2(247.529mm, 369.121mm) on Top Layer And Pad R8-2(266.719mm, 369.161mm) or
Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad R15-2(92.208mm,-369.148mm) on Top Layer And Pad C14-2(226.447mm,-369.121mm) or
Un-Routed Net Constraint: Net MPPT GND 5 Between Pad C14-2(226.447mm, 369.121mm) on Top Layer And Pad R6-1(261.373mm, 376.161mm) or
Un-Routed Net Constraint: Net MPPT GND 5 Between Pad U22-9(178.08mm,95.5mm) on Top Layer And Pad C14-2(226.447mm,-369.121mm) on Top
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad C17-2(60.772mm,109mm) on Top Layer And Pad C15-2(63mm,107.65mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad C15-2(63mm,107.65mm) on Top Layer And Pad C45-2(170.821mm,-422.207mm) on Top
Un-Routed Net Constraint: Net V SENSE 5 Between Pad C16-1(68.205mm, 370.471mm) on Top Layer And Pad R11-2(147.593mm, 369.161mm) on
UppRouted Net Constraint: Net MPPT_GND_5 Between Pad C55-2(-73.273mm,-422.207mm) on Top Layer And Pad C16-2(68.205mm,-369.121mm) or
Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad C16-2(68.205mm,-369.121mm) on Top Layer And Pad R15-2(92.208mm,-369.148mm) on
UppRouted Net Constraint: Net V_SENSE_2 Between Pad C17-1(60.772mm,107.65mm) on Top Layer And Pad R20-2(61.2mm,105mm) on Top Layer
Un-Routed Net Constraint: Net V SENSE 2 Between Pad U11-1(51.2mm,108mm) on Top Layer And Pad C17-1(60.772mm,107.65mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad U11-3(51.2mm,109mm) on Top Layer And Pad C17-2(60.772mm,109mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad C17-2(60.772mm,109mm) on Top Layer And Pad U8-2(62mm,114.375mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 6 Between Pad R25-2(-86.608mm, -369.148mm) on Top Layer And Pad C18-2(51.695mm, -369.121mm) or
Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad C18-2(51.695mm,-369.121mm) on Top Layer And Pad R9-1(221.989mm,-369.161mm) on
UppRouted Net Constraint: Net MPPT GND 3 Between Pad C51-2(30.399mm.-422.374mm) on Top Laver And Pad C19-2(40.265mm.-369.121mm) on
UppRouted Net Constraint: Net MPPT_GND_3 Between Pad C19-2(40.265mm,-369.121mm) on Top Layer And Pad C60-2(72.4mm,98.675mm) on Top.
Un-Routed Net Constraint: Net V_SENSE_6 Between Pad C20-1(-110.865mm, 370.471mm) on Top Layer And Pad R24-2(-32.379mm, 369.148mm) or
Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad C20-2(-110.865mm,-369.121mm) on Top Layer And Pad R25-2(-86.608mm,-369.148mm) or
Un-Routed Net Constraint: Net V_SENSE_3 Between Pad C21-1(71.658mm,105.288mm) on Top Layer And Pad R26-2(72mm,107.45mm) on Top Layer
Un-Routed Net Constraint: Net V_SENSE_3 Between Pad TP18-TP(-93.847mm, -369.034mm) on Top Layer And Pad C21-1(71.658mm,105.288mm) or
Un-Routed Net Constraint: Net MPPT GND 3 Between Pad C21-2(70.308mm.105.288mm) on Top Layer And Pad C60-2(72.4mm.98.675mm) on Tor
Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad R27-2(70mm,107.225mm) on Top Layer And Pad C21-2(70.308mm,105.288mm) on Top
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad C22-2(-53.207mm,-574.607mm) on Top Layer And Pad C45-2(170.821mm,-422.207mm) on
Un-Routed Net Constraint: Net MPPT GND 3 Between Pad C23-2(-47.365mm,-574.607mm) on Top Layer And Pad C51-2(30.399mm,-422.374mm) or
Un-Routed Net Constraint: Net HV_LOW Between Pad C47-2(12.325mm.103mm) on Top Layer And Pad C24-2(12.325mm.105mm) on Top Layer
Un-Routed Net Constraint: Net HV_LOW Between Pad U13-3(10.278mm,109mm) on Top Layer And Pad C24-2(12.325mm,105mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad C25-2(-87.624mm,-574.607mm) on Top Layer And Pad C35-2(-31.998mm,-453.957mm) or
Un-Routed Net Constraint: Net MPPT GND 5 Between Pad C26-2(-93.466mm,-574.607mm) on Top Layer And Pad C33-2(-43.682mm,-453.957mm) or
Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad C27-2(-99.308mm,-574.607mm) on Top Layer And Pad C37-2(-55.366mm,-453.957mm) or
Un-Routed Net Constraint: Net HV_LOW Between Pad U13-9(14.678mm,109.5mm) on Top Layer And Pad C29-2(17.835mm,109.675mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 2 Between Pad U11-9(46.8mm, 108.5mm) on Top Layer And Pad C31-2(48.886mm, 97.803mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 2 Between Pad U23-4(47.095mm,93.725mm) on Top Layer And Pad C31-2(48.886mm,97.803mm) on Top
Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad C55-2(-73.273mm,-422.207mm) on Top Layer And Pad C33-2(-43.682mm,-453.957mm) or
Un-Routed Net Constraint: Net MPPT GND 6 Between Pad C53-2(-41.142mm,-422.207mm) on Top Layer And Pad C35-2(-31.998mm,-453.957mm) or
Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad C37-2(-55.366mm, 453.957mm) on Top Layer And Pad C49-2(51.949mm, 422.207mm) or
Un-Routed Net Constraint: Net HV_LOW Between Pad C47-2(12.325mm,103mm) on Top Layer And Pad R13-2(12.675mm,92.325mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 4 Between Pad C49-2(51.949mm,-422.207mm) on Top Layer And Pad U20-15(103.825mm,95.75mm) on
UppRouted Net Constraint: Net MPPT GND 6 Between Pad R25-2(-86.608mm, -369.148mm) on Top Layer And Pad C53-2(-41.142mm, -422.207mm) or
Un-Routed Net Constraint: Net HV_LOW Between Pad C58-2(4.675mm,106mm) on Top Layer And Pad C57-2(5mm,108.325mm) on Top Layer
Un-Routed Net Constraint: Net HV_LOW Between Pad C57-2(5mm,108.325mm) on Top Layer And Pad U13-3(10.278mm,109mm) on Top Layer
Un-Routed Net Constraint: Net HV_LOW Between Pad U5-2(4mm,115.375mm) on Top Layer And Pad C57-2(5mm,108.325mm) on Top Layer
Un-Routed Net Constraint: Net V SENSE 1 Between Pad R42-2(3mm,108.225mm) on Top Layer And Pad C58-1(3.325mm,106mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad C60-2(72.4mm,98.675mm) on Top Layer And Pad U26-4(80.095mm,93.725mm) on Top
Layer Onstraint: Net SOLAR_RELAY_GND Between Pad D2-2(52mm,58.148mm) on Top Layer And Pad D1-1(56mm,58.092mm) on Top Layer
Un-Routed Net Constraint: Net SOLAR_RELAY_GND Between Pad D1-1(56mm,58.092mm) on Top Layer And Pad LED1-2(59.55mm,57.828mm) on Top
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Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net SOLAR_RELAY_GND Between Pad Q1-2(47.571mm,57.96mm) on Top Layer And Pad D2-2(52mm,58.148mm) on Top
Un-Routed Net Constraint: Net NetF1_2 Between Pad F1-2(46.71mm,47mm) on Multi-Layer And Pad F1-2(57mm,47mm) on Multi-Layer
Un-Routed Net Constraint: Net NetF1 2 Between Pad K1-6(15.6mm,48.95mm) on Multi-Layer And Pad F1-2(46.71mm,47mm) on Multi-Layer
Un-Routed Net Constraint: Net NetK1_4 Between Pad R13-1(12.675mm,89.675mm) on Top Layer And Pad K1-4(15.6mm,71.05mm) on Multi-Layer
Un-Routed Net Constraint: Net NetK1_4 Between Pad K1-4(15.6mm,71.05mm) on Multi-Layer And Pad TH_TP2-TH_TP(24.745mm,-304.931mm) or
Un-Routed Net Constraint: Net SOLAR RELAY GND Between Pad K1-B(31.6mm,48.95mm) on Multi-Layer And Pad Q1-6(45.721mm,57.31mm) on Tor.
Un-Routed Net Constraint: Net SOLAR RELAY GND Between Pad TP2-TP(-126.004mm, -304.423mm) on Top Layer And Pad K1-B(31.6mm, 48.95mm)
Um-Routed Net Constraint: Net NetLED1_1 Between Pad LED1-1(61.05mm,57.828mm) on Top Layer And Pad R2-2(61.084mm,62.092mm) on Top Layer
Un-Routed Net Constraint: Net NetLED2 1 Between Pad LED2-1(46.189mm,71.982mm) on Top Layer And Pad R19-2(48.6mm,81mm) on Top Layer
Un-Routed Net Constraint: Net NetLED3 1 Between Pad LED3-1(-98.302mm,-554.377mm) on Top Layer And Pad R5-2(-80.626mm,-552.803mm) on Top
Un-Routed Net Constraint: Net SCK_MPPT2 Between Pad U18-13(39.365mm,95.75mm) on Top Layer And Pad P10-3(46mm,118mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad P10-5(39mm,118mm) on Multi-Layer And Pad P8-2(46mm,141mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT GND 2 Between Pad P10-5(39mm,118mm) on Multi-Layer And Pad U11-9(46.8mm,108.5mm) on Top Layer
Un-Routed Net Constraint: Net PB13_SPI2_SCK Between Pad P1-10(102mm,65.5mm) on Top Layer And Pad U20-4(106.365mm,86.25mm) on Top Layer
Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad TP24-TP(85.096mm, 422.12mm) on Top Layer And Pad P1-13(102mm,64mm) on Top
Layer outed Net Constraint: Net PB11/I2C2_SDA Between Pad P1-13(102mm,64mm) on Top Layer And Pad U25-7(116.635mm,88.275mm) on Top Layer
Un-Routed Net Constraint: Net SCK MPPT3 Between Pad P11-3(86mm,118mm) on Multi-Layer And Pad U19-13(90.565mm,95.75mm) on Top Layer
Un-Routed Net Constraint: Net PB10/I2C2_SCL Between Pad TP23-TP(77.73mm,-422.12mm) on Top Layer And Pad P1-14(102mm,63.5mm) on Top
Un-Routed Net Constraint: Net PB10/I2C2_SCL Between Pad P1-14(102mm,63.5mm) on Top Layer And Pad U25-6(115.365mm,88.275mm) on Top
University of the Constraint: Net MPPT GND 3 Between Pad P11-5(79mm,118mm) on Multi-Layer And Pad P7-2(87mm,131.47mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad U10-2(71mm,118.375mm) on Top Layer And Pad P11-5(79mm,118mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT GND 3 Between Pad P11-5(79mm.118mm) on Multi-Laver And Pad U12-2(80mm.109.5mm) on Top Laver
Un-Routed Net Constraint: Net PA5_Temp_6 Between Pad P1-20(102mm,60.5mm) on Top Layer And Pad P15-4(130mm,40mm) on Multi-Layer
Un-Routed Net Constraint: Net PA4_Temp_5 Between Pad P1-21(102mm,60mm) on Top Layer And Pad P15-8(130mm,37mm) on Multi-Layer
Un-Routed Net Constraint: Net PA3_Temp_4 Between Pad P1-22(102mm,59.5mm) on Top Layer And Pad P15-3(127mm,40mm) on Multi-Layer
Un-Routed Net Constraint: Net PA2_Temp_3 Between Pad P1-23(102mm,59mm) on Top Layer And Pad P15-7(127mm,37mm) on Multi-Layer
Un-Routed Net Constraint: Net SCK_MPPT4 Between Pad U20-13(106.365mm,95.75mm) on Top Layer And Pad P12-3(112mm,118mm) on Multi-Layer
Un-Routed Net Constraint: Net PA1 Temp 2 Between Pad P1-24(102mm.58.5mm) on Top Layer And Pad P15-2(124mm.40mm) on Multi-Layer
Un-Routed Net Constraint: Net PA0_Temp_1 Between Pad P1-25(102mm,58mm) on Top Layer And Pad P15-6(124mm,37mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad P12-5(105mm,118mm) on Multi-Layer And Pad P6-2(111mm,141mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad P12-5(105mm,118mm) on Multi-Layer And Pad U16-2(113.5mm,109.5mm) on Top Layer
Un-Routed Net Constraint: Net PB9/I2C1_SDA Between Pad R16-2(83.775mm,68mm) on Top Layer And Pad P1-31(98.4mm,60.5mm) on Top Layer
Un-Routed Net Constraint: Net PB8/I2C1_SCL Between Pad R17-2(83.775mm,70mm) on Top Layer And Pad P1-32(98.4mm,61mm) on Top Layer
Un-Routed Net Constraint: Net SCK_MPPT5 Between Pad P13-3(151.5mm,118mm) on Multi-Layer And Pad U21-13(156mm,95.5mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 5 Between Pad P13-5(144.5mm,118mm) on Multi-Layer And Pad P5-2(152mm,131.47mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad P13-5(144.5mm,118mm) on Multi-Layer And Pad U15-2(144.938mm,108.5mm) on Top
Layer Onstraint: Net MPPT_GND_5 Between Pad U7-2(137mm,118mm) on Top Layer And Pad P13-5(144.5mm,118mm) on Multi-Layer
Un-Routed Net Constraint: Net PB5 DEMUX A2 Between Pad U2-3(71.275mm,71.905mm) on Top Layer And Pad P1-36(98.4mm,63mm) on Top Layer
Un-Routed Net Constraint: Net PB4 DEMUX A1 Between Pad U2-2(71.275mm,73.175mm) on Top Layer And Pad P1-37(98.4mm,63.5mm) on Top Layer
Un-Routed Net Constraint: Net PB3_DEMUX_A0_Between Pad U2-1(71.275mm,74.445mm) on Top Layer And Pad P1-38(98.4mm,64mm) on Top Layer
Un-Routed Net Constraint: Net PA15/ADC INTERRUPT Between Pad U3-2(61mm,81.6mm) on Top Layer And Pad P1-39(98.4mm,64.5mm) on Top Layer
Un-Routed Net Constraint: Net SCK_MPPT6 Between Pad U22-13(173mm,95.5mm) on Top Layer And Pad P14-3(179mm,118mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad P14-5(172mm,118mm) on Multi-Layer And Pad P4-2(176mm,141mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT GND 6 Between Pad P14-5(172mm,118mm) on Multi-Layer And Pad U14-2(181.486mm,108.5mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 2 Between Pad R12-1(3.225mm,124.775mm) on Top Layer And Pad P16-1(20mm,141mm) on Multi-Layer
Un-Routed Net Constraint: Net PA8/SOLAR_RELAY Between Pad R3-1(46.139mm,65.318mm) on Top Layer And Pad P1-7(102mm,67mm) on Top Layer
Un-Routed Net Constraint: Net PB15_SPI2_MOSI Between Pad P1-8(102mm,66.5mm) on Top Layer And Pad U20-3(105.095mm,86.25mm) on Top Layer
Un-Routed Net Constraint: Net PB14_SPI2_MISO Between Pad P1-9(102mm,66mm) on Top Layer And Pad U20-6(108.905mm,86.25mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 5 Between Pad P6-1(111mm,131.47mm) on Multi-Layer And Pad U7-2(137mm,118mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad R23-1(69.424mm,123.225mm) on Top Layer And Pad P7-1(87mm,141mm) on Multi-Layer
Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad P8-1(46mm,131.47mm) on Multi-Layer And Pad R10-1(63mm,124.775mm) on Top Layer
Un-Routed Net Constraint: Net SCK MPPT1 Between Pad P9-3(20mm,118mm) on Multi-Layer And Pad U17-13(25.365mm,95.75mm) on Top Layer
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Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net HV_LOW Between Pad P9-5(13mm,118mm) on Multi-Layer And Pad U13-10(14.678mm,110mm) on Top Layer
Un-Routed Net Constraint: Net SOLAR_RELAY_GND Between Pad Q1-1(47.571mm,57.31mm) on Top Layer And Pad Q1-2(47.571mm,57.96mm) on Top
Un-Routed Net Constraint: Net SOLAR RELAY GND Between Pad Q1-7(46.646mm,57.66mm) on Top Layer And Pad Q1-2(47.571mm,57.96mm) on Top
Un-Routed Net Constraint: Net NetQ1_3 Between Pad Q1-3(47.571mm,58.61mm) on Top Layer And Pad R3-2(47.689mm,65.318mm) on Top Layer
Un-Routed Net Constraint: Net SOLAR_RELAY_GND Between Pad Q1-6(45.721mm,57.31mm) on Top Layer And Pad Q1-5(45.721mm,57.96mm) on Top
Un-Routed Net Constraint: Net SOLAR RELAY GND Between Pad Q1-5(45.721mm,57.96mm) on Top Layer And Pad Q1-7(46.646mm,57.66mm) on Top
Un-Routed Net Constraint: Net MPPT GND 3 Between Pad R10-1(63mm,124.775mm) on Top Layer And Pad U10-2(71mm,118.375mm) on Top Layer
Un-Routed Net Constraint: Net NetR10_2 Between Pad R21-1(62.775mm,119.891mm) on Top Layer And Pad R10-2(63mm,123.225mm) on Top Layer
Un-Routed Net Constraint: Net NetK1 4 Between Pad R1-1(9.895mm,89.675mm) on Top Layer And Pad R13-1(12.675mm,89.675mm) on Top Layer
Un-Routed Net Constraint: Net NetK1 4 Between Pad U1-1(7.069mm,89.265mm) on Top Layer And Pad R1-1(9.895mm,89.675mm) on Top Layer
Un-Routed Net Constraint: Net NetR11_1 Between Pad U7-4(136.05mm,115.25mm) on Top Layer And Pad R11-1(146.043mm,-369.161mm) on Top
Law Fouted Net Constraint: Net V SENSE 5 Between Pad R11-2(147.593mm, -369.161mm) on Top Laver And Pad TP12-TP(162.185mm, -369.034mm) or Top Laver And Pad TP12-TP(162.185mm, 
Un-Routed Net Constraint: Net V SENSE 5 Between Pad U15-1(144.938mm,109mm) on Top Layer And Pad R11-2(147.593mm,-369.161mm) on Top
Un-Routed Net Constraint: Net HV_LOW Between Pad R1-2(9.895mm, 92.325mm) on Top Layer And Pad R13-2(12.675mm, 92.325mm) on Top Layer
Un-Routed Net Constraint: Net HV_LOW Between Pad U1-3(7.069mm,91.805mm) on Top Layer And Pad R1-2(9.895mm,92.325mm) on Top Layer
Un-Routed Net Constraint: Net NetR12_2 Between Pad R14-1(3.225mm,120mm) on Top Layer And Pad R12-2(3.225mm,123.225mm) on Top Layer
Un-Routed Net Constraint: Net HV LOW Between Pad R13-2(12.675mm,92.325mm) on Top Layer And Pad U24-4(15.295mm,93.725mm) on Top Layer
Un-Routed Net Constraint: Net NetR12_2 Between Pad TP10-TP(1.762mm,117.648mm) on Top Layer And Pad R14-1(3.225mm,120mm) on Top Layer
Un-Routed Net Constraint: Net HV_LOW Between Pad U5-2(4mm,115.375mm) on Top Layer And Pad R14-2(4.775mm,120mm) on Top Layer
Un-Routed Net Constraint: Net NetR9 2 Between Pad R15-1(92.208mm, 370.698mm) on Top Layer And Pad TP9-TP(169.297mm, 369.034mm) on Top
Un-Routed Net Constraint: Net PB9/I2C1_SDA Between Pad U3-9(61mm,86mm) on Top Layer And Pad R16-2(83.775mm,68mm) on Top Layer
Un-Routed Net Constraint: Net PB8/I2C1 SCL Between Pad U3-10(60.5mm,86mm) on Top Laver And Pad R17-2(83,775mm,70mm) on Top Laver
Un-Routed Net Constraint: Net NetR18_1 Between Pad U1-7(2.269mm, 90.535mm) on Top Layer And Pad R18-1(64.762mm, 77.225mm) on Top Layer
Un-Routed Net Constraint: Net NetR20_1 Between Pad U8-4(61.05mm,111.625mm) on Top Layer And Pad R20-1(62.75mm,105mm) on Top Layer
Un-Routed Net Constraint: Net V_SENSE_2 Between Pad R20-2(61.2mm,105mm) on Top Layer And Pad TP14-TP(84.969mm,-369.034mm) on Top
Layer Onstraint: Net NetR10_2 Between Pad U8-3(61.05mm,114.375mm) on Top Layer And Pad R21-1(62.775mm,119.891mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad R21-2(61.225mm,119.891mm) on Top Layer And Pad U8-2(62mm,114.375mm) on Top
Lawrouted Net Constraint: Net NetR22 2 Between Pad TP15-TP(-8.249mm,-369.034mm) on Top Laver And Pad R22-2(45.853mm,-369.148mm) on Top
Un-Routed Net Constraint: Net NetR22_2 Between Pad R22-2(45.853mm,-369.148mm) on Top Layer And Pad U9-3(192mm,117.75mm) on Top Layer
Un-Routed Net Constraint: Net NetR23_2 Between Pad R23-2(69.424mm,124.775mm) on Top Layer And Pad U10-3(70.05mm,118.375mm) on Top Layer
Un-Routed Net Constraint: Net NetR24 1 Between Pad R24-1(-32.379mm,-370.698mm) on Top Layer And Pad U9-4(192mm,115mm) on Top Layer
Un-Routed Net Constraint: Net V SENSE 6 Between Pad R24-2(-32.379mm, -369.148mm) on Top Layer And Pad TP16-TP(-15.361mm, -369.034mm) or
Un-Routed Net Constraint: Net NetR22_2 Between Pad R25-1(-86.608mm,-370.698mm) on Top Layer And Pad TP15-TP(-8.249mm,-369.034mm) on Top
Un-Routed Net Constraint: Net NetR26_1 Between Pad U10-4(70.05mm,115.625mm) on Top Layer And Pad R26-1(72mm,109mm) on Top Layer
Un-Routed Net Constraint: Net V SENSE 3 Between Pad R26-2(72mm,107.45mm) on Top Layer And Pad U12-1(80mm,110mm) on Top Layer
Un-Routed Net Constraint: Net NetR23_2 Between Pad TP17-TP(-80.893mm,-369.034mm) on Top Layer And Pad R27-1(70mm,108.775mm) on Top
Lay-Routed Net Constraint: Net NetR23_2 Between Pad R27-1(70mm,108.775mm) on Top Layer And Pad U10-3(70.05mm,118.375mm) on Top Layer
Un-Routed Net Constraint: Net MPPT GND 3 Between Pad R27-2(70mm,107.225mm) on Top Layer And Pad U12-3(80mm,109mm) on Top Layer
Un-Routed Net Constraint: Net PA8/SOLAR RELAY Between Pad R4-1(46mm,63mm) on Top Layer And Pad R3-1(46.139mm,65.318mm) on Top Layer
Un-Routed Net Constraint: Net PB10/I2C2_SCL Between Pad R32-2(16mm,83mm) on Top Layer And Pad U24-6(16.565mm,88.275mm) on Top Layer
Un-Routed Net Constraint: Net PB11/I2C2 SDA Between Pad R33-2(16.225mm,86mm) on Top Layer And Pad U24-7(17.835mm,88.275mm) on Top
Liny@outed Net Constraint: Net PA8/SOLAR_RELAY Between Pad TP3-TP(-185.059mm,-304.423mm) on Top Layer And Pad R4-1(46mm,63mm) on Top
Un-Routed Net Constraint: Net NetR42_1 Between Pad R42-1(3mm,109.775mm) on Top Layer And Pad U5-4(3.05mm,112.625mm) on Top Layer
Un-Routed Net Constraint: Net V SENSE 1 Between Pad R42-2(3mm,108.225mm) on Top Layer And Pad U13-1(10.278mm,110mm) on Top Layer
Un-Routed Net Constraint: Net NetR6 2 Between Pad R6-2(262.923mm,-376.161mm) on Top Layer And Pad R8-1(265.169mm,-369.161mm) on Top
Layer Onstraint: Net NetR7_1 Between Pad U6-4(127mm,115.25mm) on Top Layer And Pad R7-1(317.747mm,-369.161mm) on Top Layer
Un-Routed Net Constraint: Net V_SENSE_4 Between Pad R7-2(319.297mm, 369.161mm) on Top Layer And Pad TP8-TP(333.127mm, 369.034mm) or
Un-Routed Net Constraint: Net NetR6_2 Between Pad R8-1(265.169mm, 369.161mm) on Top Layer And Pad TP7-TP(338.969mm, 369.034mm) on Top
Un-Routed Net Constraint: Net NetR6 2 Between Pad U6-3(127mm,118mm) on Top Layer And Pad R8-1(265.169mm,-369.161mm) on Top Layer
Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad U27-4(182.095mm,93.725mm) on Top Layer And Pad R9-1(221.989mm,-369.161mm) on
UppRouted Net Constraint: Net NetR9_2 Between Pad TP9-TP(169.297mm, 369.034mm) on Top Layer And Pad R9-2(223.539mm, 369.161mm) on Top
Un-Routed Net Constraint: Net NetR12_2 Between Pad TP10-TP(1.762mm,117.648mm) on Top Layer And Pad U5-3(3.05mm,115.375mm) on Top Layer
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Un-Routed Net Constraint ((All)) Un-Routed Net Constraint: Net V_SENSE_1 Between Pad U13-1(10.278mm,110mm) on Top Layer And Pad TP11-TP(258.451mm, 369.034mm) on Top Un-Routed Net Constraint: Net NetR10_2 Between Pad U8-3(61.05mm,114.375mm) on Top Layer And Pad TP13-TP(97.669mm,-369.034mm) on Top Un-Routed Net Constraint: Net V SENSE 6 Between Pad TP16-TP(-15.361mm,-369.034mm) on Top Layer And Pad U14-1(181.486mm,109mm) on Top Un-Routed Net Constraint: Net NetR18_1 Between Pad TP1-TP(-82.189mm,-304.423mm) on Top Layer And Pad U1-7(2.269mm,90.535mm) on Top UNIT OF THE PART O Un-Routed Net Constraint: Net NetR26 1 Between Pad U10-4(70.05mm,115.625mm) on Top Layer And Pad U10-1(71.95mm,118.375mm) on Top Layer Un-Routed Net Constraint: Net NetK1 4 Between Pad U1-1(7.069mm,89.265mm) on Top Layer And Pad U1-2(7.069mm,90.535mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad U11-2(51.2mm,108.5mm) on Top Layer And Pad U11-3(51.2mm,109mm) on Top Layer Un-Routed Net Constraint: Net MPPT GND 2 Between Pad U11-9(46.8mm, 108.5mm) on Top Layer And Pad U11-2(51.2mm, 108.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT GND 3 Between Pad U12-3(80mm,109mm) on Top Layer And Pad U12-2(80mm,109.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad U12-2(80mm,109.5mm) on Top Layer And Pad U12-9(84.4mm,109.5mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U1-3(7.069mm,91.805mm) on Top Layer And Pad U1-4(7.069mm,93.075mm) on Top Layer Un-Routed Net Constraint: Net HV LOW Between Pad U13-9(14.678mm,109.5mm) on Top Layer And Pad U13-10(14.678mm,110mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U13-3(10.278mm,109mm) on Top Layer And Pad U13-2(10.278mm,109.5mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U13-2(10.278mm, 109.5mm) on Top Layer And Pad U13-9(14.678mm, 109.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad U14-2(181.486mm,108.5mm) on Top Layer And Pad U14-10(185.886mm,109mm) on Top Un-Routed Net Constraint: Net MPPT GND 6 Between Pad U14-10(185.886mm,109mm) on Top Layer And Pad U9-2(192.95mm,117.75mm) on Top Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad U14-3(181.486mm,108mm) on Top Layer And Pad U14-2(181.486mm,108.5mm) on Top Un-Routed Net Constraint: Net MPPT_GND_6 Between Pad U14-3(181.486mm,108mm) on Top Layer And Pad U27-4(182.095mm,93.725mm) on Top Un-Routed Net Constraint: Net MPPT GND 5 Between Pad U15-3(144.938mm,108mm) on Top Layer And Pad U15-2(144.938mm,108.5mm) on Top Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad U15-3(144.938mm,108mm) on Top Layer And Pad U28-4(145.73mm,94mm) on Top Layer Un-Routed Net Constraint: Net MPPT GND 4 Between Pad U16-3(113.5mm.109mm) on Top Layer And Pad U16-2(113.5mm.109.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad U16-2(113.5mm,109.5mm) on Top Layer And Pad U6-2(127.95mm,118mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad U20-9(111.445mm,95.75mm) on Top Layer And Pad U16-3(113.5mm,109mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U17-15(22.825mm,95.75mm) on Top Layer And Pad U17-10(29.175mm,95.75mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U17-10(29.175mm, 95.75mm) on Top Layer And Pad U17-9(30.445mm, 95.75mm) on Top Layer Un-Routed Net Constraint: Net HV_LOW Between Pad U24-4(15.295mm,93.725mm) on Top Layer And Pad U17-15(22.825mm,95.75mm) on Top Layer Un-Routed Net Constraint: Net PA7 SPI2 MOSI Between Pad U17-3(24.095mm.86.25mm) on Top Laver And Pad U18-3(38.095mm.86.25mm) on Top Un-Routed Net Constraint: Net PA5_SPI2_SCK Between Pad U17-4(25.365mm,86.25mm) on Top Layer And Pad U18-4(39.365mm,86.25mm) on Top Un-Routed Net Constraint: Net SPI2_CS_1 Between Pad U17-5(26.635mm,86.25mm) on Top Layer And Pad U2-15(76.725mm,73.175mm) on Top Layer Un-Routed Net Constraint: Net PA6 SPI2 MISO Between Pad U17-6(27.905mm,86.25mm) on Top Layer And Pad U18-6(41.905mm,86.25mm) on Top Un-Routed Net Constraint: Net MPPT GND 2 Between Pad U18-15(36.825mm,95.75mm) on Top Layer And Pad U18-10(43.175mm,95.75mm) on Top Un-Routed Net Constraint: Net MPPT_GND_2 Between Pad U18-10(43.175mm,95.75mm) on Top Layer And Pad U18-9(44.445mm,95.75mm) on Top Un-Routed Net Constraint: Net PA7_SPI2_MOSI Between Pad U18-3(38.095mm,86.25mm) on Top Layer And Pad U19-3(89.295mm,86.25mm) on Top Un-Routed Net Constraint: Net PA5 SPI2 SCK Between Pad U18-4(39.365mm,86.25mm) on Top Layer And Pad U19-4(90.565mm,86.25mm) on Top Un-Routed Net Constraint: Net SPI2_CS_2 Between Pad U18-5(40.635mm,86.25mm) on Top Layer And Pad U2-14(76.725mm,71.905mm) on Top Layer Un-Routed Net Constraint: Net PA6_SPI2_MISO Between Pad U18-6(41.905mm,86.25mm) on Top Layer And Pad U19-6(93.105mm,86.25mm) on Top Un-Routed Net Constraint: Net MPPT GND 2 Between Pad U18-9(44.445mm,95.75mm) on Top Layer And Pad U23-4(47.095mm,93.725mm) on Top Un-Routed Net Constraint: Net MPPT GND 3 Between Pad U19-15(88.025mm,95.75mm) on Top Layer And Pad U19-10(94.375mm,95.75mm) on Top Un-Routed Net Constraint: Net MPPT_GND_3 Between Pad U19-10(94.375mm, 95.75mm) on Top Layer And Pad U19-9(95.645mm, 95.75mm) on Top Un-Routed Net Constraint: Net MPPT GND 3 Between Pad U26-4(80.095mm,93.725mm) on Top Layer And Pad U19-15(88.025mm,95.75mm) on Top Un-Routed Net Constraint: Net SPI2_CS_3 Between Pad U2-13(76.725mm,70.635mm) on Top Layer And Pad U19-5(91.835mm,86.25mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_4 Between Pad U20-15(103.825mm,95.75mm) on Top Layer And Pad U20-10(110.175mm,95.75mm) on Upprovided Net Constraint: Net MPPT GND 4 Between Pad U20-10(110.175mm,95.75mm) on Top Layer And Pad U20-9(111.445mm,95.75mm) on Top Un-Routed Net Constraint: Net PB15 SPI2 MOSI Between Pad U20-3(105.095mm,86.25mm) on Top Layer And Pad U21-3(154.73mm,86mm) on Top Un-Routed Net Constraint: Net PB13_SPI2_SCK Between Pad U20-4(106.365mm,86.25mm) on Top Layer And Pad U21-4(156mm,86mm) on Top Layer Un-Routed Net Constraint: Net SPI2_CS_4 Between Pad U2-12(76.725mm,69.365mm) on Top Layer And Pad U20-5(107.635mm,86.25mm) on Top Layer Un-Routed Net Constraint: Net PB14_SPI2_MISO Between Pad U20-6(108.905mm,86.25mm) on Top Layer And Pad U21-6(158.54mm,86mm) on Top Un-Routed Net Constraint: Net MPPT GND 4 Between Pad U20-9(111.445mm,95.75mm) on Top Layer And Pad U25-4(114.095mm,93.725mm) on Top Un-Routed Net Constraint: Net SPI2_CS_6 Between Pad U2-10(76.725mm,66.825mm) on Top Layer And Pad U22-5(174.27mm,86mm) on Top Layer Un-Routed Net Constraint: Net SPI2_CS_5 Between Pad U2-11(76.725mm,68.095mm) on Top Layer And Pad U21-5(157.27mm,86mm) on Top Layer

Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad U21-15(153.46mm,95.5mm) on Top Layer And Pad U21-10(159.81mm,95.5mm) on Top

Un-Routed Net Constraint ((All))

Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad U21-10(159.81mm,95.5mm) on Top Layer And Pad U21-9(161.08mm,95.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT_GND_5 Between Pad U28-4(145.73mm,94mm) on Top Layer And Pad U21-15(153.46mm,95.5mm) on Top Layer Un-Routed Net Constraint: Net PB15 SPI2 MOSI Between Pad U21-3(154.73mm,86mm) on Top Layer And Pad U22-3(171.73mm,86mm) on Top Layer Un-Routed Net Constraint: Net PB13_SPI2_SCK Between Pad U21-4(156mm,86mm) on Top Layer And Pad U22-4(173mm,86mm) on Top Layer Un-Routed Net Constraint: Net PB14_SPI2_MISO Between Pad U21-6(158.54mm,86mm) on Top Layer And Pad U22-6(175.54mm,86mm) on Top Layer Un-Routed Net Constraint: Net MPPT GND 5 Between Pad U21-9(161.08mm,95.5mm) on Top Layer And Pad U22-15(170.46mm,95.5mm) on Top Layer Un-Routed Net Constraint: Net MPPT GND 5 Between Pad U22-15(170.46mm,95.5mm) on Top Layer And Pad U22-10(176.81mm,95.5mm) on Top Lany Routed Net Constraint: Net MPPT_GND_5 Between Pad U22-10(176.81mm,95.5mm) on Top Layer And Pad U22-9(178.08mm,95.5mm) on Top Layer Un-Routed Net Constraint: Net PB10/I2C2 SCL Between Pad U24-6(16.565mm,88.275mm) on Top Layer And Pad U23-6(48.365mm,88.275mm) on Top Un-Routed Net Constraint: Net PB10/I2C2 SCL Between Pad U23-6(48.365mm,88.275mm) on Top Layer And Pad U26-6(81.365mm,88.275mm) on Top Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad U24-7(17.835mm,88.275mm) on Top Layer And Pad U23-7(49.635mm,88.275mm) on Top Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad U23-7(49.635mm,88.275mm) on Top Layer And Pad U26-7(82.635mm,88.275mm) on Top Un-Routed Net Constraint: Net PB10/I2C2 SCL Between Pad U26-6(81.365mm,88.275mm) on Top Layer And Pad U25-6(115.365mm,88.275mm) on Top Un-Routed Net Constraint: Net PB10/l2C2_SCL Between Pad U25-6(115.365mm,88.275mm) on Top Layer And Pad U28-6(147mm,88.55mm) on Top Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad U26-7(82.635mm,88.275mm) on Top Layer And Pad U25-7(116.635mm,88.275mm) on Top Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad U25-7(116.635mm,88.275mm) on Top Layer And Pad U28-7(148.27mm,88.55mm) on Top Un-Routed Net Constraint: Net PB10/l2C2 SCL Between Pad U28-6(147mm,88.55mm) on Top Layer And Pad U27-6(183.365mm,88.275mm) on Top Un-Routed Net Constraint: Net PB11/I2C2_SDA Between Pad U28-7(148.27mm,88.55mm) on Top Layer And Pad U27-7(184.635mm,88.275mm) on Top Un-Routed Net Constraint: Net NetR42_1 Between Pad U5-4(3.05mm,112.625mm) on Top Layer And Pad U5-1(4.95mm,115.375mm) on Top Layer Un-Routed Net Constraint: Net NetR7 1 Between Pad U6-4(127mm,115.25mm) on Top Layer And Pad U6-1(128.9mm,118mm) on Top Layer Un-Routed Net Constraint: Net NetR11_1 Between Pad U7-4(136.05mm,115.25mm) on Top Layer And Pad U7-1(137.95mm,118mm) on Top Layer Un-Routed Net Constraint: Net NetR20 1 Between Pad U8-4(61.05mm.111.625mm) on Top Laver And Pad U8-1(62.95mm.114.375mm) on Top Laver Un-Routed Net Constraint: Net NetR24_1 Between Pad U9-4(192mm,115mm) on Top Layer And Pad U9-1(193.9mm,117.75mm) on Top Layer

Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)

Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(127.2mm,51.5mm) on Multi-Layer Actual Hole Size = 2.7mm

Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(127.2mm,76.5mm) on Multi-Layer Actual Hole Size = 2.7mm

Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(97.2mm,51.5mm) on Multi-Layer Actual Hole Size = 2.7mm

Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(97.2mm,76.5mm) on Multi-Layer Actual Hole Size = 2.7mm

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(44)
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad P1-(100.2mm,56.2mm) on Top Layer And Pad P1-(98.7mm,56.95mm) or
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad P1-(100.2mm,71.8mm) on Top Layer And Pad P1-(98.7mm,71.05mm) or
Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad Q1-1(47.571mm,57.31mm) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad Q1-2(47.571mm,57.96mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad Q1-3(47.571mm,58.61mm) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad Q1-3(47.571mm,58.61mm) on Top Layer And Pad
Q1nb(Abno Sorder, Makinshi)ver Constraint: (0.047mm < 0.254mm) Between Pad Q1-4(45.721mm,58.61mm) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad Q1-4(45.721mm,58.61mm) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad Q1-4(45.721mm,58.61mm) on Top Layer And Pad
Minb(Am6\$6tder, Makknobi)ver Constraint: (0.047mm < 0.254mm) Between Pad Q1-5(45.721mm,57.96mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.187mm < 0.254mm) Between Pad Q1-7(46.646mm,57.66mm) on Top Layer And Pad
Minb(Abro Sorder, Mask 1981) ver Constraint: (0.147mm < 0.254mm) Between Pad U10-1(71.95mm, 118.375mm) on Top Layer And Pac
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U10-2(71mm,118.375mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U4-1(78.164mm,60.752mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.012mm < 0.254mm) Between Pad U4-1(78.164mm,60.752mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.012mm < 0.254mm) Between Pad U4-2(78.764mm,60.752mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U4-3(78.764mm,62.192mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U4-3(78.764mm,62.192mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U4-4(78.164mm,62.192mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-1(4.95mm,115.375mm) on Top Layer And Pad U5-2(4mm,115.375mm)
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-2(4mm,115.375mm) on Top Layer And Pad U5-3(3.05mm,115.375mm)
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U6-1(128.9mm,118mm) on Top Layer And Pad U6-2(127.95mm,118mm)
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U6-2(127.95mm,118mm) on Top Layer And Pad U6-3(127mm,118mm) or
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-1(137.95mm,118mm) on Top Layer And Pad U7-2(137mm,118mm) or
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-2(137mm,118mm) on Top Layer And Pad U7-3(136.05mm,118mm) or
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U8-1(62.95mm,114.375mm) on Top Layer And Pad
Mลิกผิ(ต์ผิกก ชื่อเปล่ะ มิ/โลสหาชิโ)ver Constraint: (0.147mm < 0.254mm) Between Pad U8-2(62mm,114.375mm) on Top Layer And Pad
ไม่อีกสี(ณ์เกิจ Smittel Mask ราเหตุ) Constraint: (0.147mm < 0.254mm) Between Pad U9-1(193.9mm,117.75mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U9-2(192.95mm,117.75mm) on Top Layer And Pad
U9-3(192mm,117.75mm)

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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.188mm < 0.254mm) Between Arc (77.91mm,60.244mm) on Top Overlay And Pac
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad C1-1(57.299mm,73.438mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad C1-1(57.299mm,73.438mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C1-2(51.399mm,73.438mm) on Top Layer And Text "LED2'
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad C1-2(51.399mm,73.438mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad C1-2(51.399mm,73.438mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C15-2(63mm,107.65mm) on Top Layer And Text "R20'
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C17-1(60.772mm,107.65mm) on Top Layer And Text "R20"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C2-1(55.724mm,67mm) on Top Layer And Text "D1"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C24-1(13.675mm,105mm) on Top Layer And Text "C47"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C24-2(12.325mm,105mm) on Top Layer And Text "C47"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C26-2(-93.466mm,-574.607mm) on Top Layer And Text "C26"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C30-1(46.952mm,83.125mm) on Top Layer And Text "R19"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C30-2(46.952mm,84.475mm) on Top Layer And Text "R19"
Silk To Solder Mask Clearance Constraint: (0.212mm < 0.254mm) Between Pad C31-2(48.886mm,97.803mm) on Top Layer And Text "U23"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad C47-1(13.675mm,103mm) on Top Layer And Text "R30"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad D1-1(56mm,58.092mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.254mm) Between Pad D1-1(56mm,58.092mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.254mm) Between Pad D1-1(56mm,62.092mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.254mm) Between Pad D1-2(56mm,62.092mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D2-2(52mm,58.148mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad F1-1(87.99mm,47mm) on Multi-Layer And Text "P3"
\$8&\tag{31.6mm,71.05mm} on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.237mm < 0.254mm) Between Pad K1-B(31.6mm,48.95mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED1-2(59.55mm,57.828mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED2-2(46.189mm,70.482mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED3-2(-98.302mm,-552.877mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-(100.2mm,56.2mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-(100.2mm,71.8mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.223mm < 0.254mm) Between Pad P1-1(102mm,70mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.223mm < 0.254mm) Between Pad P1-25(102mm,58mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.239mm < 0.254mm) Between Pad P1-26(98.4mm,58mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-26(98.4mm,58mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-27(98.4mm,58.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-28(98.4mm,59mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-29(98.4mm,59.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-30(98.4mm,60mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-31(98.4mm,60.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-32(98.4mm,61mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-33(98.4mm,61.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-34(98.4mm,62mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-35(98.4mm,62.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-36(98.4mm,63mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-37(98.4mm,63.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-38(98.4mm,64mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-39(98.4mm,64.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-40(98.4mm,65mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-41(98.4mm,65.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-42(98.4mm,66mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-43(98.4mm,66.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-44(98.4mm,67mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-45(98.4mm,67.5mm) on Top Layer And Track
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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-46(98.4mm,68mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-47(98.4mm,68.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-48(98.4mm,69mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-49(98.4mm,69.5mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.179mm < 0.254mm) Between Pad P15-0(118mm,36.06mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.147mm < 0.254mm) Between Pad P15-0(118mm,36.06mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.147mm < 0.254mm) Between Pad P15-0(133mm,36.06mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P15-0(133mm,36.06mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.213mm < 0.254mm) Between Pad P15-0(133mm,36.06mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.239mm < 0.254mm) Between Pad P1-50(98.4mm,70mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad P1-50(98.4mm,70mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Pad Q1-1(47.571mm,57.31mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.254mm) Between Pad Q1-2(47.571mm,57.96mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.254mm) Between Pad Q1-2(47.571mm,57.96mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Pad Q1-3(47.571mm,58.61mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Pad Q1-4(45.721mm,58.61mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.03mm < 0.254mm) Between Pad Q1-5(45.721mm,57.96mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.254mm) Between Pad Q1-5(45.721mm,57.96mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.712/mm < 0.254/mm) Between Pad Q1-6(45.721mm,57.31mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R10-2(63mm,123.225mm) on Top Layer And Text "R21"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R12-2(3.225mm,123.225mm) on Top Layer And Text "R14"
Silk To Solder Mask Clearance Constraint: (0.147mm < 0.254mm) Between Pad R13-1(12.675mm,89.675mm) on Top Layer And Text "C48'
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R14-1(3.225mm,120mm) on Top Layer And Text "TP10"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R14-2(4.775mm,120mm) on Top Layer And Text "TP10"
Silk To Solder Mask Clearance Constraint: (0.178mm < 0.254mm) Between Pad R18-1(64.762mm,77.225mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.178mm < 0.254mm) Between Pad R18-2(64.762mm,78.775mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.096mm < 0.254mm) Between Pad R26-1(72mm,109mm) on Top Layer And Text "C21"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R26-2(72mm,107.45mm) on Top Layer And Text "C21"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R27-1(70mm,108.775mm) on Top Layer And Text "C21"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R27-2(70mm,107.225mm) on Top Layer And Text "C21"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R3-1(46.139mm,65.318mm) on Top Layer And Text "R4"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R3-2(47.689mm,65.318mm) on Top Layer And Text "R4"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R33-1(17.775mm,86mm) on Top Layer And Text "R32"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad R33-2(16.225mm,86mm) on Top Layer And Text "R32"
Silk To Solder Mask Clearance Constraint: (0.032mm < 0.254mm) Between Pad TP10-TP(1.762mm,117.648mm) on Top Layer And Text "U5'
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad U1-2(7.069mm,90.535mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.227mm < 0.254mm) Between Pad U1-2(7.069mm,90.535mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.246mm < 0.254mm) Between Pad U1-3(7.069mm,91.805mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.228mm < 0.254mm) Between Pad U1-3(7.069mm,91.805mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U13-6(14.678mm,108mm) on Top Layer And Text "C24"
Silk To Solder Mask Clearance Constraint: (0.152mm < 0.254mm) Between Pad U13-7(14.678mm,108.5mm) on Top Layer And Text "C24"
Silk To Solder Mask Clearance Constraint: (0.049mm < 0.254mm) Between Pad U1-7(2.269mm,90.535mm) on Top Layer And Text "C7"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U1-8(2.269mm,89.265mm) on Top Layer And Text "C7"
Silk To Solder Mask Clearance Constraint: (0.031mm < 0.254mm) Between Pad U23-5(47.095mm,88.275mm) on Top Layer And Text "C30"
Silk To Solder Mask Clearance Constraint: (0.036mm < 0.254mm) Between Pad U23-6(48.365mm,88.275mm) on Top Layer And Text "C30"
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad U23-7(49.635mm,88.275mm) on Top Layer And Text "C30"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-4(15.295mm,93.725mm) on Top Layer And Text "R13"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-5(15.295mm,88.275mm) on Top Layer And Text "C48"
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Pad U24-5(15.295mm,88.275mm) on Top Layer And Text "R33"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-6(16.565mm,88.275mm) on Top Layer And Text "C48"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-6(16.565mm,88.275mm) on Top Layer And Text "R33"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-7(17.835mm,88.275mm) on Top Layer And Text "R33'

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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U24-8(19.105mm,88.275mm) on Top Layer And Text "R33"
Silk To Solder Mask Clearance Constraint: (0.252mm < 0.254mm) Between Pad U2-9(76.725mm,65.555mm) on Top Layer And Text "U4"
Silk To Solder Mask Clearance Constraint: (0.193mm < 0.254mm) Between Pad U3-1(60.5mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad U3-1(60.5mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.243mm < 0.254mm) Between Pad U3-10(60.5mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.195mm < 0.254mm) Between Pad U3-10(60.5mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.206mm < 0.254mm) Between Pad U3-2(61mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.206mm < 0.254mm) Between Pad U3-3(61.5mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.206mm < 0.254mm) Between Pad U3-4(62mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.206mm < 0.254mm) Between Pad U3-5(62.5mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U3-5(62.5mm,81.6mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.195mm < 0.254mm) Between Pad U3-6(62.5mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U3-6(62.5mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.195mm < 0.254mm) Between Pad U3-7(62mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.195mm < 0.254mm) Between Pad U3-8(61.5mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.195mm < 0.254mm) Between Pad U3-9(61mm,86mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U4-1(78.164mm,60.752mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U4-2(78.764mm,60.752mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U4-3(78.764mm,62.192mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad U4-4(78.164mm,62.192mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U8-4(61.05mm,111.625mm) on Top Layer And Text "C17"
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad U8-5(62.95mm,111.625mm) on Top Layer And Text "C15"
Silk To Solder Mask Clearance Constraint: (0.116mm < 0.254mm) Between Pad U8-5(62.95mm,111.625mm) on Top Layer And Text "C17"

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Silk to Silk (Clearance=0.254mm) (AII),(AII)
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "1" (121.1mm,43.7mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "8" (130.2mm,34.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C15" (62.486mm,110.462mm) on Top Overlay And Text "C17"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C15" (62.486mm,110.462mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C2" (52.285mm,68.932mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "C24" (11.89mm,106.525mm) on Top Overlay And Text "C47"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C24" (11.89mm,106.525mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C24" (11.89mm,106.525mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.007mm < 0.254mm) Between Text "C31" (50.646mm,100.581mm) on Top Overlay And Text "U23'
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C48" (12.478mm,87.127mm) on Top Overlay And Text "R33"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C7" (2.645mm,88.535mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C7" (2.645mm,88.535mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.158mm < 0.254mm) Between Text "C7" (2.645mm,88.535mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C8" (74.469mm,63.311mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.152mm < 0.254mm) Between Text "C8" (74.469mm,63.311mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C9" (81.489mm,63.604mm) on Top Overlay And Text "R16"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "D1" (54.489mm,64.988mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "LED2" (45.97mm,73.438mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.04mm < 0.254mm) Between Text "P1" (98.903mm,73.338mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.238mm < 0.254mm) Between Text "R12" (2.749mm,126.241mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.044mm < 0.254mm) Between Text "R12" (2.749mm,126.241mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R13" (11.695mm,94.135mm) on Top Overlay And Text "U24"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R18" (67.524mm,76.061mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R26" (71.532mm,110.473mm) on Top Overlay And Text "R27"
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R30" (13.46mm,101.241mm) on Top Overlay And Text "R31"
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "R33" (15.776mm,87.475mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.191mm < 0.254mm) Between Text "R33" (15.776mm,87.475mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "U1" (1.124mm,94.745mm) on Top Overlay And Track

 $\label{lem:height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All) Height Constraint: Component K1-RELAY SPST 12V 10A (23mm,60mm) on Top Layer Actual Height = 64mm (Prefered=12.7mm) (All) (Prefered=12.7mm) (Prefered=12.7mm) (Prefered=12.7mm) (All) (Prefered=12.7mm) (Prefered=1$

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