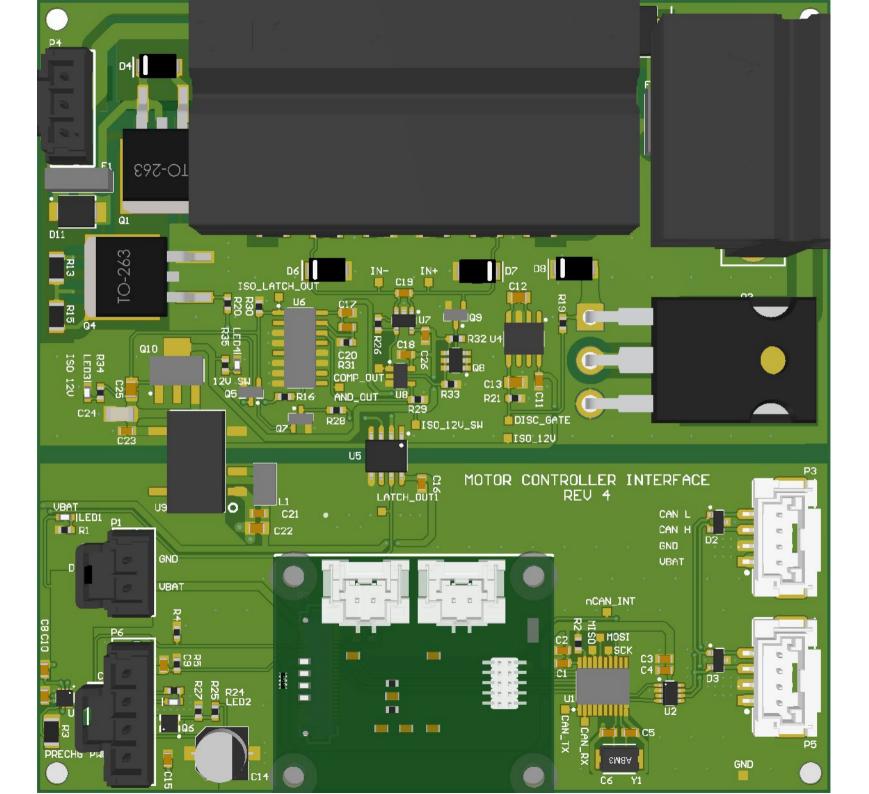
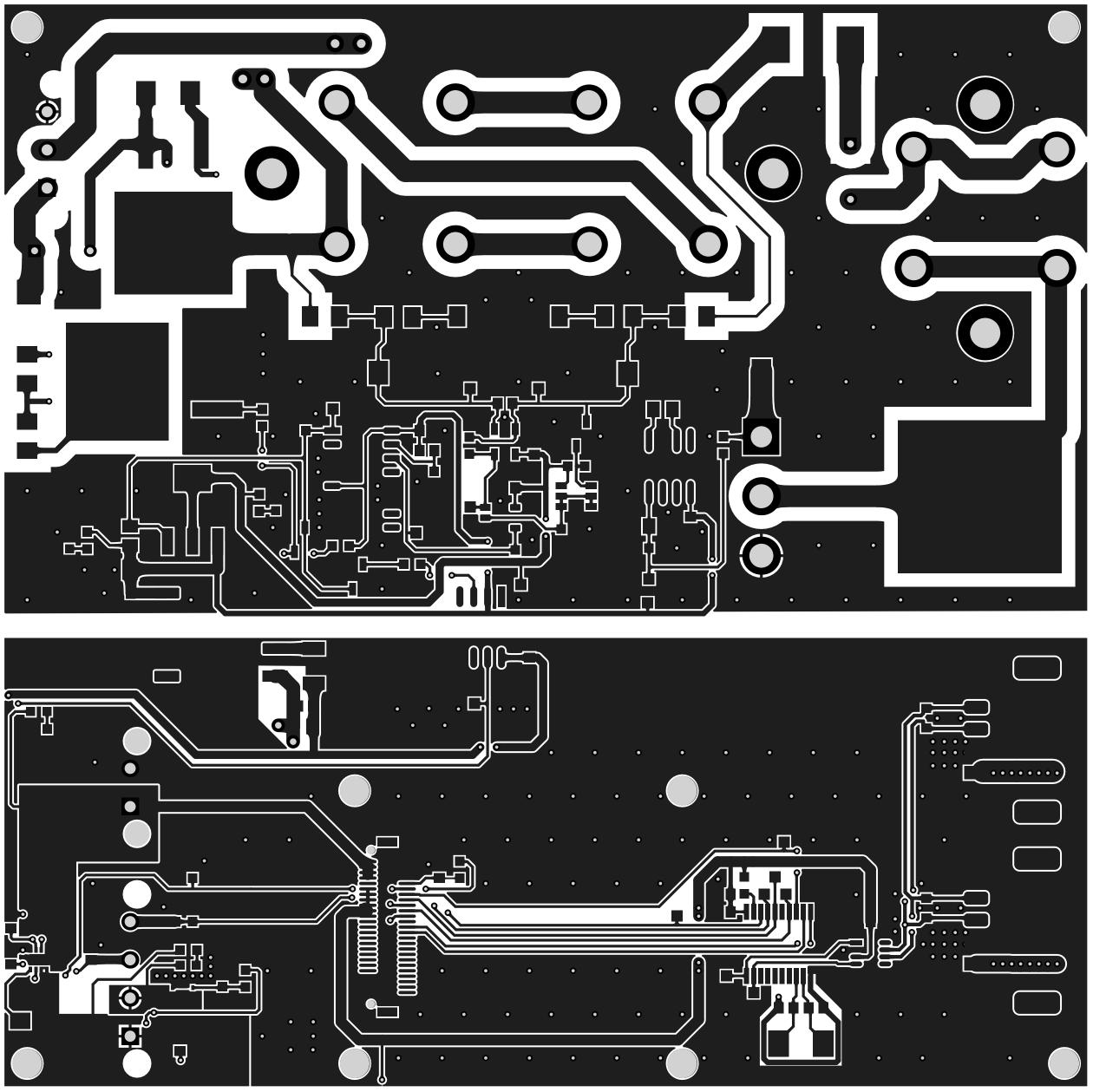


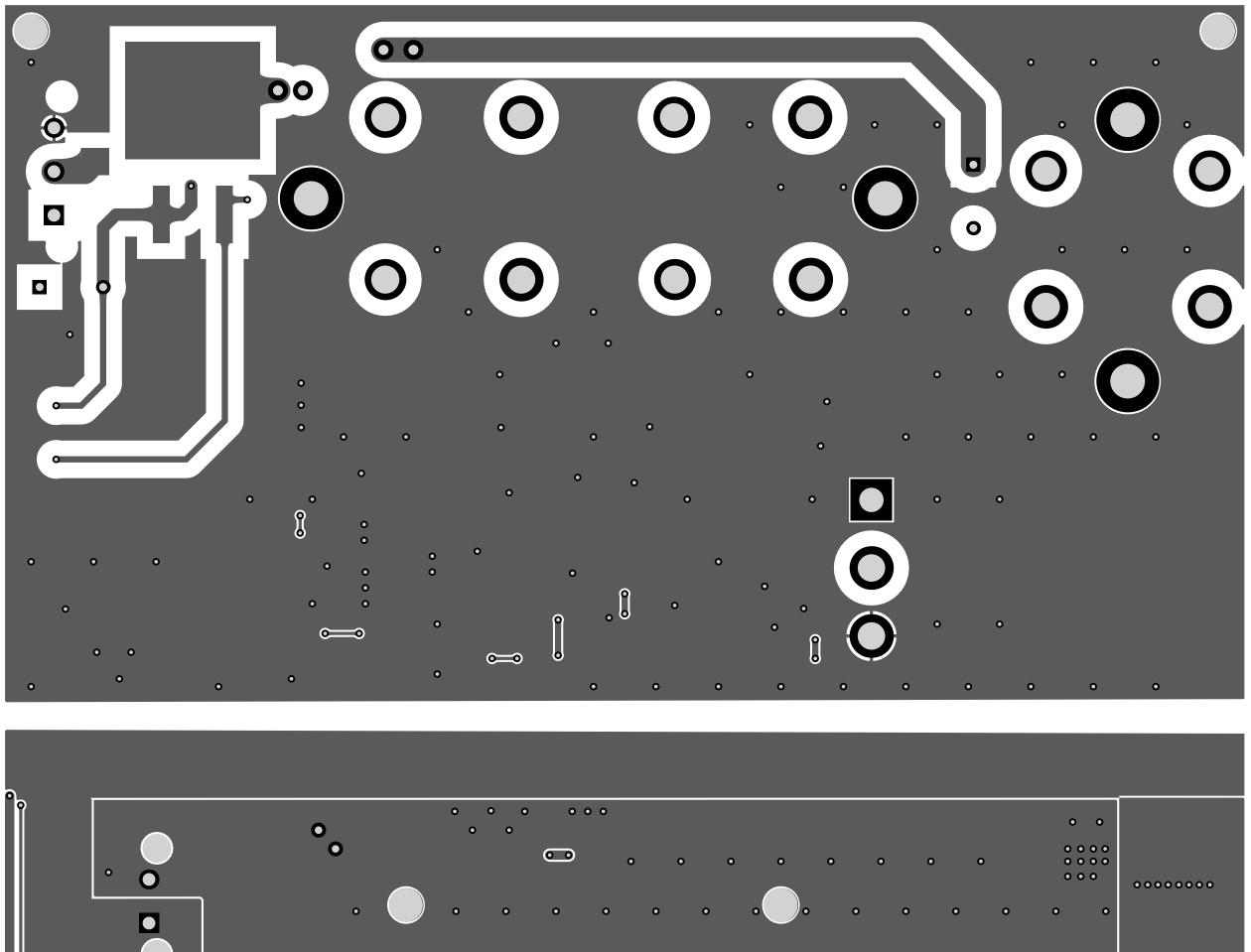
Bill of Materials			
Project:	PreCharge.PrjPcb		
Revision:	4.0		
Project Lead:	Taiping Li		
Generated On:	2019-01-18 6:34:35 PM		
Production Quantity:	1		
Currency	CAD		
Total Parts Count:	111		

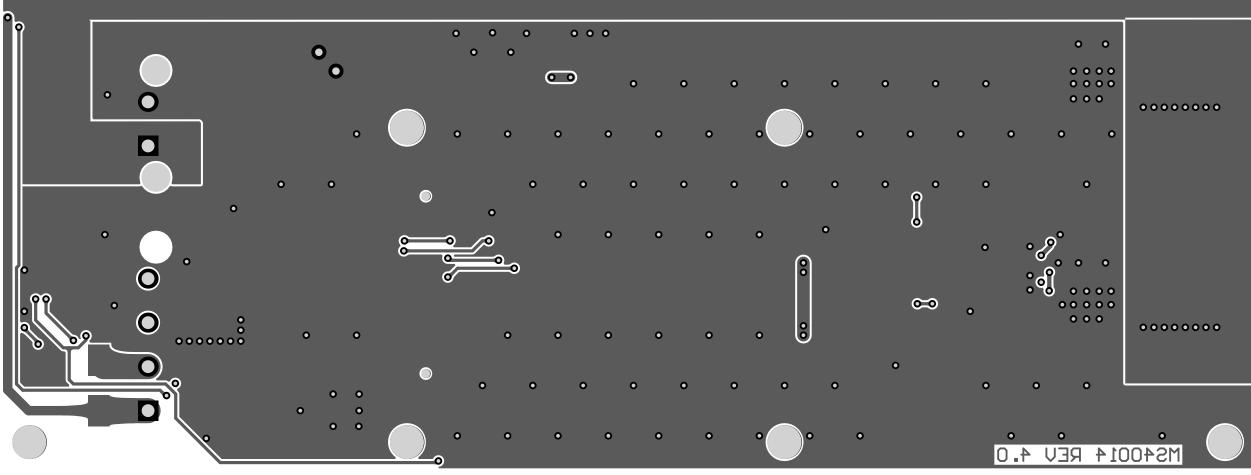


Total Parts Count:	111							
LibRef	Designator	Manufacturer 1	Manufacturer Part Number 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Quantity	Supplier Subtotal 1
CAP CER 0.1UF 50V 10% X7R 0603	C1	Kyocera AVX	06035C-104KAT2A	Digi-Key	478-5052-1-ND	0.23	1	\$ 0.23
CAP CER 1UF 50V 10% X7R 0603 CAP CER 4.7UF 25V 10% X5R 0603	C2 C3	Taiyo Yuden Murata	UMK107AB7105KA-T GRM188R61E475KE11D	Digi-Key Digi-Key	587-3247-1-ND 490-7203-1-ND	0.38 0.49	1	\$ 0.38 \$ 0.49
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 25PE 50V +5% C0G/NP0 0603	C4 C5	Kyocera AVX Samsung	06035C-104KAT2A CL10C250JB8NNNC	Digi-Key Digi-Key	478-5052-1-ND 1276-2244-1-ND	0.23 0.13	1	\$ 0.23 \$ 0.13
CAP CER 25PF 50V ±5% C0G/NP0 0603	C6	Samsung	CL10C250JB8NNNC	Digi-Key Digi-Key	1276-2244-1-ND	0.13	1	\$ 0.13
CAP CER 10nF 50V 5% X7R 0603 CAP CER 0 1UF 50V 10% X7R 0603	C7 C8	KEMET Kvocera AVX	C0603C103J5JACTU 06035C-104KAT2A	Digi-Key Digi-Key	399-13384-1-ND 478-5052-1-ND	0.49	1 1	\$ 0.49 \$ 0.23
CAP CER 10nF 50V 5% X7R 0603	C9 C10	KEMET	C0603C103J5JACTU	Digi-Key	399-13384-1-ND	0.49	1	\$ 0.49
CAP CER 6800pF 50V 10% X7R 0603 CAP CER 2.2UF 25V 10% X5R 0603	C11	Samsung Murata	CL10B682KB8SFNC GRM188R61E225KA12D	Digi-Key Digi-Key	1276-2103-1-ND 490-10731-1-ND	0.13 0.23	1	\$ 0.13 \$ 0.23
CAP CER 10uF 25V 10% X5R 0805 CAP CER 10uF 25V 10% X5R 0805	C12 C13	Murata Murata	GRM21BR61E106KA73L	Digi-Key Digi-Key	490-5523-1-ND 490-5523-1-ND	0.57 0.57	1	\$ 0.57 \$ 0.57
CAP ALUM 100UF 20% 35V SMD	C14	Panasonic	EEE1VA101XP	Digi-Key	PCE3951CT-ND	0.62	1	\$ 0.62
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 0.1UF 50V 10% X7R 0603	C15 C16	Kyocera AVX Kyocera AVX	06035C-104KAT2A	Digi-Key Digi-Key	478-5052-1-ND 478-5052-1-ND	0.23 0.23	1	\$ 0.23 \$ 0.23
CAP CER 0.1UF 50V 10% X7R 0603	C17	Kyocera AVX	06035C-104KAT2A	Digi-Key	478-5052-1-ND	0.23	1	\$ 0.23
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 680PF 50V C0G 0603	C18 C19	Kyocera AVX Samsung	06035C-104KAT2A CL10C681JB8NNNC	Digi-Key Digi-Key	478-5052-1-ND 1276-1820-1-ND	0.23 0.21	1	\$ 0.23 \$ 0.21
CAP CER 1UF 50V 10% X7R 0603	C20 C21	Taiyo Yuden	UMK107AB7105KA-T	Digi-Key	587-3247-1-ND 478-5052-1-ND	0.38 0.23	1	\$ 0.38 \$ 0.23
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 4.7UF 50V 10% X5R 0805	C22	Murata	GRT21BR61H475ME13L	Digi-Key Digi-Key	490-12395-1-ND	0.66	1	\$ 0.66
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 2.2UF 100V ±20% X7R 1206	C23 C24	Kyocera AVX Murata	06035C-104KAT2A GRM31CR72A225MA73I	Digi-Key Digi-Key	478-5052-1-ND 490-12773-1-ND	0.23	1	\$ 0.23
CAP CER 10uF 25V 10% X5R 0805	C25	Murata	GRM21BR61E106KA73L	Digi-Key	490-5523-1-ND	0.57	1	\$ 0.57
CAP CER 0.1UF 50V 10% X7R 0603 CAP CER 0.1UF 50V 10% X7R 0603	C26 C27	Kyocera AVX Kyocera AVX	06035C-104KAT2A 06035C-104KAT2A	Digi-Key Digi-Key	478-5052-1-ND 478-5052-1-ND	0.23 0.23	1	\$ 0.23 \$ 0.23
DIODE TVS 15VWM 24.4VC DO-214AA (SMB)	D1 D2	Taiwan Semiconductor	SMBJ15CA PESD1CAN 215	Digi-Key	SMBJ15CAFSCT-ND 1727-3817-1-ND	0.97	1	\$ 0.97
DIODE TVS 24VWM 70VC SOT23 DIODE TVS 24VWM 70VC SOT23	D2 D3	Nexperia Nexperia	PESD1CAN,215 PESD1CAN,215	Digi-Key Digi-Key	1727-3817-1-ND 1727-3817-1-ND	0.65 0.65	1	\$ 0.65 \$ 0.65
DIODE ZENER 16V 5W DO-214AA (SMB)	D4	MCC	SMBJ5353B-TP	Digi-Key	SMBJ5353B-TPMSCT- ND	0.9	1	\$ 0.90
DIODE GEN PURP 800V 8A SMC	D5	Diodes	S8KC-13	Digi-Key	S8KCDICT-ND	0.9	1	\$ 0.90
DIODE ZENER 16V 5W DO-214AA (SMB)	D6	MCC	SMBJ5353B-TP	Digi-Key	SMBJ5353B-TPMSCT- ND	0.9	1	\$ 0.90
DIODE ZENER 16V 5W DO-214AA (SMB)	D7	MCC	SMBJ5353B-TP	Digi-Key	SMBJ5353B-TPMSCT-	0.9	1	\$ 0.90
DIODE ZENER 16V 5W DO-214AA (SMB)	D8				ND SMBJ5353B-TPMSCT-	0.9	1	\$ 0.90
DIODE ZENER 16V 5W DO-214AA (SMB)  DIODE SCHOTTKY 60V 3A SMA	D8 D9	MCC Diodes	SMBJ5353B-TP B3604-13-F	Digi-Key Digi-Key	ND B360A-FDICT-ND	0.9	1	\$ 0.90
DIODE TVS 154V 246V DO-2144A (SMB)	D10	<u>Littelfuse</u>	TPSMB180CA	Digi-Key	F10356CT-ND	1.11	1	\$ 1.11
DIODE TVS 154V 246V DO-2144A (SMB) FUSE 3A 250VAC/450VDC RADIAL	D11 F1	Littelfuse Eaton	TPSMB180CA BK/PCC-3-R	Digi-Key Digi-Key	F10356CT-ND 283-2768-ND	1.11 3.2	1	\$ 1.11 \$ 3.20
FUSE 3A 250VAC/450VDC RADIAL	F2	Eaton	BK/PCC-3-R	Digi-Key	283-2768-ND	3.2	1	\$ 3.20
IND 6.8uH 260mA 20% 1210 LED GREEN CLEAR 2V 0603	L1 LED1	TDK Wurth Electronics	NLFV32T-6R8M-EF 150060VS75000	Digi-Key Digi-Key	445-15776-1-ND 732-4980-1-ND	0.58 0.19	1	\$ 0.58 \$ 0.19
LED BLUE CLEAR 2.8V 0603	LED2	Vishay Lite-On	LTST-C193TBKT-5A	Digi-Key	160-1827-1-ND	0.62	1	\$ 0.62
LED BLUE CLEAR 2.8V 0603 LED YELLOW CLEAR 2.1V 0603	LED3 LED4	Vishay Lite-On Wurth Electronics	150060YS75000	Digi-Key Digi-Key	160-1827-1-ND 732-4981-1-ND	0.62 0.19	1	\$ 0.62 \$ 0.19
HEATSINK 4xTO-247 FUSE HOLDER PC-TRON RADIAL	M1 M2	Wakefield	OMNI-UNI-30-50-D	Digi-Key	345-1574-ND	5.42	1	\$ 5.42
HEATSINK 2xTO-247_1	M!1	Wakefield	OMNI-UNI-30-25-D	Digi-Key	345-1573-ND	3.71	1	\$ 3.71
CONN 2POS ULTRA-FIT 0.138" CONN 50POS Bergstak Plug 0.02"	P1 P2	Molex Amphenol FCI	1722861302 10132797-055100LF	Digi-Key Digi-Key	WM11673-ND 609-5226-1-ND	1.94	1	\$ 1.94
CONN 4POS DURA-CLIK 0.079*	P3 P4	Molex Molex	560020-0420	Digi-Key	WM10864CT-ND WM11702-ND	2.2	1	\$ 2.20
CONN 3POS ULTRA-FIT 0.138" CONN 4POS DURA-CLIK 0.079"	P4 P5	Molex Molex	1722871103 560020-0420	Digi-Key Digi-Key	WM11702-ND WM10864CT-ND	1.09	1	\$ 1.09 \$ 2.20
CONN 4POS ULTRA-FIT 0.138" MOSFET P-CH 500V 10A 300W TO-263	P6 Q1	Molex IXYS	1722871104 IXTA10P50P	Digi-Key Digi-Key	WM11703-ND IXTA10P50P-ND	1.25 6.87	1 1	\$ 1.25 \$ 6.87
MOSFET P-CH 500V 10A 300W TO-263	Q2	IXYS	IXTA10P50P	Digi-Key	IXTA10P50P-ND	6.87	1	\$ 6.87
MOSFET DEPLETION N-CH 500V 6A TO247 MOSFET N-CH 650V 8A X2 TO-263	Q3 Q4	IXYS IXYS	IXTH6N50D2 IXTA8N65X2	Digi-Key Digi-Key	IXTH6N50D2-ND IXTA8N65X2-ND	8.35 3.29	1	\$ 8.35 \$ 3.29
MOSFET N-CH 60V 310MA SOT23	Q5	Diodes	DMN65D8L-7	Digi-Key	DMN65D8L-7DICT-ND IRLHS6342TRPBFCT-		1	
MOSFET N-CH 30V 8.7A 2.1W 6-PQFN (2x2)	Q6	Infineon	IRLHS6342TRPBF	Digi-Key	ND ND	0.89	1	\$ 0.89
MOSFET N-CH 60V 310MA SOT23 MOSFET P-CH 30V 4A 1.6W SOT-23-6	Q7 Q8	Diodes STMicroelectronics	DMN65D8L-7 STT4P3LLH6	Digi-Key Digi-Key	DMN65D8L-7DICT-ND 497-15521-1-ND	0.86	1	\$ 0.86
MOSFET N-CH 60V 310MA SOT23	Q9	Diodes	DMN65D8L-7	Digi-Key	DMN65D8L-7DICT-ND	0.50	1	0.00
MOSFET P-CH 60V 3A SOT223	Q10	Diodes	ZXMP6A17GQTA	Digi-Key	ZXMP6A17GQTADIDKR- ND	1.14	1	\$ 1.14
RES 10K OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R1 R2	Yageo Phycomp	RC0603FR-0710KL RC0603FR-0710KL	Digi-Key	311-10.0KHRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES 200 OHM 5% 2/3W 1206	R3	Yageo Phycomp Panasonic	ERJ-P08J201V	Digi-Key Digi-Key	P200ALCT-ND	0.23	1	\$ 0.23
RES 10K OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R4 R5	Yageo Phycomp	RC0603FR-0710KL RC0603FR-0710KI	Digi-Key Digi-Key	311-10.0KHRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES 18 OHM 100W 5% TO-247	R6	Ohmite	AP10118RJ	Digi-Key	AP101 18R J-ND	0.13	1	0.13
RES 18 OHM 100W 5% TO-247 RES 18 OHM 100W 5% TO-247	R7 R8	Ohmite Ohmite	AP10118RJ AP10118RJ	Digi-Key Digi-Key	AP101 18R J-ND AP101 18R J-ND		1	+
RES 18 OHM 100W 5% TO-247 RES 330 OHM 100W 5% TO-247	R9 R10	Ohmite Ohmite	AP10118RJ AP101330RJ	Digi-Key	AP101 18R J-ND AP101 330R J-ND		1	
RES SMD 470K OHM 0.1% 1/4W 1206	R10 R11	Susumu	RG3216P-4703-B-T1	Digi-Key Digi-Key	RG32P470KBDKR-ND	0.88	1	\$ 0.88
RES SMD 470K OHM 0.1% 1/4W 1206 RES SMD 29.4K OHM 1% 1/4W 1206	R12 R13	Susumu Panasonic	RG3216P-4703-B-T1 FR.I-8FNF2942V	Digi-Key Digi-Key	RG32P470KBDKR-ND P29.4KFCT-ND	0.88 0.15	1 1	\$ 0.88 \$ 0.15
RES 330 OHM 100W 5% TO-247	R14	Ohmite	AP101330RJ	Digi-Key	AP101 330R J-ND		1	
RES SMD 470K OHM 0.1% 1/4W 1206 RES 10K OHM 1% 1/10W 0603	R15 R16	Susumu Yaqeo Phycomp	RG3216P-4703-B-T1 RC0603FR-0710KL	Digi-Key Digi-Key	RG32P470KBDKR-ND 311-10.0KHRCT-ND	0.88 0.13	1	\$ 0.88 \$ 0.13
RES SMD 29.4K OHM 1% 1/4W 1206	R17	Panasonic	ERJ-8ENF2942V	Digi-Key	P29.4KFCT-ND	0.15	1	\$ 0.15
RES SMD 33K OHM 0.1% 1/4W 1206 RES 40.2 OHM 0.5% 1/10W 0603	R18 R19	Yageo Yageo	RT1206BRD0733KL RT0603DRE0740R2L	Digi-Key Digi-Key	YAG2038CT-ND 311-2576-1-ND	0.82 0.16	1	\$ 0.82 \$ 0.16
RES 22.1 OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R20 R21	Yageo Yageo Phycomp	RC0603FR-0722R1L RC0603FR-0710KL	Digi-Key Digi-Key	311-22.1HRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES SMD 0 OHM JUMPER 1/4W 1206	R22	Vishay	CRCW12060000Z0EA	Digi-Key	541-0.0ECT-ND	0.13	1	\$ 0.13
RES SMD 2K OHM 0.1% 1/4W 1206 RES 10K OHM 1% 1/10W 0603	R23 R24	Panasonic Yageo Phycomn	ERA8AEB202V RC0603FR-0710KI	Digi-Key Digi-Key	P2.0KBCCT-ND 311-10.0KHRCT-ND	0.88 0.13	1	\$ 0.88 \$ 0.13
RES 22.1 OHM 1% 1/10W 0603	R25	Yageo	RC0603FR-0722R1L	Digi-Key	311-22.1HRCT-ND	0.13	1	\$ 0.13
RES 1M OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R26 R27	Yageo Yageo Phycomp	RC0603FR-071ML RC0603FR-0710KL	Digi-Key Digi-Key	311-1.00MHRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1 1	\$ 0.13 \$ 0.13
RES 1K OHM 5% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R28 R29	Yageo Yageo Phycomp	RC0603JR-071KL RC0603FR-0710KL	Digi-Key	311-1.0KGRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES 10K OHM 1% 1/10W 0603	R30	Yageo Phycomp	RC0603FR-0710KL	Digi-Key Digi-Key	311-10.0KHRCT-ND	0.13	1	\$ 0.13
RES 10K OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R31 R32	Yageo Phycomp Yageo Phycomp	RC0603FR-0710KL RC0603FR-0710KL	Digi-Key Digi-Key	311-10.0KHRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES 10K OHM 1% 1/10W 0603	R33	Yageo Phycomp	RC0603FR-0710KL	Digi-Key	311-10.0KHRCT-ND	0.13	1	\$ 0.13
RES 10K OHM 1% 1/10W 0603 RES 10K OHM 1% 1/10W 0603	R34 R35	Yageo Phycomp Yageo Phycomp	RC0603FR-0710KL RC0603FR-0710KL	Digi-Key Digi-Key	311-10.0KHRCT-ND 311-10.0KHRCT-ND	0.13 0.13	1	\$ 0.13 \$ 0.13
RES 1K OHM 5% 1/10W 0603	R36	Yageo Yageo	RC0603JR-071KL	Digi-Key	311-1.0KGRCT-ND	0.13	1	\$ 0.13
Test Point IC CAN SPI CONTROLLER MCP2515T-I/ST 20-	TP1 U1	Mary 11	MCP2515T-I/ST	District	MODORART POTOT : -	2.72	1	
TSSOP IC CAN Tranceiver TCAN332DR	U1 U2	Microchip Texas Instruments	MCP2515T-I/ST TCAN332DR	Digi-Key Digi-Key	MCP2515T-I/STCT-ND 296-43711-1-ND	2.72	1	\$ 2.72 \$ 2.74
IC CAN Tranceiver TCAN332DR IC LOAD SWITCH ACT-HI 10.5A 8DFN	U3	ON Semiconductor	NCP45521IMNTWG-H	Digi-Key Digi-Key	NCP45521IMNTWG-	1.15	1	\$ 2.74
IC REG SWTCHD CAP INV 20MA 8SOIC	U4	ON Semiconductor Microchip	TC7662BEOA713	Digi-Key Digi-Key	HOSDKR-ND TC7662BEQA713CT-ND	3.04	1	\$ 1.15
IC OPTOISOLATOR 3.75KV PUSH PULL 8-SOIC	U5	Broadcom Avago	HCPL-0211-000E	Digi-Key	516-1625-5-ND		1	
IC SR LATCH 4CH 16SOIC IC COMPARATOR TLV1701 SOT-23-5	U6 U7	Texas Instruments Texas Instruments	CD4043BDR TLV1701QDBVRQ1	Digi-Key Digi-Key	296-31496-1-ND 296-47458-1-ND	0.64 1.78	1	\$ 0.64 \$ 1.78
IC AND GATE 1CH 5-SSOP IC DCDC ISOLATED 12V 1W 8-SMD 5-LEAD	U8	Rohm	BU4S81G2-TR	Digi-Key	BU4S81G2CT-ND	1.23	1	\$ 1.23
IC DCDC ISOLATED 12V 1W 8-SMD 5-LEAD CRYSTAL 16 MHz 18PF 2-SMD	U9 Y1	XP Power Abracon	ISE1212A ABM3-16.000MHZ-D2Y-T	Digi-Key Digi-Key	1470-2950-1-ND 535-10638-1-ND	5.64 0.91	1	\$ 5.64 \$ 0.91
							Total:	\$ 100.40









## **Electrical Rules Check Report**

Class	Document	Message
Warning	Motor Interface - Pre-Charge	3V3 contains Output Port and Unspecified Port objects (Port 3V3,Port
	Interface.SchDoc	CONTACTOR_SENSE_OUT)
Warning	Motor Interface - Pre-Charge	Nets Wire 3V3 has multiple names (Power Object 3V3,Power Object 3V3,Power Object
	Interface.SchDoc	3V3,Port 3V3,Port CONTACTOR_SENSE_OUT)
Warning	Motor Interface - Top Sheet.SchDoc	Nets Wire 3V3 has multiple names (Power Object 3V3,Power Object 3V3,Power Object
		3V3,Power Object 3V3,Power Object 3V3,Power Object 3V3,Power Object 3V3,Power Object
		3V3,Power Object 3V3,Power Object 3V3,Power Object 3V3,Sheet Entry U_Motor Interface -
		CAN-3V3(Passive), Sheet Entry U_Motor Interface - Connectors-3V3(Passive), Sheet Entry
		U_Motor Interface - Connectors-CONTACTOR_SENSE_OUT(Input),Sheet Entry U_Motor
		Interface - Pre-Charge Interface-3V3(Passive), Sheet Entry U_Motor Interface - Pre-Charge
		Interface-CONTACTOR_SENSE_OUT(Output),Port 3V3,Port 3V3,Port 3V3,Port
		CONTACTOR_SENSE_OUT,Port CONTACTOR_SENSE_OUT)
Warning	Motor Interface - Pre-Charge HV.SchDoc	Off grid at 1800.611mil,7392.532mil
Warning	Motor Interface - Pre-Charge	Off grid at 6303.039mil,8262.396mil
	HV.SchDoc	5 g a. 55557557
Warning	Motor Interface - Pre-Charge	Off grid at 12010.97mil,8056.662mil
	HV.SchDoc	

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**Design Rules Verification Report**Filename : C:\Users\Taiping\Documents\MidnightSun\hardware\MSXII\_PreChargeControlle Warnings 0 Rule Violations 104

## Warnings Total

Rule Violations	
Clearance Constraint (Gap=0.152mm) (All),(All)	0
Clearance Constraint (Gap=1.25mm) (InNetClass('HV') and Not (InNet('IN+') or InNet('IN-'))),(InNet('*'))	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( (All) )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.152mm) (Max=10mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Minimum Annular Ring (Minimum=0.05mm) (All)	0
Hole Size Constraint (Min=0.3mm) (Max=6.3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.178mm) (IsPad),(All)	65
Silk to Silk (Clearance=0.254mm) (All),(All)	37
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	2
Height Constraint (Min=0mm) (Max=200mm) (Prefered=12.7mm) (All)	0
Total	104

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Silk To Solder Mask (Clearance=0.178mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (30.841mm,77.6mm) on Top Overlay And Pad R6-1(30.841mm,77.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (30.841mm,90.6mm) on Top Overlay And Pad R8-1(30.841mm,90.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (41.721mm,77.6mm) on Top Overlay And Pad R6-2(41.721mm,77.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (41.721mm,90.6mm) on Top Overlay And Pad R8-2(41.721mm,90.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (53.961mm,77.6mm) on Top Overlay And Pad R7-1(54mm,77.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (53.961mm,90.6mm) on Top Overlay And Pad R9-1(53.941mm,90.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (64.841mm,77.6mm) on Top Overlay And Pad R7-2(64.88mm,77.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (64.841mm,90.6mm) on Top Overlay And Pad R9-2(64.821mm,90.6mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (83.741mm, 75.43mm) on Top Overlay And Pad R10-2(83.7mm, 75.42mm)
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (83.741mm,86.31mm) on Top Overlay And Pad R10-1(83.7mm,86.3mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (96.741mm,75.46mm) on Top Overlay And Pad R14-2(96.8mm,75.42mm)
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Arc (96.741mm,86.34mm) on Top Overlay And Pad R14-1(96.8mm,86.3mm) on
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad C1-2(65.557mm,16.374mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.134mm < 0.178mm) Between Pad C16-2(48.5mm,38.355mm) on Top Layer And Text "LATCH_OUT1"
Silk To Solder Mask Clearance Constraint: (0.077mm < 0.178mm) Between Pad C17-1(39.779mm,60.525mm) on Top Layer And Text "C17"
Silk To Solder Mask Clearance Constraint: (0.077mm < 0.178mm) Between Pad C17-2(38.429mm,60.525mm) on Top Layer And Text "C17"
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad C2-2(65.575mm,18mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad C7-2(8.333mm,13.35mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad COMP_OUT-TP(43.08mm,53.5mm) on Top Layer And Text "COMP_OUT
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.178mm) Between Pad D9-1(8mm,9mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.178mm) Between Pad D9-1(8mm,9mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.178mm) Between Pad D9-2(8mm,5mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.166mm < 0.178mm) Between Pad D9-2(8mm,5mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.115mm < 0.178mm) Between Pad MOSI-TP(70.989mm,19.609mm) on Top Layer And Text "MISO"
Silk To Solder Mask Clearance Constraint: (0.119mm < 0.178mm) Between Pad P3-7(95mm,25.55mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.16mm < 0.178mm) Between Pad P3-7(95mm,38.75mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.16mm < 0.178mm) Between Pad P5-7(95mm,21.25mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.119mm < 0.178mm) Between Pad P5-7(95mm,8.05mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad Q5-2(26.025mm,51.635mm) on Top Layer And Text "12V SW"
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.178mm) Between Pad Q6-1(17.656mm,8.15mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.178mm) Between Pad Q6-2(17.656mm,8.8mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.178mm) Between Pad Q6-2(17.656mm,8.8mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.178mm) Between Pad Q6-3(17.656mm,9.45mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.178mm) Between Pad Q6-4(15.806mm,9.45mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.178mm) Between Pad Q6-5(15.806mm,8.8mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.112mm < 0.178mm) Between Pad Q6-5(15.806mm,8.8mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.178mm) Between Pad Q6-6(15.806mm,8.15mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.067mm < 0.178mm) Between Pad R29-2(47.2mm,49.575mm) on Top Layer And Text "U8" (45.3mm,49.8mm
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U2-1(78.425mm, 13.575mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.177mm < 0.178mm) Between Pad U2-2(78.425mm, 12.925mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.177mm < 0.178mm) Between Pad U2-3(78.425mm, 12.275mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.177mm < 0.178mm) Between Pad U2-4(78.425mm, 11.625mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U2-5(81.075mm,11.625mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U2-6(81.075mm,12.275mm) on Top Laver And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U2-7(81.075mm,12.925mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U2-8(81.075mm, 13.575mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.162mm < 0.178mm) Between Pad U3-1(4.3mm,12.9mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.139mm < 0.178mm) Between Pad U3-4(2.8mm,12.9mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.131mm < 0.178mm) Between Pad U3-5(2.8mm,11.1mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.155mm < 0.178mm) Between Pad U3-8(4.05mm,11.25mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.155mm < 0.178mm) Between Pad U3-8(4.3mm.11.1mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.178mm) Between Pad U3-9(3.55mm,12mm) on Top Layer And Track
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Silk To Solder Mask (Clearance=0.178mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad U5-1(45.939mm,45.295mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad U5-2(44.669mm,45.295mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad U5-3(43.399mm,45.295mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.178mm) Between Pad U5-4(42.129mm,45.295mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0mm < 0.178mm) Between Pad U5-5(42.129mm,39.705mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0mm < 0.178mm) Between Pad U5-6(43.399mm,39.705mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0mm < 0.178mm) Between Pad U5-7(44.669mm,39.705mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0mm < 0.178mm) Between Pad U5-8(45.939mm,39.705mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.178mm) Between Pad U7-1(47.2mm,60.65mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.178mm) Between Pad U7-2(46.25mm,60.65mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.178mm) Between Pad U7-3(45.3mm,60.65mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.178mm) Between Pad U7-4(45.3mm,58.35mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.178mm) Between Pad U7-5(47.2mm,58.35mm) on Top Layer And Track

Silk to Silk (Clearance=0.254mm) (All),(All)
Silk To Silk Clearance Constraint: (0.075mm < 0.254mm) Between Text "C16" (49.984mm,39.905mm) on Top Overlay And Text "LATCH_OUT1"
Silk To Silk Clearance Constraint: (0.221mm < 0.254mm) Between Text "C7" (7.749mm,14.382mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "D11" (1.667mm,70mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.227mm < 0.254mm) Between Text "D2" (84.366mm,31.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.126mm < 0.254mm) Between Text "D4" (10.5mm,91.193mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.221mm < 0.254mm) Between Text "D7" (59.124mm,65.4mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.237mm < 0.254mm) Between Text "D8" (62.696mm,65.687mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.192mm < 0.254mm) Between Text "F1" (8.261mm,78.6mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.241mm < 0.254mm) Between Text "F1" (8.261mm,78.6mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.144mm < 0.254mm) Between Text "ISO_LATCH_OUT" (25.475mm,63.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.238mm < 0.254mm) Between Text "L1" (30.57mm,36.327mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.238mm < 0.254mm) Between Text "L1" (30.57mm,36.327mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.245mm < 0.254mm) Between Text "LED1" (5.4mm,34.355mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.251mm < 0.254mm) Between Text "LED2" (23.951mm,11.099mm) on Top Overlay And Text "R24" (23.9mm,12.3mm)
Silk To Silk Clearance Constraint: (0.211mm < 0.254mm) Between Text "LED3" (5.93mm,55.177mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.095mm < 0.254mm) Between Text "LED4" (24.8mm,58.547mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.077mm < 0.254mm) Between Text "M!1" (93mm,93.739mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.077mm < 0.254mm) Between Text "M!1" (93mm,93.739mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.136mm < 0.254mm) Between Text "P1" (9.45mm,33.7mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.117mm < 0.254mm) Between Text "P1" (9.45mm,33.7mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.177mm < 0.254mm) Between Text "P6" (9.45mm,19.875mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.173mm < 0.254mm) Between Text "P6" (9.45mm,19.875mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.217mm < 0.254mm) Between Text "Q5" (23.75mm,49.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.25mm < 0.254mm) Between Text "Q6" (18.356mm,7.8mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.169mm < 0.254mm) Between Text "Q9" (54.6mm,59.525mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.14mm < 0.254mm) Between Text "Q9" (54.6mm,59.525mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R14" (94.191mm,89.323mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.22mm < 0.254mm) Between Text "U1" (66.566mm,11.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.22mm < 0.254mm) Between Text "U1" (66.566mm,11.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.232mm < 0.254mm) Between Text "U2" (79.177mm,9.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "U2" (79.177mm,9.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "U2" (79.177mm,9.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.147mm < 0.254mm) Between Text "U4" (57.2mm,56.82mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.245mm < 0.254mm) Between Text "U6" (32.398mm,61.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.206mm < 0.254mm) Between Text "U8" (45.3mm,49.8mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.205mm < 0.254mm) Between Text "U8" (45.3mm,49.8mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.205mm < 0.254mm) Between Text "U8" (45.3mm,49.8mm) on Top Overlay And Track

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Board Clearance Constraint (Gap=0mm) (All)
Board Outline Clearance(Outline Edge): (Collision < 0.406mm) Between Board Edge And Track (30mm,0mm)(30mm,30mm) on Top Overlay

Board Outline Clearance(Outline Edge): (Collision < 0.406mm) Between Board Edge And Track (65mm,0mm)(65mm,30mm) on Top Overlay

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