



Automated Temporal Verification with Extended Regular Expressions

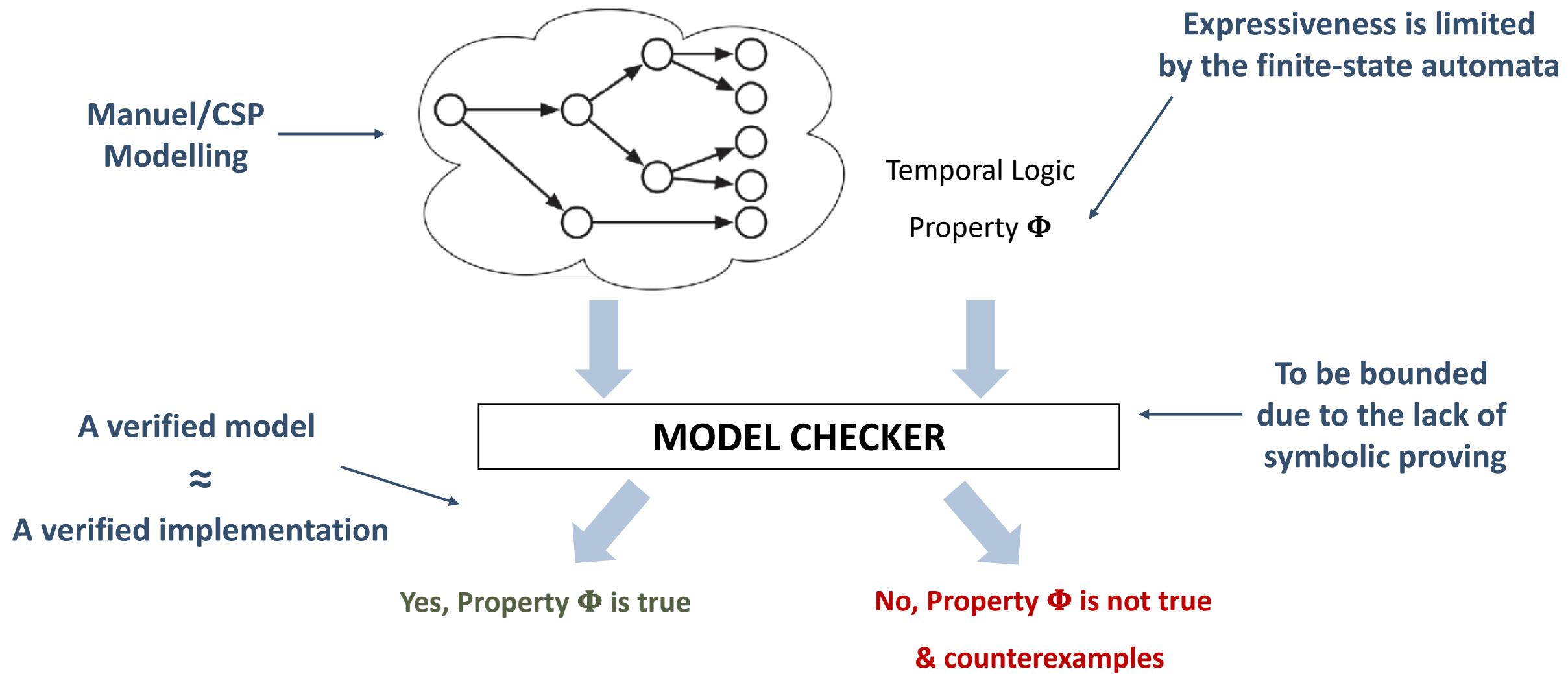
PhD Candidate: Yahui Song

Supervisor: Prof. Wei Ngan Chin

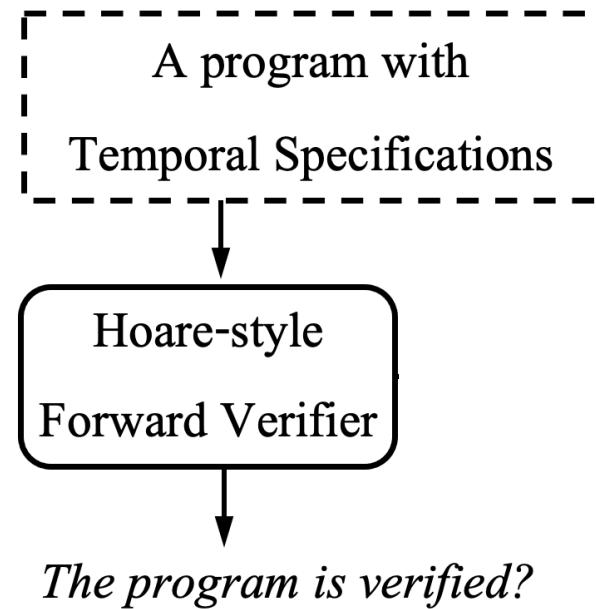
Examiners: Prof. Jin Song Dong and Prof. Joxan Jaffar

HoD Representative: Prof. Siau Cheng Khoo

Temporal Verification – Existing Framework

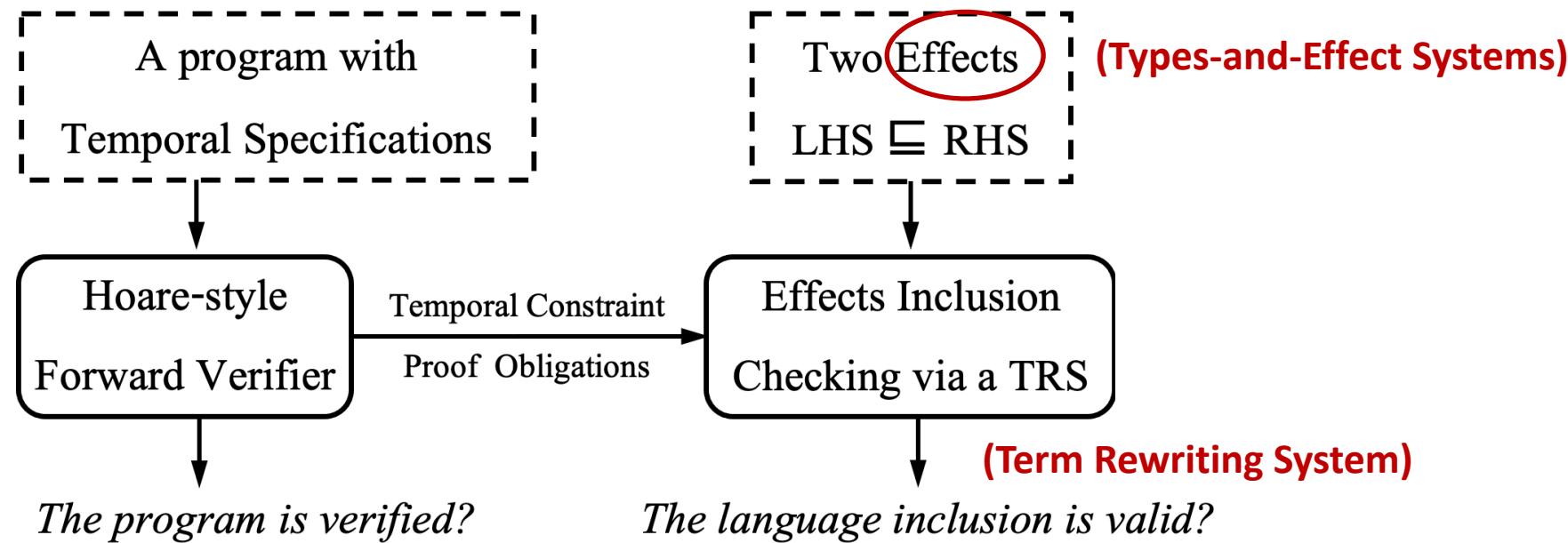


A New Framework for Temporal Verification



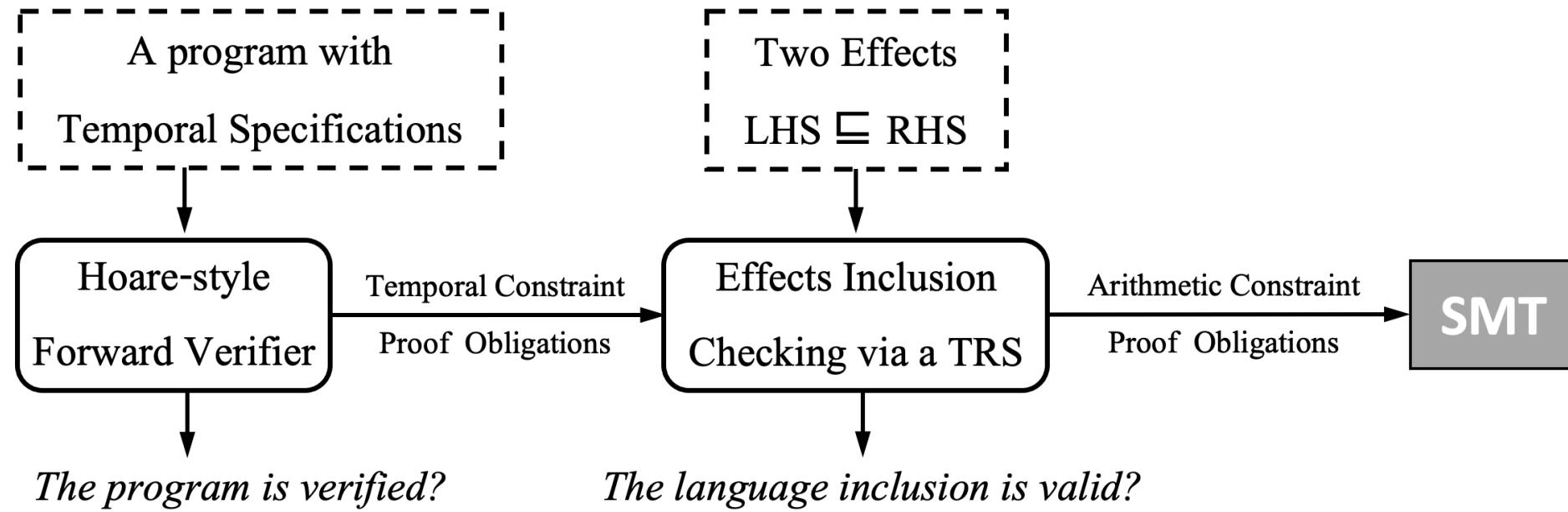
- + A verified implementation;
- + Flexible specifications, which can be combined with other logic;

A New Framework for Temporal Verification



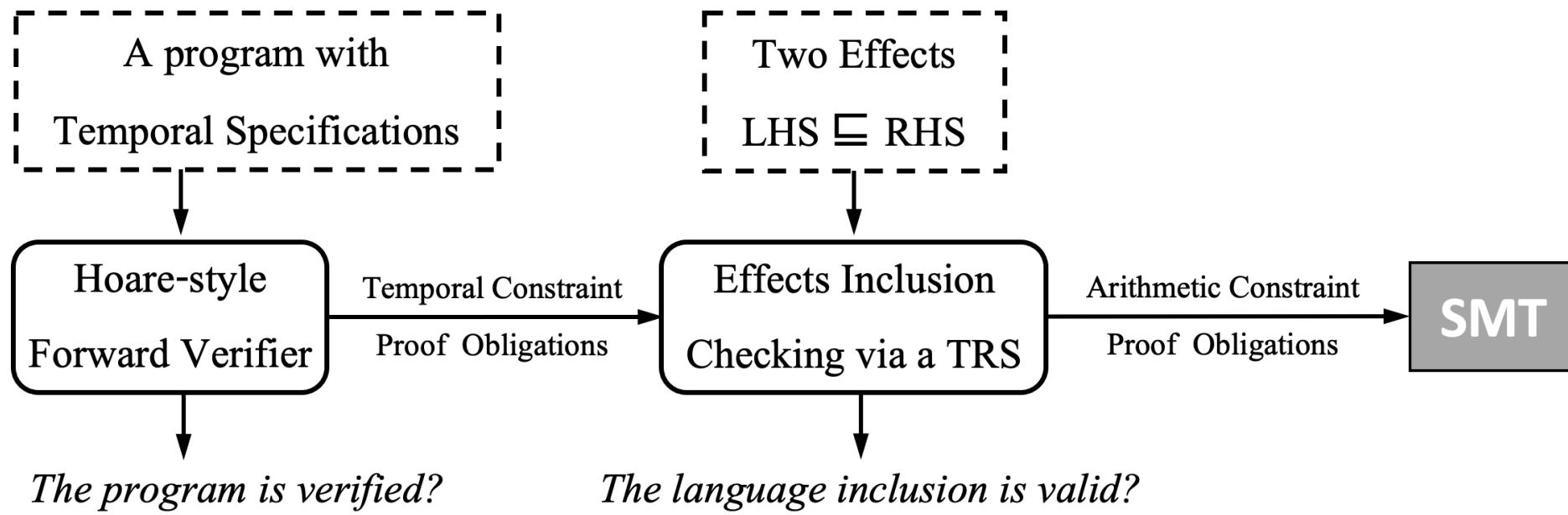
- + A verified implementation;
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- + Efficient symbolic entailment checker with (co-)inductive proofs;

A New Framework for Temporal Verification



- + A verified implementation;
- + Flexible specifications, which can be combined with other logic;
- + Efficient symbolic entailment checker with (co-)inductive proofs;
- Automation/Decidability.

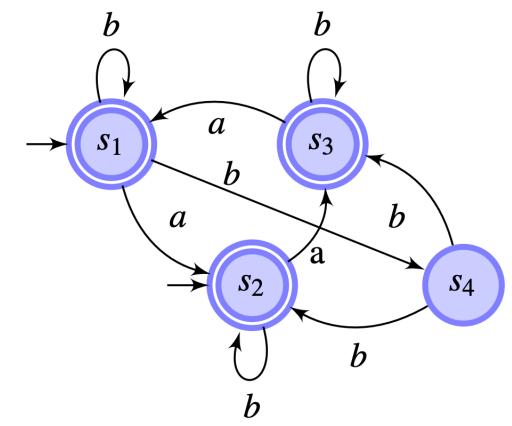
A New Framework for Temporal Verification



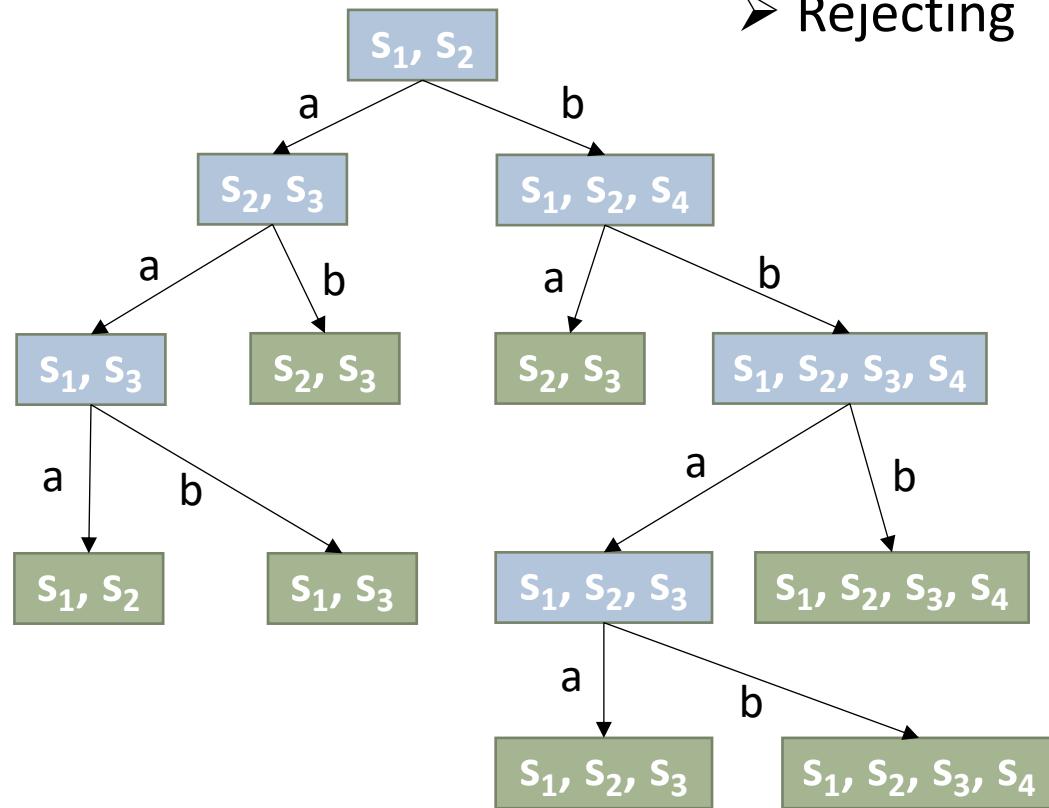
- + A verified implementation;
- + **Flexible specifications**, which can be combined with other logic;
- + **Efficient** symbolic entailment checker with (co-)inductive proofs;
- Automation/Decidability.

Automata vs. RE : $\Sigma^* \subseteq L(A)$

Flexibility and Efficiency



- Init/Next
- Processed
- Rejecting



Automata vs. RE : $\Sigma^* \subseteq L(A)$

Flexibility and Efficiency

Antimirov algorithm for solving REs' inclusions

Definition 1 (Derivatives). Given any formal language S over an alphabet Σ and any string $u \in \Sigma^*$, the derivatives of S w.r.t u is defined as: $u^{-1}S = \{w \in \Sigma^* \mid uw \in S\}$.



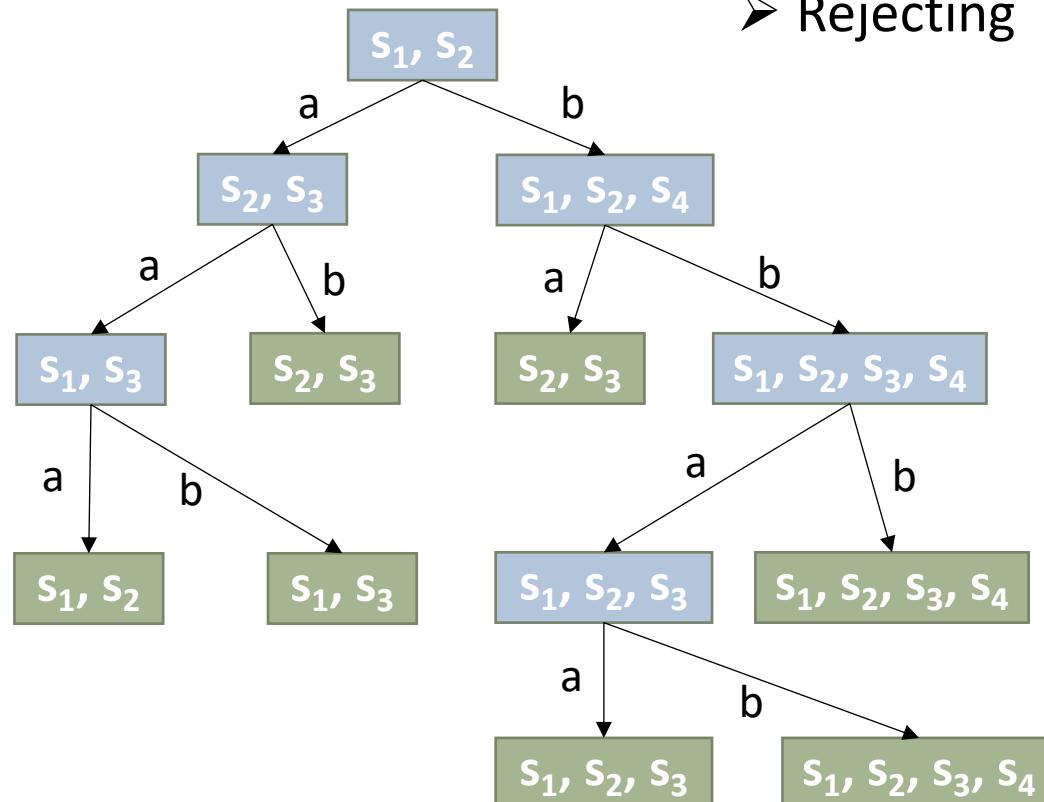
Definition 2 (Regular Expression Inclusion). For REs r and s ,

$$r \leq s \Leftrightarrow \forall A \in \Sigma. A^{-1}(r) \leq A^{-1}(s).$$

Automata vs. RE : $\Sigma^* \subseteq L(A)$

Flexibility and Efficiency

- Init/Next → ➤ First/Derivatives
- Processed → ➤ Proof Context
- Rejecting → ➤ Null-able/Infinite-able



$$\begin{array}{c}
 (a \vee b)^* \subseteq (a \vee b \vee bb)^* \quad [\text{Reoccur}] \\
 \hline
 \varepsilon \cdot (a \vee b)^* \subseteq \varepsilon \cdot (a \vee b \vee bb)^* \quad [\text{Reoccur}] \\
 \hline
 \color{red} a \cdot (a \vee b)^* \subseteq (a \vee b \vee bb)^* \quad \color{red} b \cdot (a \vee b)^* \subseteq \dots \\
 \hline
 (a \vee b)^* \subseteq (a \vee b \vee bb)^*
 \end{array}$$

Proposals Overview

	Target Language	Specification Language	Applied Domain	Research Paper
1	C	IntegratedEffs	General Effectful Programs	(ICFEM 2020)
2	Imp ^{a/s}	SyncEffs	Synchronous Programming	(VMCAI 2021)
3	C ^t	TimEffs	Time Critical Systems	(TACAS 2023)
4	λ_h	ContEffs	Algebraic Effects and Handlers	(APLAS 2022)

Main Challenges

- ❖ Customized forward verifier: to closely capture the semantics of given program;
- ❖ Customized TRS: to solve specifications on different expressiveness level;
- ❖ Soundness and termination proofs for forward verifiers and TRSs.

Outline

- 1. DependentEffs : General Effectful Programs
 - Mixed finite (inductive) and infinite (coinductive) traces
- 2. SyncEffs: Synchronous Programming
- 3. TimEffs: Time Critical Systems
- 4. ContEffs Algebraic Effects and Handlers
- 5. Conclusion and the Future Work

Integrated Dependent Effects

```
send n =  
    if n == 0 then event [Done];  
    else event [Send];  
        send (n - 1);
```

Φ' = (Send[★] • Done, Send^ω) [Hofmann, Martin, and Wei Chen. 2014]

Φ'' = (Send[■] • Done, Send^ω) [Nanjo, Yoji, et al. 2018]

Integrated Dependent Effects

```
send n =  
    if n == 0 then event [Done];  
    else event [Send];  
        send (n - 1);
```

Φ' = (Send * • Done, Send $^\omega$) [Hofmann, Martin, and Wei Chen. 2014]

Φ'' = (Send n • Done, Send $^\omega$) [Nanjo, Yoji, et al. 2018]

Φ_{pre} = **True** \wedge **Ready** \cdot _ *

$\Phi_{\text{post}}(n)$ = **n** ≥ 0 \wedge (Send n • Done) \vee **n** < 0 \wedge (Send $^\omega$)

Integrated Dependent Effects

```
send n =  
    if n == 0 then event [Done];  
    else event [Send];  
    send (n - 1);
```

```
server n =  
    event [Ready];  
    send (n);  
    server (n);
```

$$\Phi' = (\text{Send}^* \cdot \text{Done}, \text{Send}^\omega)$$

$$\Phi'' = (\text{Send}^n \cdot \text{Done}, \text{Send}^\omega)$$

$$\Phi_{\text{pre}} = \text{True} \wedge \text{Ready} \cdot \underline{_}^*$$

$$\Phi_{\text{post}}(n) = n \geq 0 \wedge (\text{Send}^n \cdot \text{Done}) \vee n < 0 \wedge (\text{Send}^\omega)$$

$$\Phi'_{\text{pre}} = n \geq 0 \wedge \epsilon$$

$$\Phi'_{\text{post}}(n) = n \geq 0 \wedge (\text{Ready} \cdot \text{Send}^n \cdot \text{Done})^\omega$$

$$\Phi'_{\text{pre}} = \text{True} \wedge \epsilon$$

$$\Phi'_{\text{post}}(n) = n \geq 0 \wedge (\text{Ready} \cdot \text{Send}^n \cdot \text{Done})^\omega$$

$$\vee n < 0 \wedge (\text{Ready} \cdot \text{Send}^\omega)$$

Integrated Dependent Effects

				Classic Regular Expressions
(Effects)	$\Phi ::= \pi \wedge \text{es} \mid \Phi_1 \vee \Phi_2 \mid \exists x. \Phi$			
(Event Sequences)	$\text{es} ::= \perp \mid \epsilon \mid \underline{\text{a}} \mid \text{es}_1 \cdot \text{es}_2 \mid \text{es}_1 \vee \text{es}_2 \mid - \mid \boxed{\text{es}^t \mid \text{es}^* \mid \text{es}^\omega}$			
(Pure Formulae)	$\pi ::= \text{True} \mid \text{False} \mid A(t_1, t_2) \mid \pi_1 \wedge \pi_2 \mid \pi_1 \vee \pi_2$			
	$\mid \neg \pi \mid \pi_1 \Rightarrow \pi_2 \mid \forall x. \pi \mid \exists x. \pi$			
x ::∈ var	n ::∈ ℤ	(Event) $\underline{\text{a}} ::\in \Sigma$	(Infinity) ω	(Kleene Star) $*$

1. Aware of termination (mixed definition): $(n \geq 0 \wedge \text{Send}^n \cdot \text{Done}) \vee (n < 0 \wedge \text{Send}^\omega)$
2. Beyond the context-free grammar: $a^n \cdot b^n \cdot c^n$
3. Effects in precondition is new: $\Phi_{\text{pre}} = \text{True} \wedge \text{Ready} \cdot -^*$
4. Undetermined termination (Kleene Star): $\text{True} \wedge \text{Send}^* \cdot \text{Done}$

Implementation and Evaluation

- An open-sourced prototype system using OCaml.
- Benchmark: 16 IOT programs implemented in C for Arduino controlling programs:
 - derive temporal properties (in total 235 properties with 124 valid and 111 invalid)
 - express these properties using both LTL formulae and our effects,
 - we record the total computation time using PAT and our TRS.

Implementation and Evaluation

Table 5. The experiments are based on 16 real world C programs, we record the lines of code (LOC), the number of testing temporal properties (#Prop.), and the (dis-) proving times (in milliseconds) using PAT and our T.r.s respectively.

- An open source library for temporal logic
- Benchmarking 16 real-world C programs
 - derived from GitHub
 - expressiveness
 - we report

Programs	LOC	#Prop.	PAT(ms)	T.r.s(ms)
1. Chrome_Dino_Game	80	12	32.09	7.66
2. Cradle_with_Joystick	89	12	31.22	9.85
3. Small_Linear_Actuator	180	12	21.65	38.68
4. Large_Linear_Actuator	155	12	17.41	14.66
5. Train_Detect	78	12	19.50	17.35
6. Motor_Control	216	15	22.89	4.71
7. Train_Demo_2	133	15	49.51	59.28
8. Fridge_Timer	292	15	17.05	9.11
9. Match_the_Light	143	15	23.34	49.65
10. Tank_Control	104	15	24.96	19.39
11. Control_a_Solenoid	120	18	36.26	19.85
12. IoT_Stepper_Motor	145	18	27.75	6.74
13. Aquariumatic_Manager	135	10	25.72	3.93
14. Auto_Train_Control	122	18	56.55	14.95
15. LED_Switch_Array	280	18	44.78	19.58
16. Washing_Machine	419	18	33.69	9.94
Total	2546	235	446.88	305.33

controlling
and 111 invalid)

Outline

1. DependentEffs : General Effectful Programs
- 2. SyncEffs: Reactive Systems
 - Synchronous program, logical correctness, causality
3. TimEffs: Time Critical Systems
4. ContEffs Algebraic Effects and Handlers
5. Conclusion and the Future Work

Esterel – A synchronous language

- System-design/modelling language. [Berry G, Gonthier G. 1992]
[Jagadeesan L J, Puchol C, Von Olnhausen J E. 1995]
[Florence, Spencer P., et al. 2019]
- Deterministic semantics.
- Primitive constructs execute in zero time except for the pause statement.
- The (i) correctness and (ii) safety issues are particularly critical.

```
1 signal S1 {  
2   present S1  
3   then nothing  
4   else emit S1  
5 }
```

(a) No valid assignments
(Logically incorrect).

```
1 signal S1 {  
2   present S1  
3   then emit S1  
4   else nothing  
5 }
```

(b) Two possible assignments
(Logically incorrect).

```
1 /*@ requires S1 @*/  
2 /*@ ensures S1 @*/  
3 present S1  
4 then emit S1  
5 else nothing
```

(c) One assignment under the
precondition (Logically correct).

Target Language $\lambda^{a/s}$, extending Esterel with synchronous constructs

(Program)	$\mathcal{P} ::= \xrightarrow{\text{module}}$
(Basic Types)	$\tau ::= IN \mid OUT \mid INOUT$
(Module Def.)	$\text{module} ::= nm \ (\overrightarrow{\tau} S) \ \langle \text{req } \Phi_{pre} \ \text{ens } \Phi_{post} \rangle \ p$
(Statement)	$p, q ::= \text{nothing} \mid \underline{\text{pause}} \mid \text{emit } S \mid p; q \mid p \parallel q \mid \text{loop } p$ $\mid \text{signal } S \text{ in } p \mid \text{present } S \text{ then } p \text{ else } q \mid \text{call } mn (\overrightarrow{S})$ $\mid \text{try } p \text{ with } q \mid \text{raise } d \mid \text{async } S \ p \ q \mid \text{await } S$
<hr/>	
(Signal Variables)	$S \in \Sigma$
	$x, mn \in \mathbf{var}$
(Depth)	
$d \in \mathbb{N} \cup \{0\}$	

Specification Language SyncEfs:

(Effects)	$\Phi ::= \perp \mid \epsilon \mid I \mid \underline{S?} \mid \Phi_1 \cdot \Phi_2 \mid \Phi_1 \vee \Phi_2 \mid \underline{\Phi_1 \parallel \Phi_2} \mid \Phi^*$
(event)	$I ::= \{\} \mid \{S \mapsto \alpha\} \mid I_1 \cup I_2$
(Signal Status)	$\alpha ::= \text{present} \mid \text{absent} \mid \text{undef}$
(Signal Variables)	$S \in \Sigma$
	(Blocking Waiting) ?
(Kleene Star) *	

Logically incorrect examples, caught by SyncEffs.

1. *present* S_1
 $\langle\{S_1 \mapsto \text{undef}\}\rangle$

2. *then*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{present}\}\rangle$

3. *nothing*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{present}\}\rangle$

4. *else*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{absent}\}\rangle$

5. *emit* S_1
 $\langle\{S_1 \mapsto \text{present}, S_1 \mapsto \text{absent}\}\rangle$

6. $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{present}\}$
 $\vee \{S_1 \mapsto \text{present}, S_1 \mapsto \text{absent}\}\rangle$
 $\langle\perp \vee \perp\rangle \Rightarrow \langle\perp\rangle$
(a)

1. *present* S_1
 $\langle\{S_1 \mapsto \text{undef}\}\rangle$

2. *then*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{present}\}\rangle$

3. *emit* S_1
 $\langle\{S_1 \mapsto \text{present}, S_1 \mapsto \text{present}\}\rangle$

4. *else*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{absent}\}\rangle$

5. *nothing*
 $\langle\{S_1 \mapsto \text{undef}, S_1 \mapsto \text{absent}\}\rangle$

6. $\langle\{S_1 \mapsto \text{present}, S_1 \mapsto \text{present}\}$
 $\vee \{S_1 \mapsto \text{undef}, S_1 \mapsto \text{absent}\}\rangle$
 $\langle\{S_1 \mapsto \text{present}\} \vee \{S_1 \mapsto \text{absent}\}\rangle$
(b)

```
1 module a_bug:
2   output S;
3   /*@
4   require {}
5   ensure {S}
6   @*/
7   signal S in
8     present S then emit S
9   else emit S
10  end present end signal
11 end module
```

Constructiveness

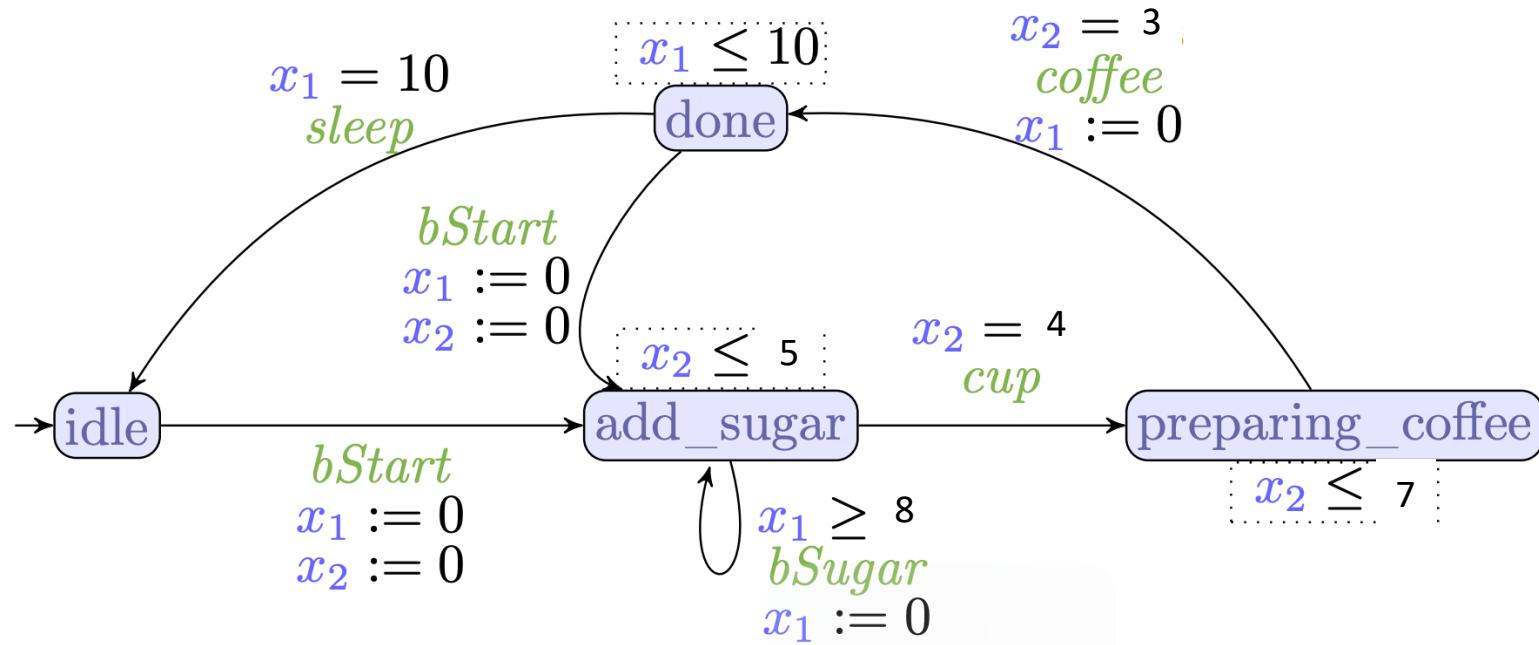
the status of the tested signal
must be determined before
executing the sub-expressions.

Outline

1. DependentEffs : General Effectful Programs
2. SyncEffs: Synchronous Programming
3. TimEffs: Time Critical Systems
 - mutable variables and concurrency
 - timed behavioural patterns, such as delay, timeout, interrupt, deadline, etc.
4. ContEffs Algebraic Effects and Handlers
5. Conclusion and the Future Work

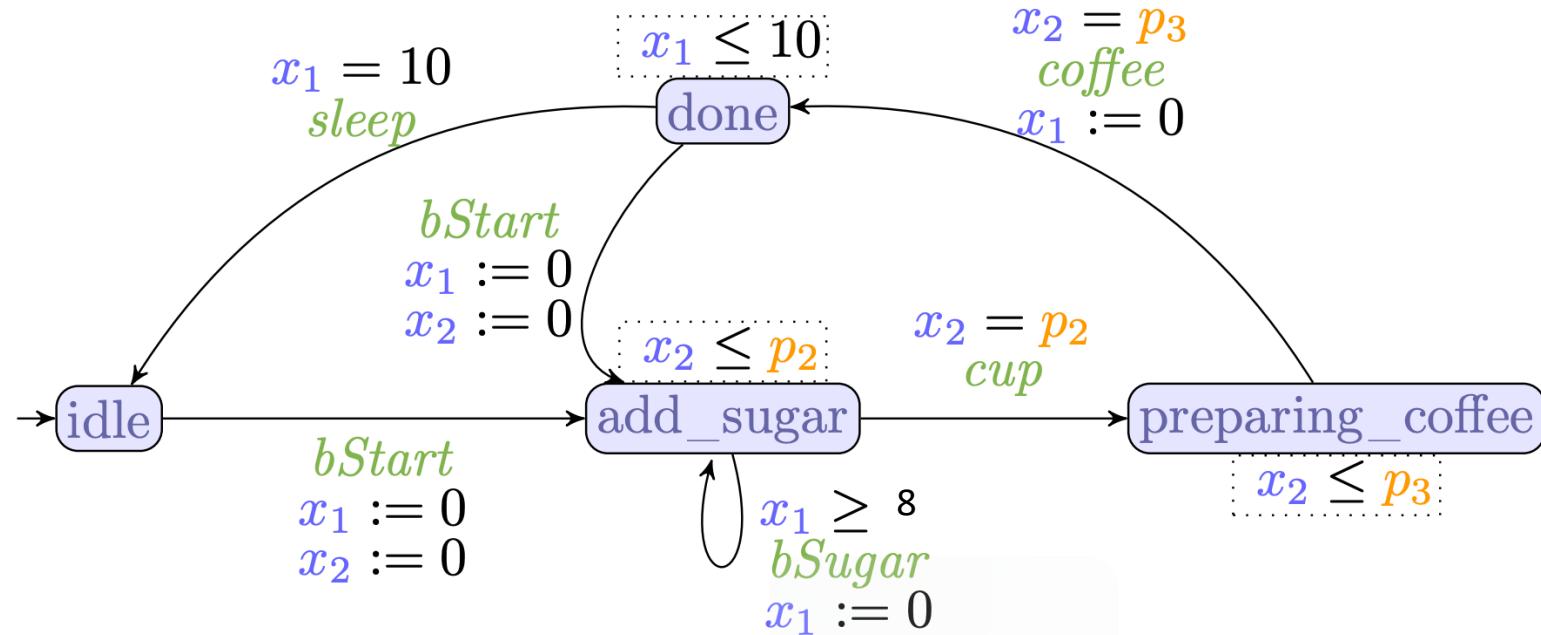
Timed Verification via Timed Automata

- Timed Automata lack high-level compositional patterns for hierarchical design.
- Manually casting clocks is tedious and error-prone.
- Timed CSP, is translated to Timed Automata (TA) so that the model checker Uppaal can be applied.



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- Timed Automata lack high-level compositional patterns for hierarchical design.
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- Timed CSP, is translated to Timed Automata (TA) so that the model checker Uppaal can be applied.



We propose TimEffs - Symbolic Timed Automata

```
1 void addOneSugar()
2 /* req: true ∧ _
3    ens: t>1 ∧ ε # t */
4 { timeout ((), 1); }
```

Target Language C^t, imperative with timed constructs:

(Expressions) $e ::= v \mid \alpha \mid [v]e \mid mn(v^*) \mid e_1; e_2 \mid e_1 || e_2 \mid \text{if } v \ e_1 \ e_2 \mid \text{event}[\mathbf{A}(v, \alpha^*)]$
 $\mid \text{delay}[v] \mid e_1 \ \text{timeout}[v] \ e_2 \mid e \ \text{deadline}[v] \mid e_1 \ \text{interrupt}[v] \ e_2$

$c \in \mathbb{Z}$

$b \in \mathbb{B}$

$mn, x \in \mathbf{var}$

(Action labels) $\mathbf{A} \in \Sigma$

Specification Language TimEffs:

(Timed Effects) $\Phi ::= \pi \wedge \theta \mid \Phi_1 \vee \Phi_2$

(Event Sequences) $\theta ::= \perp \mid \epsilon \mid ev \mid \theta_1 \cdot \theta_2 \mid \theta_1 \vee \theta_2 \mid \theta_1 || \theta_2 \mid \pi? \theta \mid \theta\#t \mid \theta^*$

(Pure) $\pi ::= True \mid False \mid bop(t_1, t_2) \mid \pi_1 \wedge \pi_2 \mid \pi_1 \vee \pi_2 \mid \neg \pi \mid \pi_1 \Rightarrow \pi_2$

(Real-Time Terms) $t ::= c \mid x \mid t_1 + t_2 \mid t_1 - t_2$

$c \in \mathbb{Z}$

$x \in \mathbf{var}$

(Real Time Bound) $\#$

(Kleene Star) \star

Inclusion Checking – SMT based Term Rewriting

```
7 void addNSugar (int n)
8 /* req: true ∧ ...
9  ens: t ≥ n ∧ EndSugar # t */
10 { if (n == 0) { event ["EndSugar"];}
11 else {
12     addOneSugar();
13     addNSugar (n-1);}}
```

$$(n=0 \wedge ES) \vee (n \neq 0 \wedge t2 > 1 \wedge (\epsilon \# t2) \cdot \Phi_{post}^{addNSugar(n-1)}) \sqsubseteq \Phi_{post}^{addNSugar(n)}$$

Inclusion Checking – SMT based Term Rewriting

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7 void addNSugar (int n)
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11 else {
12     addOneSugar();
13     addNSugar (n-1);}}
```

$$\begin{array}{l} \text{n=0} \wedge \epsilon \sqsubseteq tR \geq 0 \wedge \epsilon \# tR \quad \textcircled{4} [PROVE] \\ \text{n=0} \wedge ES \sqsubseteq tR \geq 0 \wedge ES \# tR \quad \textcircled{3} [UNFOLD] \end{array}$$

$$(n=0 \wedge ES) \vee$$

$$(n=0 \wedge ES) \vee (n \neq 0 \wedge t2 > 1 \wedge (\epsilon \# t2) \cdot \Phi_{post}^{addNSugar(n-1)} \sqsubseteq \Phi_{post}^{addNSugar(n)})$$

Inclusion Checking – SMT based Term Rewriting

```

7 void addNSugar (int n)
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10 { if (n == 0) { event ["EndSugar"];}
11   else {
12     addOneSugar();
13     addNSugar (n-1);}}
```

(I)

$$\vee (n \neq 0 \wedge t_2 > 1 \wedge t_L \geq (n-1) \wedge \epsilon \# t_2 \cdot ES \# t_L) \sqsubseteq t_R \geq n \wedge ES \# t_R$$

② [LHS-OR]

① [RENAME]

$$(n = 0 \wedge ES) \vee (n \neq 0 \wedge t_2 > 1 \wedge (\epsilon \# t_2) \cdot \Phi_{post}^{addNSugar(n-1)}) \sqsubseteq \Phi_{post}^{addNSugar(n)}$$

(I)

$$t_2 > 1 \wedge t_L \geq (n-1) \wedge t_L = (t_R - t_2) \Rightarrow t_R \geq n$$

⑦ [PROVE]

$$n \neq 0 \wedge t_2 > 1 \wedge t_L \geq (n-1) \wedge \epsilon \sqsubseteq t_R \geq n \wedge \epsilon$$

$$n \neq 0 \wedge t_2 > 1 \wedge t_L \geq (n-1) \wedge ES \# t_L \sqsubseteq t_R \geq n \wedge ES \# (t_R - t_2)$$

⑥ [UNFOLD] $\pi_u : t_L = (t_R - t_2)$

$$n \neq 0 \wedge t_2 > 1 \wedge t_L \geq (n-1) \wedge \epsilon \# t_2 \cdot ES \# t_L \sqsubseteq t_R \geq n \wedge ES \# t_R$$

⑤ [UNFOLD]

Inclusion Checking – SMT based Term Rewriting

```

7 void addNSugar (int n)
8 /* req: true ∧ _ */
9   ens: t ≥ n ∧ EndSugar # t */
10 { if (n == 0) { event ["EndSugar"];}
11   else {
12     addOneSugar();
13     addNSugar (n-1);}

```

Succeed!

$$n=0 \wedge \epsilon \sqsubseteq tR \geq 0 \wedge \epsilon \# tR \quad \text{④ [PROVE]}$$

$$n=0 \wedge ES \sqsubseteq tR \geq 0 \wedge ES \# tR \quad \text{③ [UNFOLD]}$$

(I)

$$(n=0 \wedge ES) \vee (n \neq 0 \wedge t2 > 1 \wedge tL \geq (n-1) \wedge \epsilon \# t2 \cdot ES \# tL) \sqsubseteq tR \geq n \wedge ES \# tR \quad \text{② [LHS-OR]}$$

① [RENAME]

$$(n=0 \wedge ES) \vee (n \neq 0 \wedge t2 > 1 \wedge (\epsilon \# t2) \cdot \Phi_{post}^{addNSugar(n-1)}) \sqsubseteq \Phi_{post}^{addNSugar(n)}$$

(I)

$$t2 > 1 \wedge tL \geq (n-1) \wedge tL = (tR - t2) \Rightarrow tR \geq n$$

⑦ [PROVE]

$$n \neq 0 \wedge t2 > 1 \wedge tL \geq (n-1) \wedge \epsilon \sqsubseteq tR \geq n \wedge \epsilon$$

⑥ [UNFOLD] $\pi_u : tL = (tR - t2)$

$$n \neq 0 \wedge t2 > 1 \wedge tL \geq (n-1) \wedge ES \# tL \sqsubseteq tR \geq n \wedge \cancel{ES \# (tR - t2)}$$

⑤ [UNFOLD]

$$n \neq 0 \wedge t2 > 1 \wedge tL \geq (n-1) \wedge \epsilon \# t2 \cdot ES \# tL \sqsubseteq tR \geq n \wedge ES \# tR$$

Antimirov algorithm for solving REs' inclusions

Definition 1 (Derivatives). Given any formal language S over an alphabet Σ and any string $u \in \Sigma^*$, the derivatives of S w.r.t u is defined as: $u^{-1}S = \{w \in \Sigma^* \mid uw \in S\}$.

Definition 2 (Regular Expression Inclusion). For REs r and s ,

$$r \preceq s \Leftrightarrow \boxed{\forall A \in \Sigma. A^{-1}(r) \preceq A^{-1}(s)}.$$

Antimirov algorithm for solving TimEffs' inclusions

Definition 3 (TimEffs Inclusion). For TimEffs Φ_1 and Φ_2 ,

$$\Phi_1 \sqsubseteq \Phi_2 \Leftrightarrow \boxed{\forall A \in \Sigma. \forall t \geq 0. (A \# t)^{-1} \Phi_1 \sqsubseteq (A \# t)^{-1} \Phi_2}.$$

Implementation and Evaluation

Table 5.3: Experimental Results for Manually Constructed Synthetic Examples.

No.	LOC	Forward(ms)	#Prop(✓)	Avg-Prove(ms)	#Prop(X)	Avg-Dis(ms)	#AskZ3
1	26	0.006	5	52.379	5	21.31	77
2	37	43.955	5	83.374	5	52.165	188
3	44	32.654	5	52.524	5	33.444	104
4	72	202.181	5	82.922	5	55.971	229
5	98	42.706	7	149.345	7	60.325	396
6	134	403.617	7	160.932	7	292.304	940
7	133	51.492	7	17.901	7	47.643	118
8	173	57.114	7	40.772	7	30.977	128
9	182	872.995	9	252.123	9	113.838	1142
10	210	546.222	9	146.341	9	57.832	570
11	240	643.133	9	146.268	9	69.245	608
12	260	1032.31	9	242.699	9	123.054	928
13	265	12558.05	11	150.999	11	117.288	2465
14	286	12257.834	11	501.994	11	257.800	3090
15	287	1383.034	11	546.064	11	407.952	1489
16	337	49873.835	11	1863.901	11	954.996	15505

Main Observations:

the disproving times for invalid properties are constantly lower than the proving process.

Evaluation – Fischer's Mutual Exclusion Algorithm

Table 5.

#Proc

2

3

4

5

```
1 var x := -1;
2 var cs:= 0;
3
4 void proc (int i) {
5     [x=-1] //block waiting until true
6     deadline(event["Update"](i)){x:=i},d);
7     delay (e);
8     if (x==i) {
9         event["Critical"](i){cs:=cs+1};
10        event["Exit"](i){cs:=cs-1;x:=-1};
11        proc (i);
12    } else {proc (i);}
13
14 void main ()
15 /* req: d<e ∧ ε
16    ensa:true ∧ (cs≤1)*   ensb:true ∧ ((*)_Critical.Exit._*)* */
17 { proc(0) || proc(1) || proc(2); }
```

Evaluation – Fischer’s Mutual Exclusion Algorithm

Table 5.4: Comparison with PAT via verifying Fischer’s mutual exclusion algorithm

#Proc	Prove(s)	#AskZ3-u	Disprove(s)	#AskZ3-u	PAT(s)	Uppaal(s)
2	0.09	31	0.110	37	≤ 0.05	≤ 0.09
3	0.21	35	0.093	42	≤ 0.05	≤ 0.09
4	0.46	63	0.120	47	0.05	0.09
5	25.0	84	0.128	52	0.15	0.19

Observations:

- i. automata-based model checkers (both PAT and Uppaal) are vastly efficient when given concrete values for constants d and e;
- ii. our proposal can symbolically prove the algorithm by only providing the constraints, of d and e.
- iii. our verification time largely depends on the number of querying Z3.

Outline

1. DependentEffs : General Effectful Programs
2. SyncEffs: Synchronous Programming
3. TimEffs: Time Critical Systems
- 4. ContEffs Algebraic Effects and Handlers
 - The coexistence of zero-shot, one-shot and multi-shot continuations
 - Non-terminating behaviours.
5. Conclusion and the Future Work

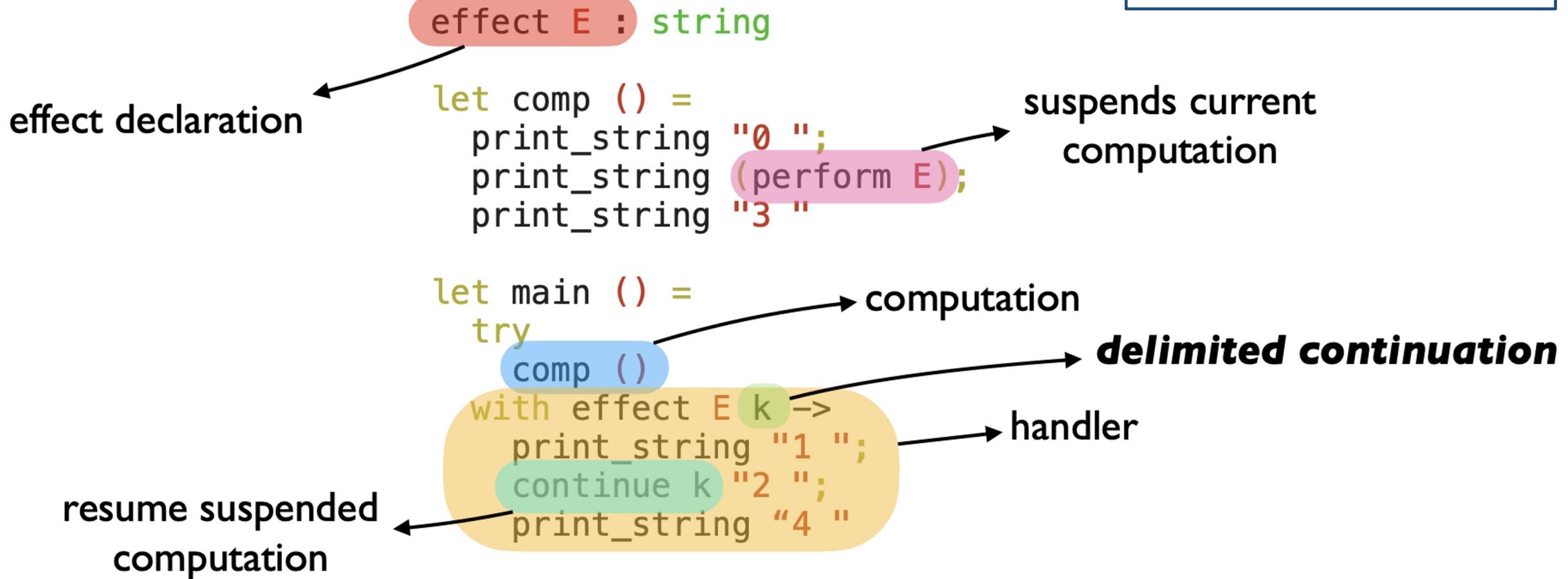
User-defined Effects and Handlers

```
effect E : string
let comp () =
  print_string "0 ";
  print_string (perform E);
  print_string "3 "
let main () =
  try
    comp ()
  with effect E k ->
    print_string "1 ";
    continue k "2 ";
    print_string "4 "
```

[de Vilhena, Paulo Emílio, and François Pottier. 2021]

[Sivaramakrishnan, K. C., et al. 2021]

User-defined Effects and Handlers



Core Language λh : pure, higher-order, call by value

(Values)

$$v ::= c \mid x \mid \lambda x \Rightarrow e$$

(Expressions)

$$e ::= v \mid v_1 v_2 \mid \text{let } x=v \text{ in } e \mid \text{if } v \text{ then } e_1 \text{ else } e_2 \mid \underline{\text{perform } A(v, \lambda x \Rightarrow e) \mid \text{match } e \text{ with } h \mid \text{resume } v}$$

Specification Language ContEffs

$$(ContEffs) \quad \Phi ::= \bigvee(\pi, \theta, v)$$

$$(Parameterized Label) \quad \textcolor{brown}{l} ::= \Sigma(v)$$

$$(Event Sequences) \quad \theta ::= \perp \mid \epsilon \mid ev \mid Q \mid \theta_1 \cdot \theta_2 \mid \theta_1 \vee \theta_2 \mid \theta^* \mid \theta^\infty \mid \theta^\omega$$

$$(Single Events) \quad ev ::= - \mid \textcolor{brown}{l} \mid \bar{l}$$

$$(Placeholders) \quad Q ::= \underline{\textcolor{brown}{l}! \mid l?(v)}$$

Examples – Zero-shot continuations (Exceptions)

```
1  effect Exc : (unit -> unit)
2  effect Other : (unit -> unit)
```

```
3
4  let f ()
5  (*@ req emp @*)
6  (*@ ens Exc!.Other!.Other?().Exc?() @*)
7  = let x = perform Exc in
8    let y = perform Other in
9    y ();
10   x ()
```

Step	History	Current Event	Continuation	Bindings
1	emp	Exc!	Other! · Other?() · Exc?() · ‡	‡ = (fun x -> x)
2	Exc	-	-	No “Continue”
Final	Exc	-	-	

```
11
12 let handler
13 (*@ req emp @*)
14 (*@ ens Exc @*)
15 = match f () with
16 | x -> x
17 | effect Exc k -> ()
```

Examples – Multi-shot continuation

```
1  effect Foo : (unit -> int)
2  effect Goo : (unit -> int)
3  effect Done: (unit)

4

5  let f ()
6  (*@ req emp @*)
7  (*@ ens Foo!.Goo!.Goo?().Foo?() @*)
8  = let x = perform Foo in
9    let y = perform Goo in
10   y (); x ()

11

12 let handler
13 (*@ req emp @*)
14 (*@ ens Foo.Goo.Done!.
15   Goo.Done! @*)
16 = match f () with
17 | x -> perform Done;
18 | effect Foo k -> continue k (fun () -> ());
19           continue k (fun () -> ())
20 | effect Goo k -> continue k (fun () -> ())
```

Implementation and Evaluation

- Core implementation: 2500 LOC in OCaml, on top of Multicore OCaml (4.12.0)
- Validation: manually annotated synthetic test cases marked with expected outputs

No.	LOC	Infer(ms)	#Prop(✓)	Avg-Prove(ms)	#Prop(X)	Avg-Dis(ms)
1	32	14.128	5	7.7786	5	6.2852
2	48	14.307	5	7.969	5	6.5982
3	71	15.029	5	7.922	5	6.4344
4	98	14.889	5	18.457	5	7.9562
5	156	14.677	7	10.080	7	4.819
6	197	15.471	7	8.3127	7	6.8101
7	240	18.798	7	18.559	7	7.468
8	285	20.406	7	23.3934	7	9.9086
9	343	26.514	9	16.5666	9	13.9667
10	401	26.893	9	18.3899	9	10.2169
11	583	49.931	14	17.203	15	10.4443
12	808	75.707	25	21.6795	24	16.9064

Summary & Links

- New framework for temporal verification.
 - ❖ More modular – a compositional verification strategy.
 - ❖ Finer-grained – semantics oriented, forward verifiers.
 - ❖ More efficient – term rewriting systems.
- Implementations upon possible application scenes and evaluations.
 - ❖ General Effectful Programs (ICFEM 2020) [[PDF](#)] [[Video](#)] [[Code](#)]
 - ❖ Reactive Systems (VMCAI 2021) [[PDF](#)] [[Video](#)] [[Code1&Code2](#)]
 - ❖ Time Critical Systems (TACAS 2023) [[PDF](#)] [[Code](#)]
 - ❖ Algebraic Effects and Handlers (APLAS 2022) [[PDF](#)] [[Code](#)]

Possible Future Work

- Symbolic verification for probabilistic programming
- Temporal verification for hyper-properties (hyper temporal logic)
- Practical analysis for mixed synchronous and asynchronous features
- Trace-based verification with spatial information
 - ❖ Ongoing work: “Extending Separation Logic for Unrestricted Effect Handlers”
- Temporal verification with incorrectness logic
- Program-analyzer based repair
 - ❖ Ongoing work: “Automated Program Repair guided by Temporal Properties”

Thank you for
your attention!

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Thesis Revision Plan

On the comments of Examiner 1

1. Add more details of the similarities to the types-and-effects system;
2. Enrich the introductory with background material, such as the detailed comparison between automata-based and the RE-based entailment proving;
3. Emphasize the novel departure (for each of the separated works) from the original Antimirov algorithm;
4. Expand the discussions of various experiments, and the results will be summarized rigorously against the adversaries or baselines.

Thesis Revision Plan

On the comments of Examiner 2

1. In Chapters 3 ~ 6, move the examples to later sections after technical definitions;
2. Move the essence proofs to the main text and leave the simple ones as lemmas;
3. Add Rules for precondition strengthening and postcondition weakening;
4. Gather the forward rules into a figure in each of the chapters;
5. In tables 4.4, 5.3, and 6.1, compare results with existing methods, or justify why no comparison is made (e.g., no similar tools exist).