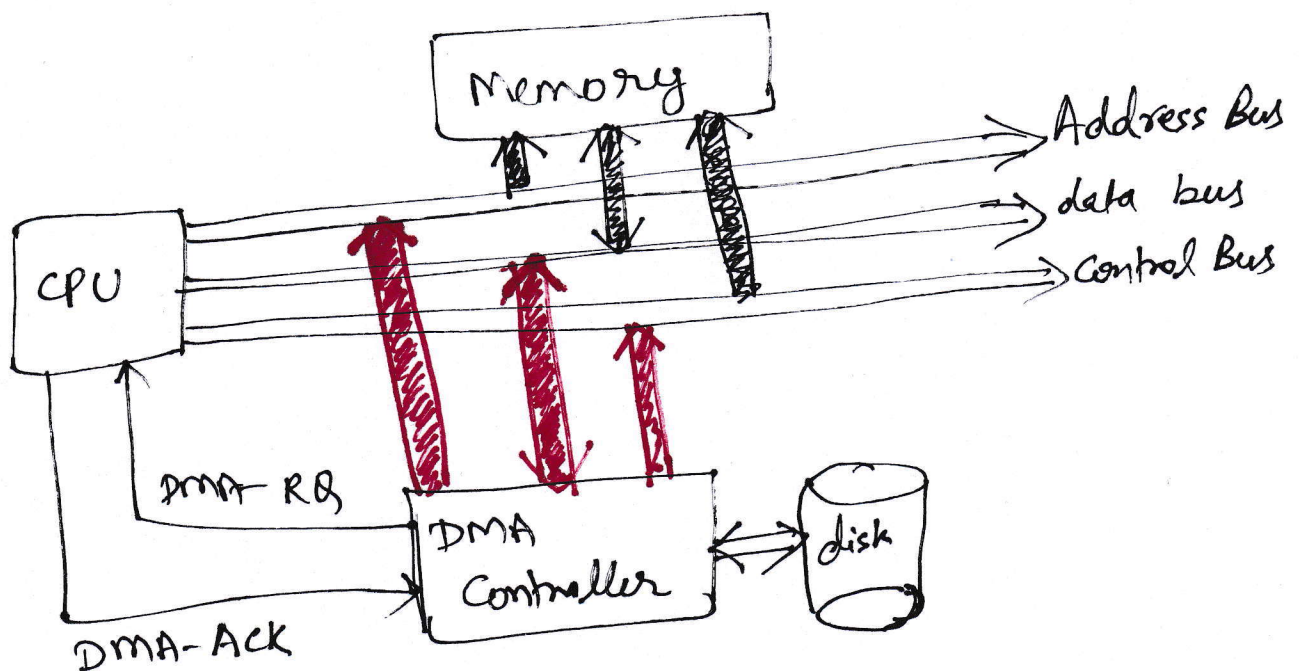


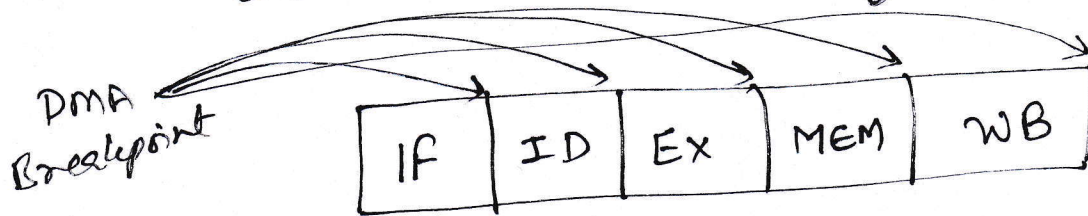
DMA Controller

- Enable direct data transfer b/w I/O devices & memory w/o CPU intervention
- No need to execute 'instruction'
- Much faster than programmed I/O



DMA Breakpoints:

- DMA request can be acknowledged at the end of any m/c cycle



→ For every DMA channel, the DMA controller will have three registers

- (a) memory address
- (b) Word count
- (c) Address of data on disk

