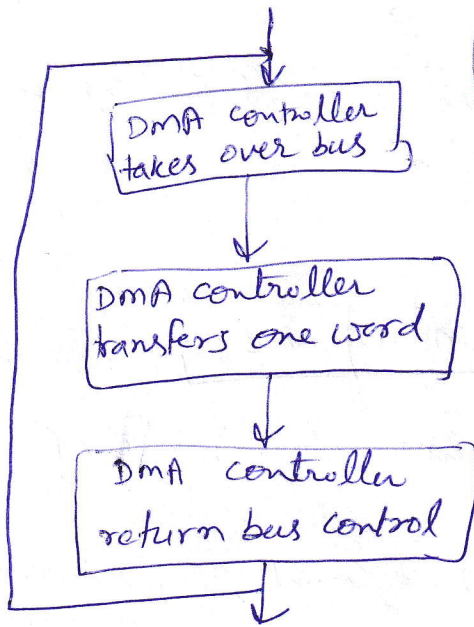
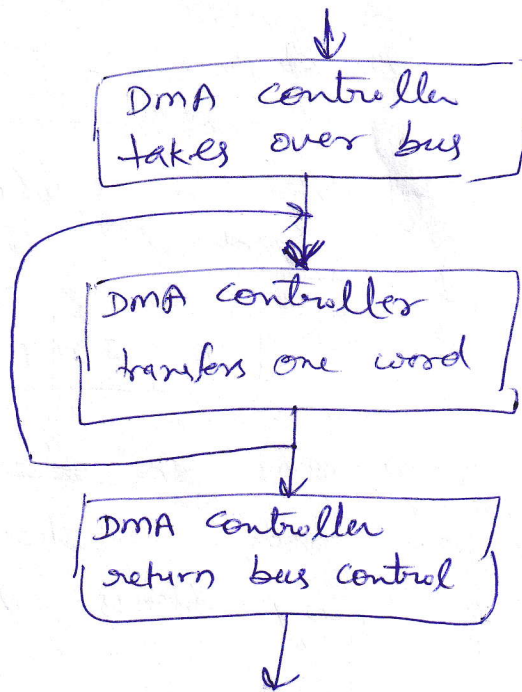


cycle
stealing
mode



Block
Transfer
Mode



Other Applications of DMA

- Other than data transfer to/from high-speed peripheral devices, DMA can be used in some other areas as well;
- High-speed memory-to-memory block move
 - Refreshing dynamic memory systems, by periodically generating dummy read requests to the columns.

Some Examples on I/O transfer (1)

Q.1. Suppose we want to read 2048 bytes in programmed I/O mode of transfer. The bus width is 32 bits. Each time an interrupt occurs, it takes 4 μ sec to service it (i.e., transfer 32 bits). How much CPU time is required to read 2048 bytes?

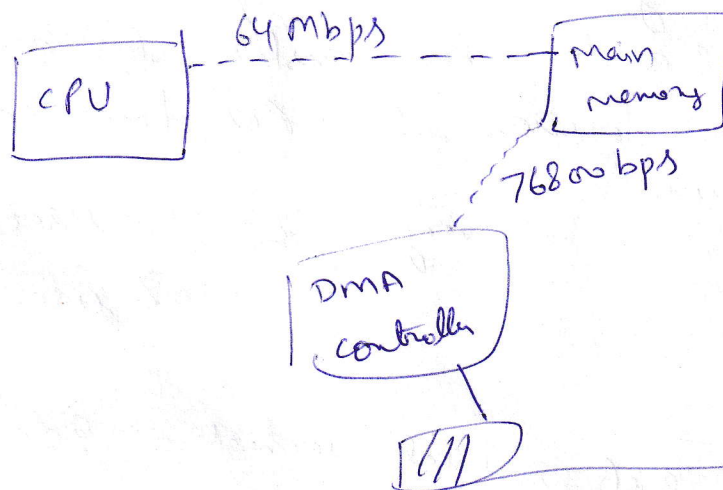
Ans.

Transfer 4 bytes in 4 μ sec

1 " " $\frac{4}{4} 1 \mu$ sec

$$\therefore 2048 \text{ ————— } \frac{4}{4} \times 2048 \mu\text{sec} \\ = 2048 \text{ msec}$$

Ques.2. A DMA module is transferring bytes to main memory from an external device at 76800 bps. The CPU can fetch instructions at a rate of 2 million instructions per second. Assume instruction size is 32 bits. How much will the processor be slowed down due to DMA activity?



$$\frac{2M}{2 \times 10^6} \times 32 \text{ bits} \\ = 64 \text{ Mbps}$$

In one second
CPU: 64×10^6 bits
DMA: 76800 bits

$$\text{slowdown} = \frac{76800}{64 \times 10^6} \times 100 \% \quad \text{Ans.}$$

(0.12%)

Ques³ A DMA controller transfers 32-bits word to memory using cycle stealing. The words are assembled from a device that transmits bytes at a rate of 2400 bytes per second. The CPU is fetching and executing instructions at an average rate of 1 million instructions per second. By how much time will the CPU be slowed down because of the DMA transfer?

In one second

CPU : 1 M words

$$\text{DMA C} : \frac{2400}{4} = 600 \text{ words}$$

$$\text{slowdown} : \frac{600}{1M} \times 100 \% = 0.06\%$$

Ques⁴ Consider a system employing interrupt-driven I/O for a device that transfers data at 8 KB/s on a continuous basis. The interrupt processing takes about 100 μsec and the I/O device interrupts the CPU for every byte.

While executing the ISR, the processor takes about 8 μsec for the transfer of each byte. What is the fraction of CPU time consumed by the I/O device?

$$\begin{aligned} \text{Overhead for every byte} &= 100 \mu\text{sec} + 8 \mu\text{sec} \\ &= 108 \mu\text{sec} \end{aligned}$$

1 second

$$\begin{aligned} 8 \text{ KB} \Rightarrow \text{Total overhead} &= 8000 \times 108 \mu\text{sec} \\ &= 8 \text{ ---} \\ &= 800 \text{ msec} = 0.8 \end{aligned}$$