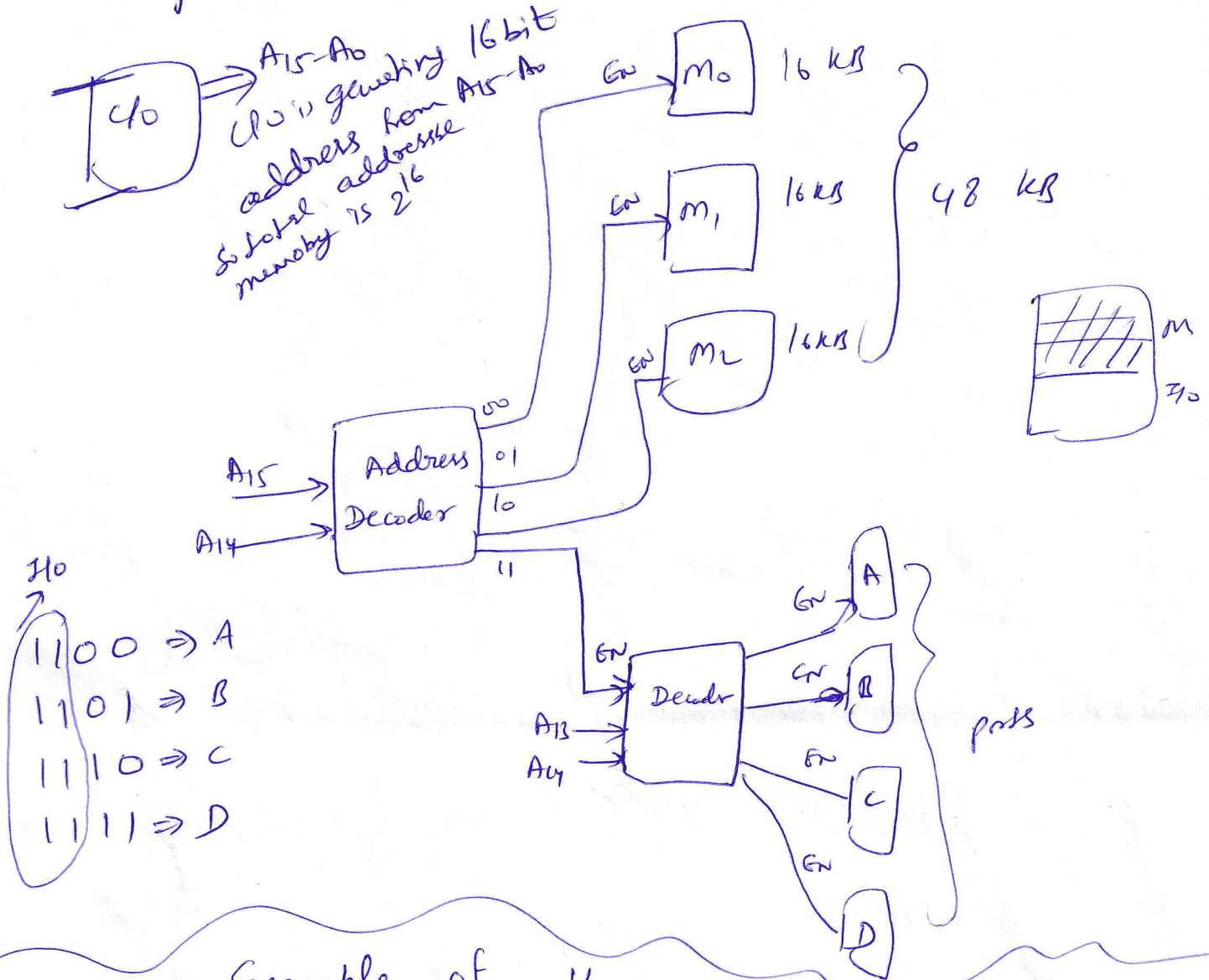


Example of memory mapped Device Interfacing



Example of I/O mapped device interfacing

