CS311 Lab Report
Assignment 5 - Discrete Event Simulator

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1 Introduction

We upgrade the pipelined processor model to a discrete event simulator model.

2 Commands

In order to simulate the processor, we run the commands in order:
```
ant
ant make-jar
java -jar <path_to_simulator.jar> <path_to_config_file> <path_to_output_file> <path_to_object_file>
```

3 Results

Object File	Number of Instructions	Number of Cycles	Instructions per Cycles
descending.out	276	15235	0.01811
evenorodd.out	5	252	0.01984
fibonacci.out	77	3890	0.01979
palindrome.out	48	2305	0.02082
prime.out	28	1400	0.02

Table 1: Performance Metrics

It is clear that the instructions per cycle remain somewhat the same for all programs.